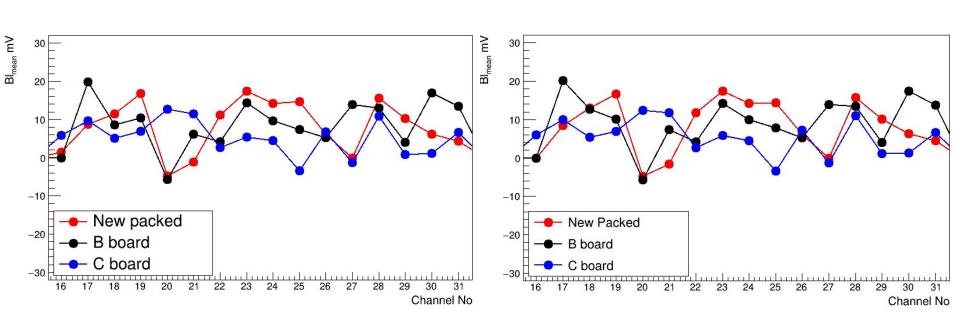
## Mean baseline position

(New packed asic & B board & C board)

Tp 15 ns

Tp 20 ns

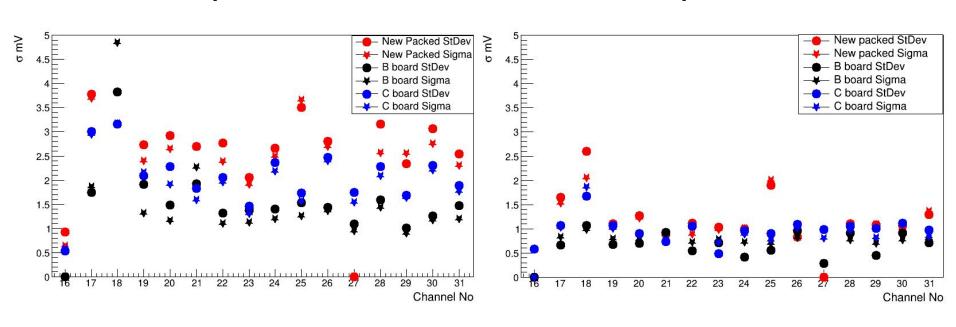


## Standard dev. & Sigma from gaussian

(New packed asic & B board & C board)



**Tp 20 ns** 



## Tests with various capacitances Standard Dev. (mV)

Boards (one FEB) connected to the same detector module

