

Neeraj Patil
BE
Electronics
Engineer

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Summary

Creative individual with a background comprising several projects and collaborative opportunities. An engaging personality with the ability to work efficiently on various tasks. Seeking the opportunity to utilize and expose my potential to the fullest and develop my skills to deliver consistent performance and enhance professional and personal growth with the right mix of ethics and values. A highly motivated individual committed to hard work and intensive analysis. Flexible and polished Student, promoting well-developed skills.

Education

College / University	Course	Duration	CGPA/Percentage
VESIT, Mumbai University	B.E.	1 August 2017-1 June 2021	9.14
D.G.Ruparel College	HSC	23 June 2015-31 May 2017	87.85
Sri Ma Bal Niketan	SSC	11 June 2009 -31 May 2015	92.40

Experience

LTIMindtree, Mumbai - Engineer cloud services and software || 29th June 2021 - present

Completed full stack training in Java angular and spring. Salesforce certified administrator, platform developer, and platform app builder. Worked on 4 projects, solved bugs, prepared documents and provided technical support to clients. Trained freshers as part of the training team.

FatCat Robotics Private Limited, Online - IOT intern || 18th Mar 2020 - 18thJul 2020

Made data acquisition, detected nearby intruders and hotspots on solar panels using image processing on Raspberry Pi, and built a real-time dashboard to display important parameters using React JS.

SmartBridge Educational Services Private Limited, Online - Machine Learning || 15th May 2020 - 14th Jun 2020

“Predicting life Expectancy using Machine Learning” using IBM Watson Studio and IBM cloud services.

Veermata Jijabai Technological Institute (VJTI), Mumbai - Research Intern || 3rd Dec 2019-2nd Jan 2020

Done programming in Python language, signal processing, performed fast Fourier transform, and implemented it successfully on Labjack T7 pro DAQ. Analysis was performed on Nvidia Jetson TX2

Tan Swa Technologies INC, Navi Mumbai - Engineering Intern || 5th Jun 2019 -29th Jun 2019

Understood the working of purge panels and customer requirements. Also performed testing, and assembled PLC Panels.

Vivekanand Education Society's Institute Of Technology, Mumbai - Research Intern || Dec 2018 -Jan 2019

Prepared Simulation-based results on Photonic crystal based early cancer detection using Comsol Multiphysics

Skills

● Python ● IOT ● SQL ● Java ● Angular ● React JS ● Leadership

Certifications

● Structuring Machine Learning Projects ● Neural Networks & Deep Learning ● Sequence Models ● Deep learning Onramp ● Machine learning Onramp ● Matlab Onramp ● Cryptography ● Robot operating system ● Salesforce certified Administrator ● Salesforce certified Platform developer 1 ● Salesforce certified Platform App builder

Projects

- IoT Based Weather Forecast System - Machine learning
- Audiobooks for Visually impaired - Website using React js did web scraping conversion of text to audio
- Design cost effective business solution with good use of technology for eco-friendly design of residential apartment buildings | | Intech Olympiad 2020 | | Water ,air, electricity prediction, security etc monitoring using IOT and artificial intelligence
- Communication system using light fidelity(LIFI)
- Newspaper app for printing news in a standard format | | Java GUI and connected database
- Chaotic jerk circuit | | Non linear dynamics implemetation of tan hyperbolic function
- Chrome extension for detecting malicious web page

Awards

Co-curricular

- Selected among top 20 teams in India for Covid-19 Samadhan | MHRD Mega Online challenge organized by MHRD's Innovation cell, AICTE, Forge and InnovationCuris.
 - Selected among the top 17 teams in Intech Olympiad 2020 organised by COEP, Pune.
 - Qualified for Semi-finale stage of IICDC (National Level Project Competition by Texas Instruments)
 - Published paper as an author at the 12th International Conference on Computing Communication and Networking Technologies (IEEE) on the title "IoT based smart lock with predictive maintenance" under the guidance of Mr. Abhay Kshirsagar
 - Published paper as an author at International Journal of Research and Analytical Reviews (IJRAR) on the title "Covid Saarthi" under the guidance of Mr. Abhay Kshirsagar
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Extra-curricular

- Second Runner-up in E-Hardware Hackathon organised by ISA VESIT
 - Member of Robotics team in "PRAXIS 18", Technical event VESIT
 - Conducted workshops for juniors in Tinkerer's Lab
 - Participated in Smart India Hackathon in 2018 and 2019.
 - Participated in national-level project Competition "Electrowiz 2019" organised by IETE DMCE.
 - Participated in Looking Beyond Syllabus 2019 Technical Project Competition organized by VESIT
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- Was a narrator of the drama team that won VesLit drama
- Selected in the top 50 in group discussion organized by Ecell VESIT