

AEC LAB REPORT-1

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Experiment-1:

RC Circuits and diode characterization

1. Know your equipment:

a) First we familiarize ourselves with bread board, power supply and multimeter.

b)

i. Now read the RC specifications of oscilloscope probes and channel on the equipment

Probe specifications:-

10:1 75MHz

1M ohm and 15pF

1:1

1M ohm and 100pF

ii. Plot Demo signal available on DSO in one of the channels and shape of signal =square wave,
amplitude=1.31v(peak to peak),
frequency=1000.2 Hz



iii. now we generate a signal and specifications are below:

Type of signal	Amp(Vin)	Frequency	Probe factor	OSC factor	V(OSC) (mv) (measured)	VOSC = (OSCfactor / Probefactor) x Vin.
sinusoidal	4v	10.002kHz	10	10	4.14	4
sinusoidal	4v	10.002kHz	10	5	2.07	2
sinusoidal	4v	10.002kHz	1	10	41.4	40
sinusoidal	4v	10.002kHz	1	5	20.7	20
sinusoidal	4v	10.002kHz	1	2	8.3	8

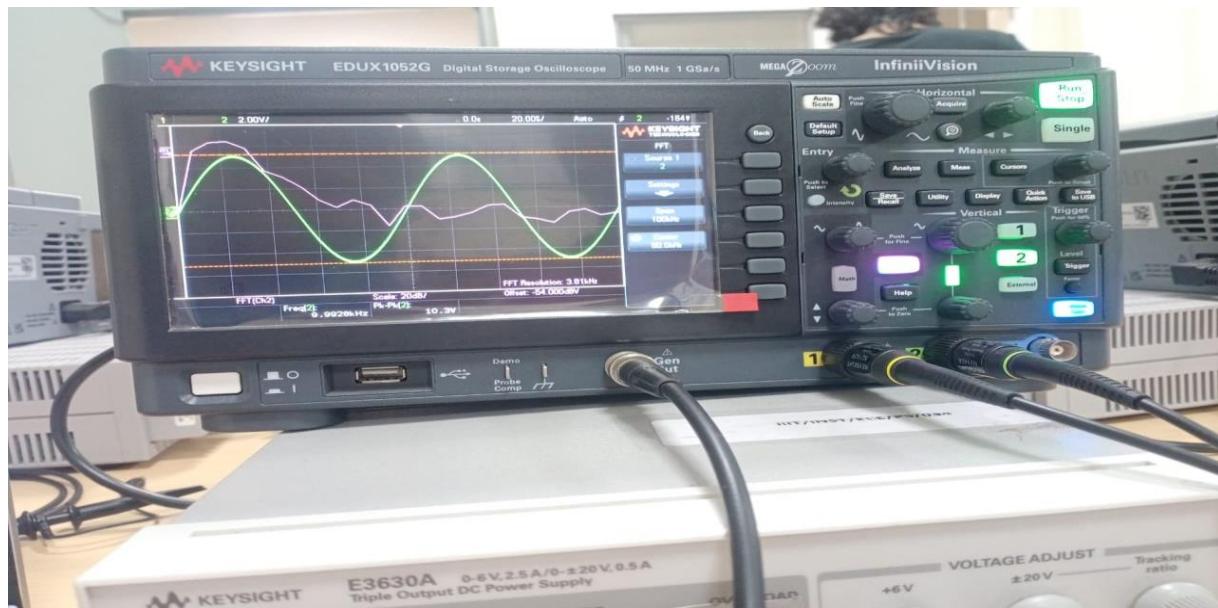
Then generate Sine wave of frequency 10 KHz, span=100kHz, centre=50KHz and observe the spectrum using FFT.

First Harmonic(x_1)=10kHz

Second Harmonic(x_2)=20kHz

Third Harmonic(x_3)=30kHz

We are getting frequencies in form of $f, 2f, 3f \dots$



iv. Generate Square wave of frequency=10kHz, span frequency=100kHz , centre frequency=50KHz and observe the Frequency Spectrum.

Take R=47ohm and C=47nf.

Then perform the same by taking input as square wave:

First Harmonic(x_1)=10kHz
 second Harmonic(x_3)=30.2kHz
 third Harmonic(x_5)=50kHz
 fourth Harmonic(x_5)=70kHz
 Fifth Harmonic(x_5)=89.6kHz
 sixth Harmonic(x_5)=110kHz
 seventh Harmonic(x_5)=129.4kHz

We are getting frequencies in form of $f, 3f, 5f, 7f, \dots$

Cut-off frequency ($f_c = 1/2\pi RC$) :-

$$\begin{aligned}
 f_c &= \frac{1}{2\pi RC} \\
 &= \frac{1}{2 \times 3.14 \times 47 \times 47 \times 10^{-9}} \\
 &= \frac{10^9}{2 \times 3.14 \times 47 \times 47} = \frac{10^9}{6.28 \times 2209} \\
 f_c &= 72.06 \text{ kHz}
 \end{aligned}$$

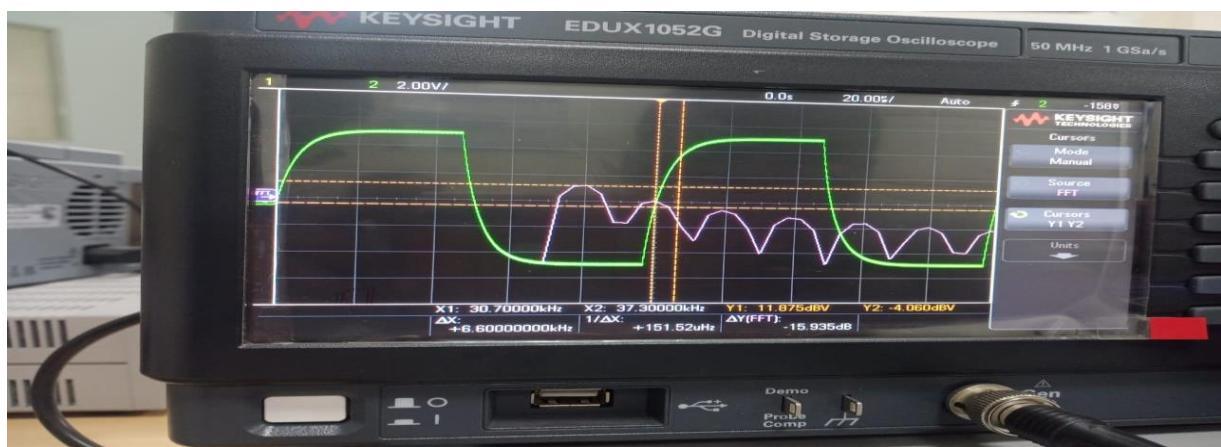
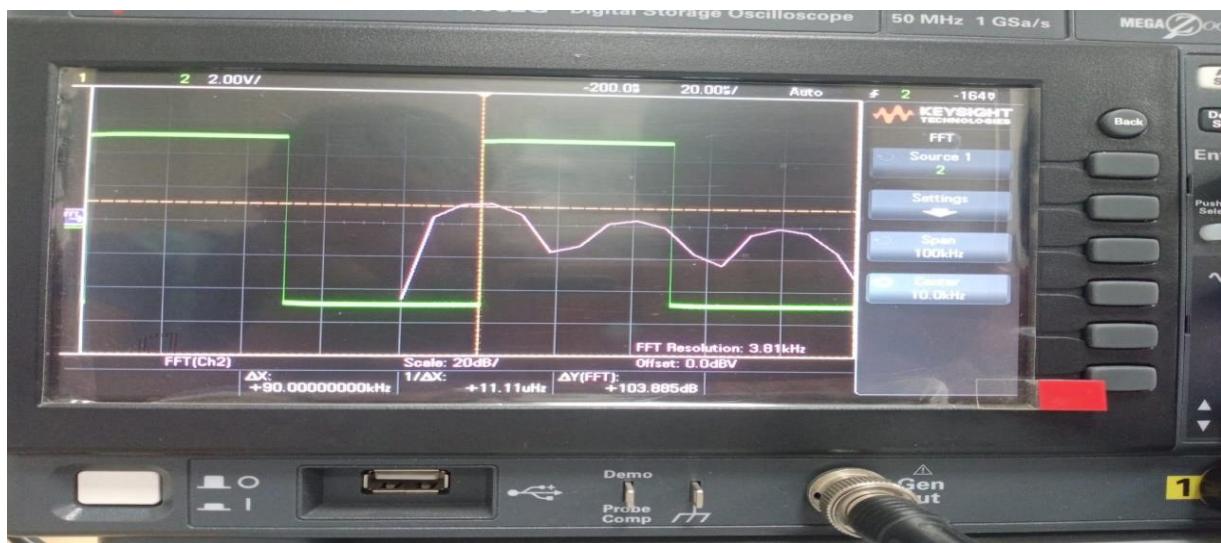
Measured with::

->Probe ratio:- 1:1

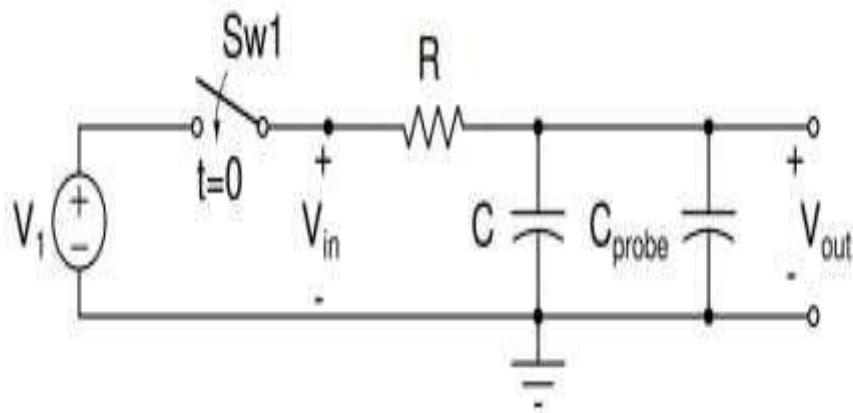
->Oscilloscope ratio:- 2:1

	1 st harmonic (strength / frequency)	2 nd harmonic (strength / frequency)	3rd harmonic (strength / frequency)	4th harmonic (strength / frequency)	5th harmonic (strength / frequency)
Without filter	13.125db	3.75db	-1.25db	4.375db	-6.25db
With filter	11.875db	1.25db	-5.00db	-11.25db	-15.625db

Therefore frequencies above cutoff frequency(72.06KHz) are filtered.



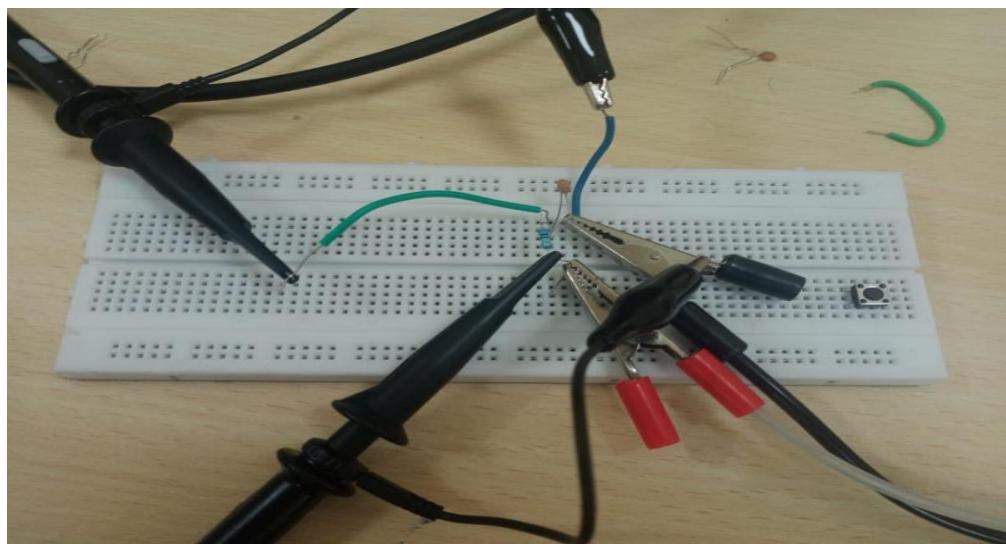
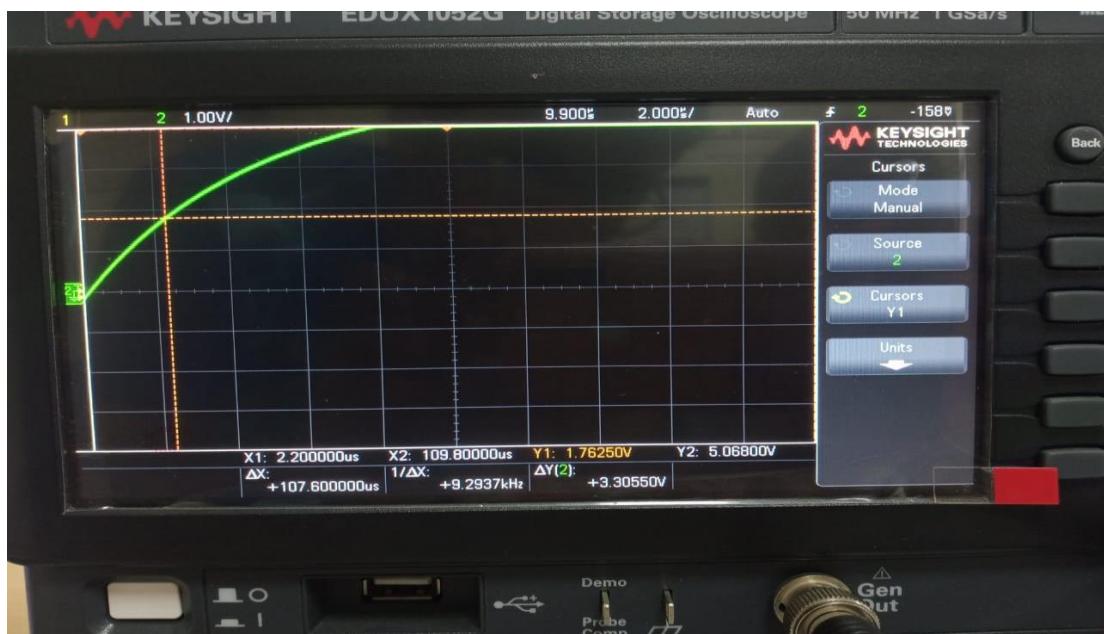
2. Estimate the effective probe-capacitance:



- >Take $R = 1\text{M}\Omega$ and $C = 10 \text{ pF}$.
- >Then plot input(square wave) and output using two channels of DSO and measure the time constant of the circuit (use probes/DSO in 10x mode).
- >To measure the time constant, we apply a step voltage (V_{in}) to the circuit and observe the output voltage (V_{out}) using a DSO. The time constant can be calculated by measuring the time taken for the output voltage to rise to 63.2% of its final value after applying the step voltage.
- >If the measured time constant is longer than the theoretical value, it indicates the presence of additional capacitance in the circuit, such as probe capacitance, wire capacitance, and breadboard capacitance.
- >The effective probe capacitance can be estimated using the following equation: $C_{probe} = (\tau_{meas} - \tau_{theo}) / R$

Verification of time constant (Measured and calculated):

Time constant $= 0.63 * V$





Cload	Probe factor	Calculated Time Constant (μs)	Measured Time Constant (μs)	Cprobe (pf)
10pf	1x	10 (μs)	124 (μs)	114
27pf	1x	27 (μs)	119 (μs)	109
100pf	1x	100 (μs)	133.72	123.7
10pf	10x	10 (μs)	42 (μs)	27
27pf	10x	27 (μs)	23.6 (μs)	8.6
100pf	10x	100 (μs)	37.8(μs)	21.8

->From the table, we can see that the measured time constants are slightly longer than the calculated time constants for all cases.

This indicates the presence of additional capacitance in the circuit due to the probe, wires, and breadboard.

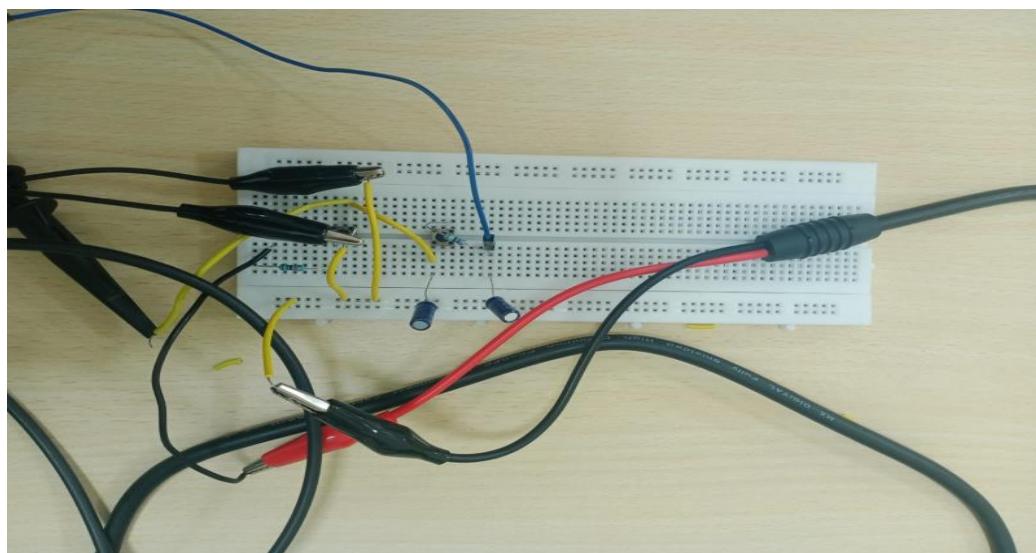
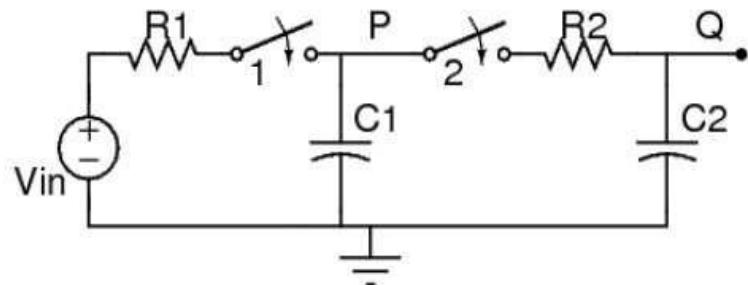
The estimated effective probe capacitance (Cprobe) is higher for higher values of Cload and for higher probe factors.

This is because a larger Cload requires a larger capacitance to be charged and discharged, leading to a longer time constant and a larger effective probe capacitance.

Similarly, a higher probe factor increases the effective probe capacitance due to the additional capacitance introduced by the probe.

3. Two capacitor experiment:

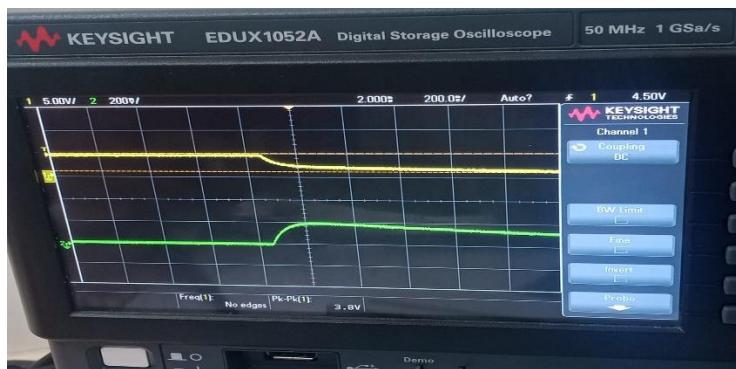
In this experiment we will be observing the switching behaviour (charging and dis-charging) of capacitors for the two capacitor network.



->Connect the circuit as shown above.
-> $C_1 = C_2 = 1 \mu F$ and $R_1 = R_2 = 100 \text{ k}\Omega$.

->Then apply a DC supply voltage of 1V with the help of the function generator.
->Initially, switch 1 is closed (push button is kept pressed) and switch 2 is opened. This allows the capacitor C_1 to charge to the supply voltage(1V).
-> Total time taken to reach steady state:-
 $4 * \text{time constant} - 5 * \text{time constant}$

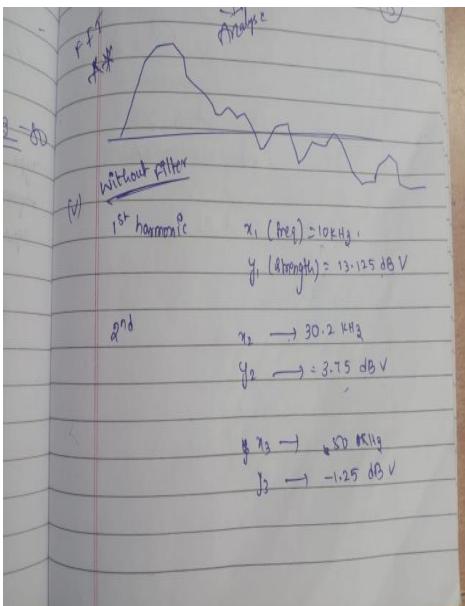
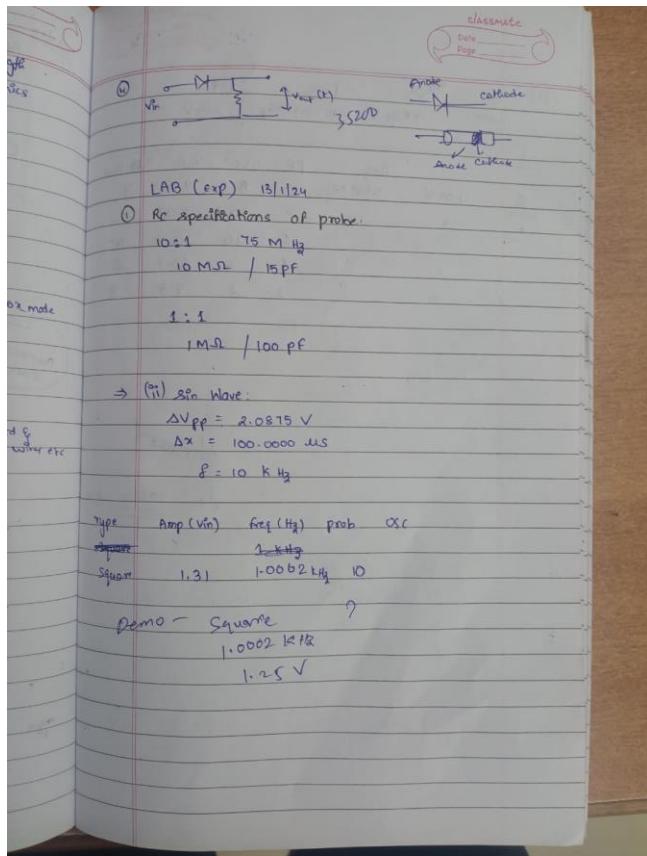
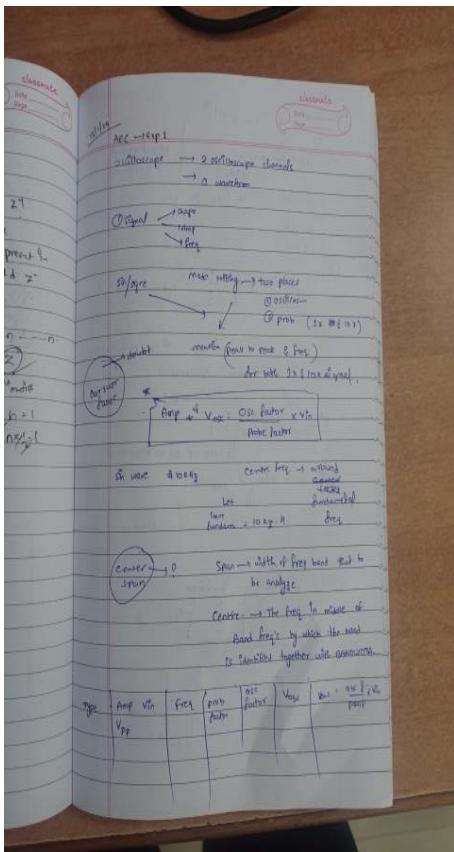
Resistance (R1)	Capacitance(C1)	VP (initial)	VP (final)	Time taken to reach steady state
100kohm	1uF	0v	1v	530.6ms
10kohm	1uF	0v	1v	55.4ms



->Now when C_1 is completely charged, open switch 1 and close switch 2, then observe the voltage across nodes P and Q using probes in the oscilloscope.

->Now change the resistance value to 10k ohm and do the same process as above.

Resistance (R2)	Capacitance(C2)	VP(initial)	VP(final)	Vq(initial)	Vq(final)	Time taken to reach steady state
100k ohm	1uF	1	0.5	0	0.5	264.6 m s
10kohm	1uF	1	0.5	0	0.5	27.4m s



WIC Filter:

$x_1 \rightarrow 10$	$y_1 \rightarrow 13.125 \text{ dB V}$
$x_2 \rightarrow 30.2$	$y_2 \rightarrow 1.25 \text{ dB V}$
$x_3 \rightarrow 50$	$y_3 \rightarrow -5 \text{ dB V}$
$x_4 \rightarrow 70$	$y_4 \rightarrow -11.25 \text{ dB V}$
$x_5 \rightarrow 29.6$	$y_5 \rightarrow -15.625 \text{ dB V}$
$x_6 \rightarrow 110$	$y_6 \rightarrow -18.75 \text{ dB V}$
$x_7 \rightarrow 127.4$	$y_7 \rightarrow -20.658 \text{ dB V}$

$R = 47 \Omega$
 $C = 47 \text{ pF}$
 $RC = 2.209 \times 10^{-6} \text{ s}$
 $f_C = 72.06 \text{ KHz}$

