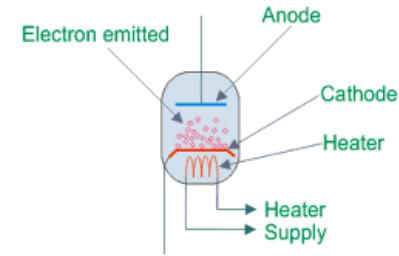


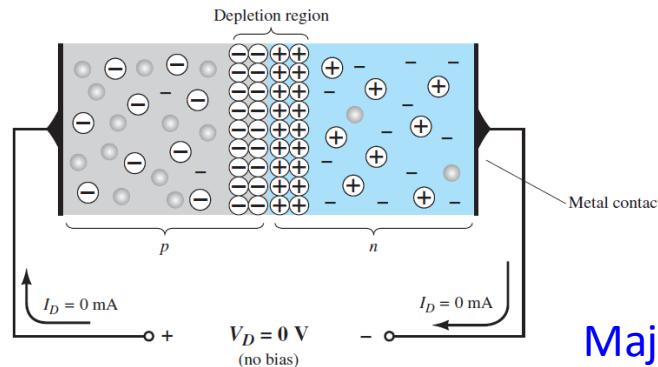
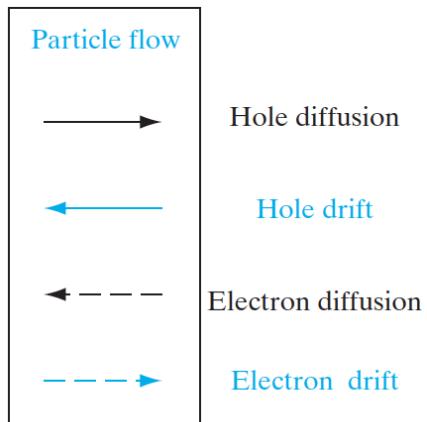
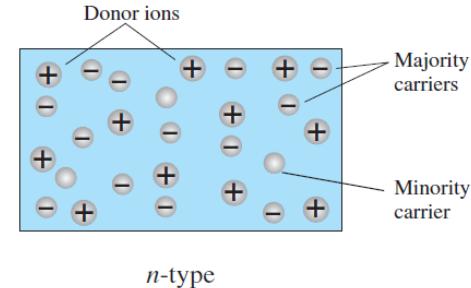
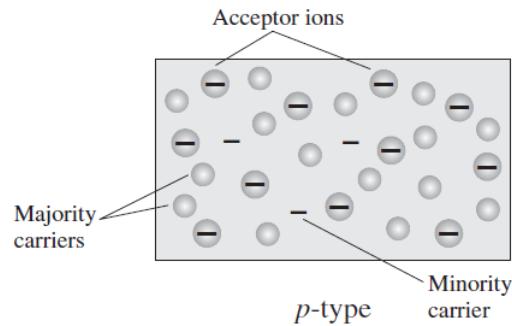


Fundamentals of Electronics
ECE 101

Diode: semiconductor junction



- Once we have both n-type and p-type materials available with us, what happens when we put two different kind of materials together.



Static and mobile charge.

$$J_p(\text{drift}) + J_p(\text{diff.}) = 0$$

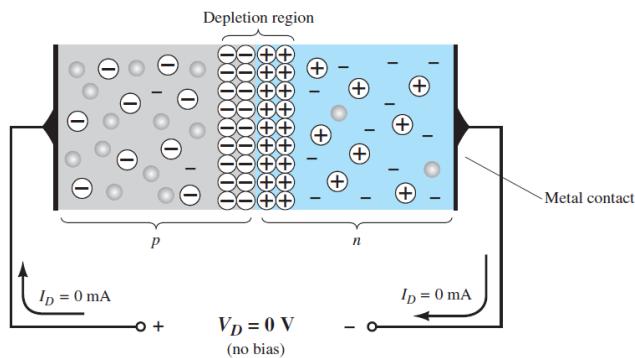
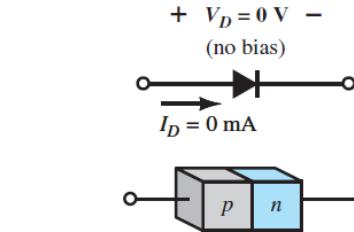
$$J_n(\text{drift}) + J_n(\text{diff.}) = 0$$

Majority and minority currents.

Equilibrium and steady state

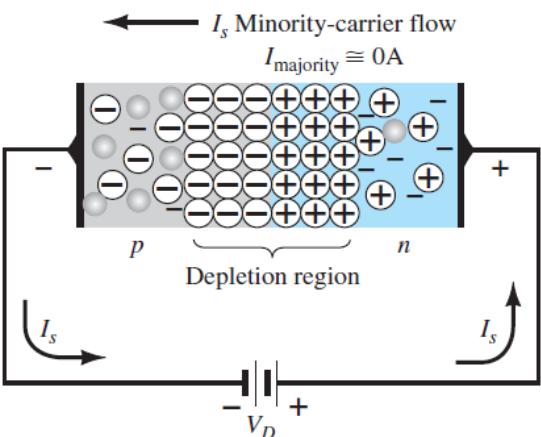
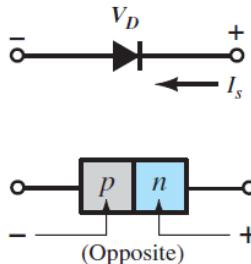
PN junction: under bias

No bias

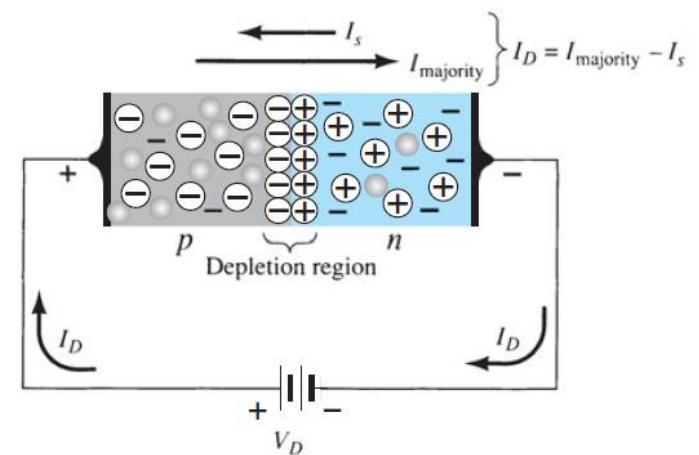
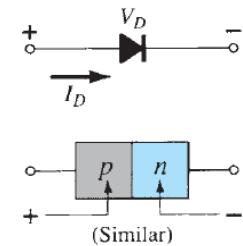


Depletion region

Reverse bias



Forward bias



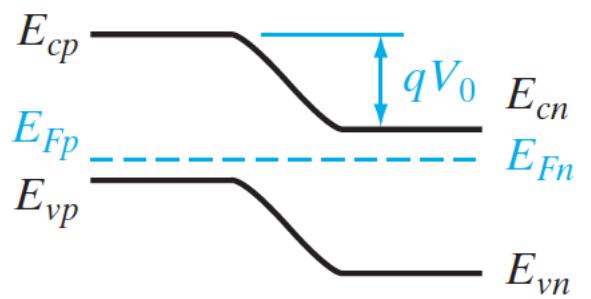
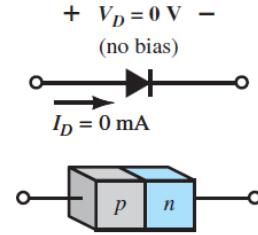
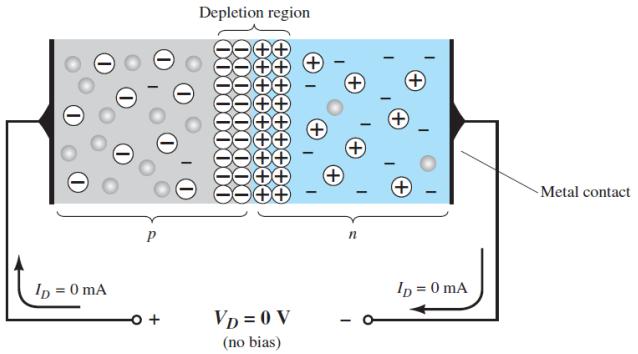
The current that exists under reverse-bias conditions is called the reverse saturation current: nA - μA

$$I_D = I_s(e^{V_D/nV_T} - 1)$$

n – ideality factor of diode

PN junction: diode

Equilibrium conditions

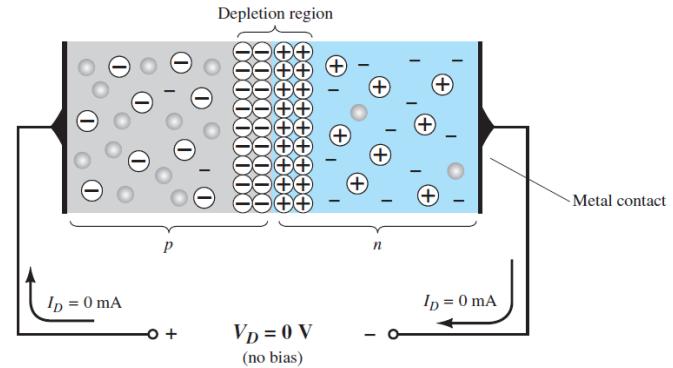
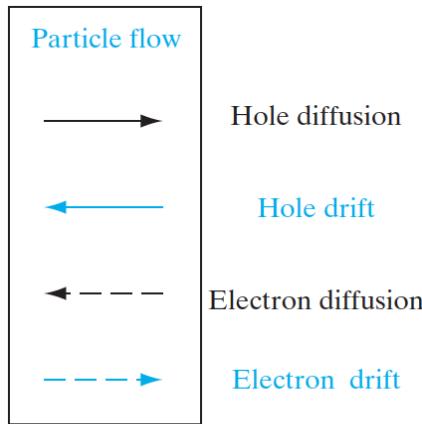
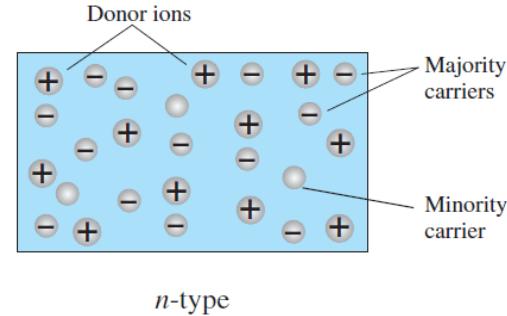
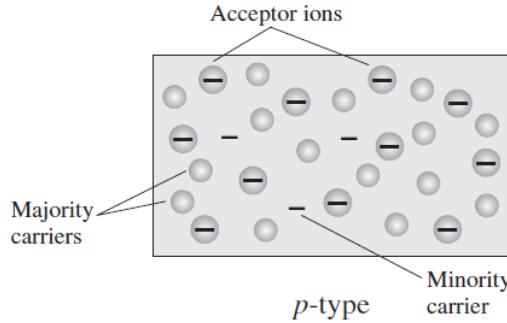


Depletion region

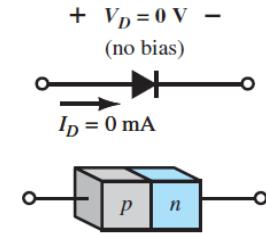
$$V_0 = \frac{kT}{q} \ln \frac{N_a}{n_i^2 / N_d} = \frac{kT}{q} \ln \frac{N_a N_d}{n_i^2}$$

$$W = \left[\frac{2\epsilon V_0}{q} \left(\frac{N_a + N_d}{N_a N_d} \right) \right]^{1/2} = \left[\frac{2\epsilon V_0}{q} \left(\frac{1}{N_a} + \frac{1}{N_d} \right) \right]^{1/2}$$

Summary



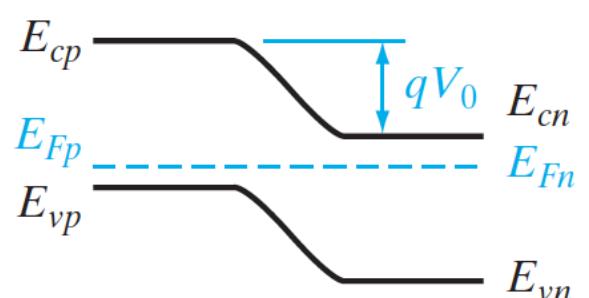
Equilibrium conditions



Static and mobile charge.

$$J_p(\text{drift}) + J_p(\text{diff.}) = 0$$

$$J_n(\text{drift}) + J_n(\text{diff.}) = 0$$

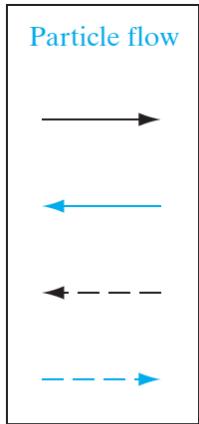
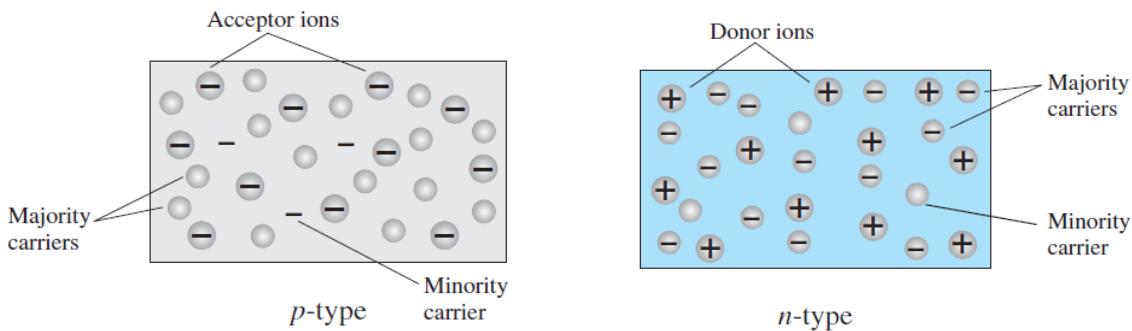


$$V_0 = \frac{kT}{q} \ln \frac{N_a}{n_i^2/N_d} = \frac{kT}{q} \ln \frac{N_a N_d}{n_i^2}$$

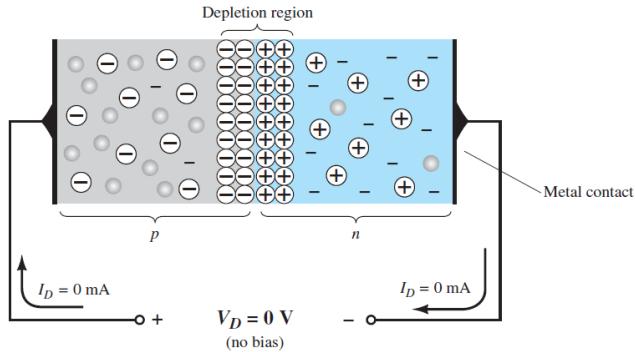
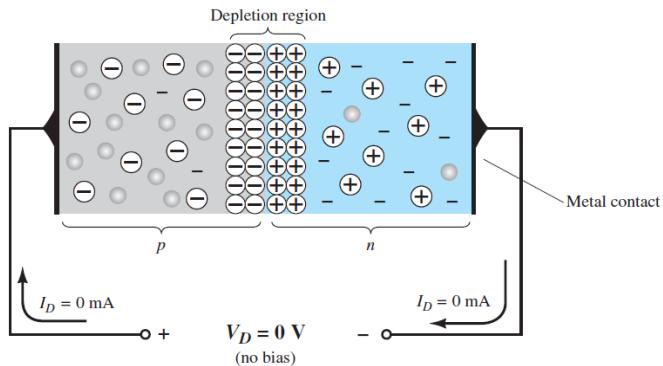
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Majority and minority currents.

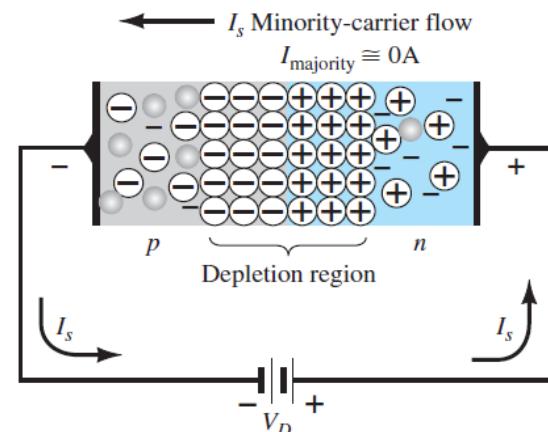
Summary



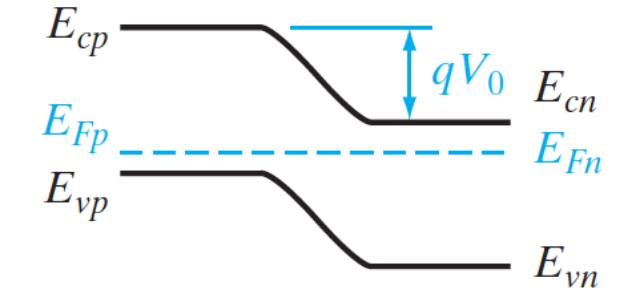
Hole diffusion
Hole drift
Electron diffusion
Electron drift



No bias

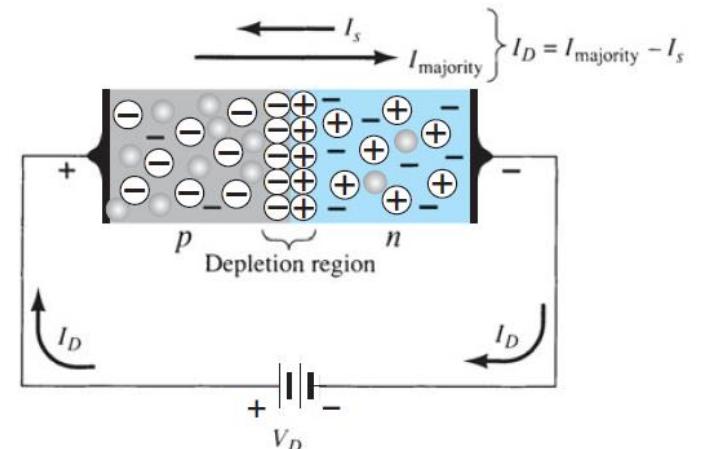


Reverse bias

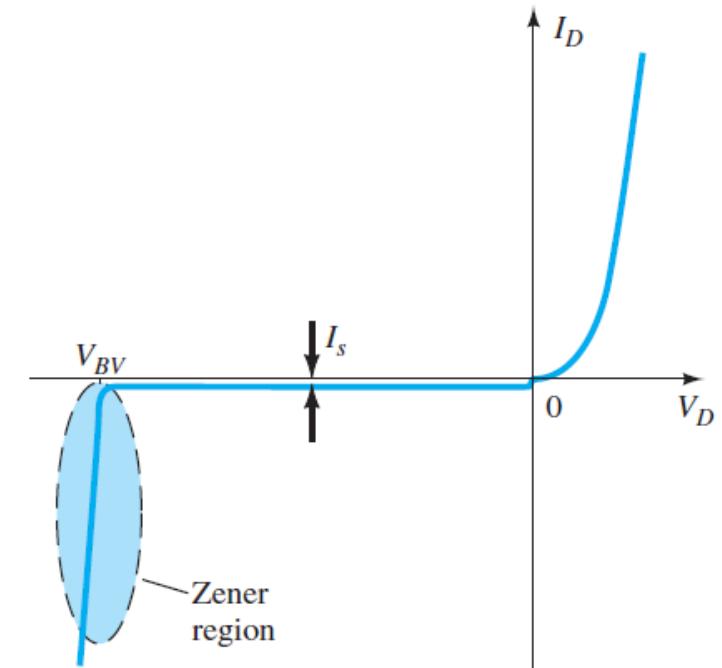
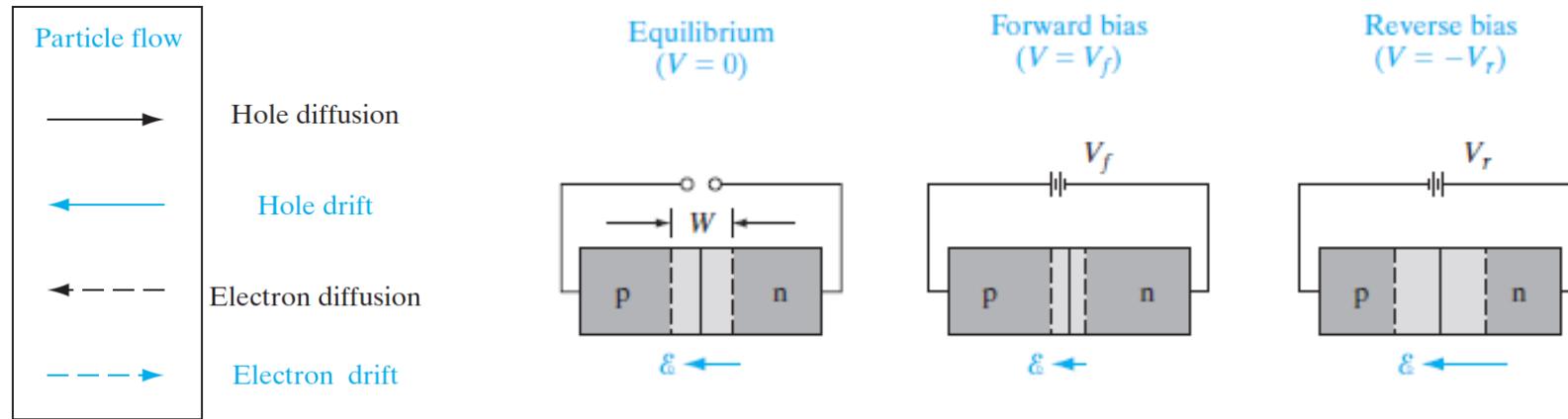


$$V_0 = \frac{kT}{q} \ln \frac{N_a}{n_i^2/N_d} = \frac{kT}{q} \ln \frac{N_a N_d}{n_i^2}$$

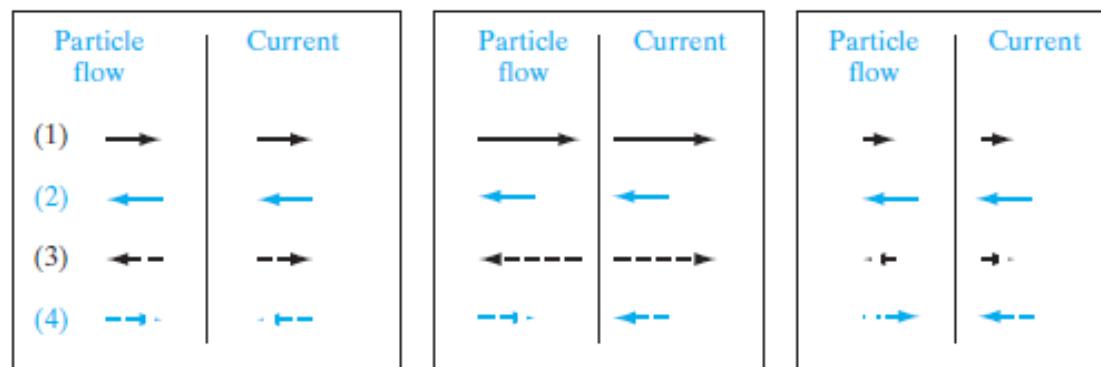
$$W = \left[\frac{2\epsilon V_0}{q} \left(\frac{N_a + N_d}{N_a N_d} \right) \right]^{1/2} = \left[\frac{2\epsilon V_0}{q} \left(\frac{1}{N_a} + \frac{1}{N_d} \right) \right]^{1/2}$$



Forward bias



$$I_D = I_s(e^{V_D/nV_T} - 1)$$

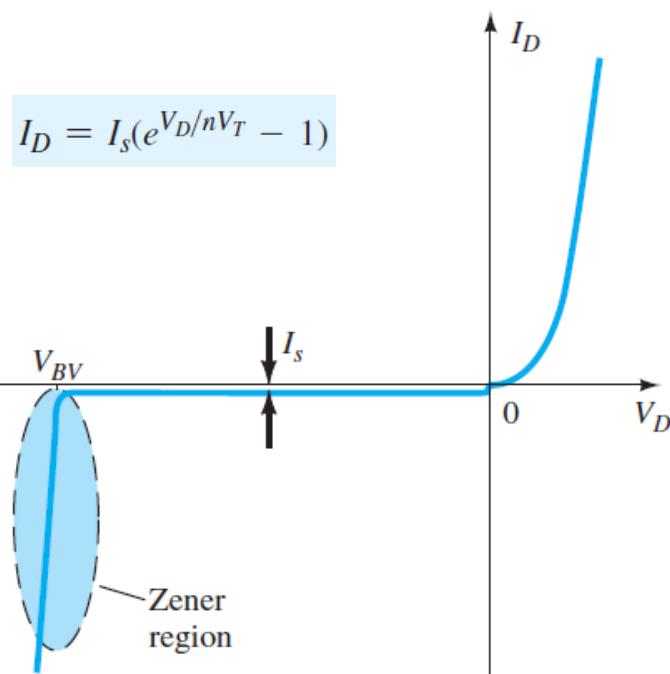


(1) Hole diffusion
(2) Hole drift

(3) Electron diffusion
(4) Electron drift

n – ideality factor of diode

Diode IV characteristics and breakdown

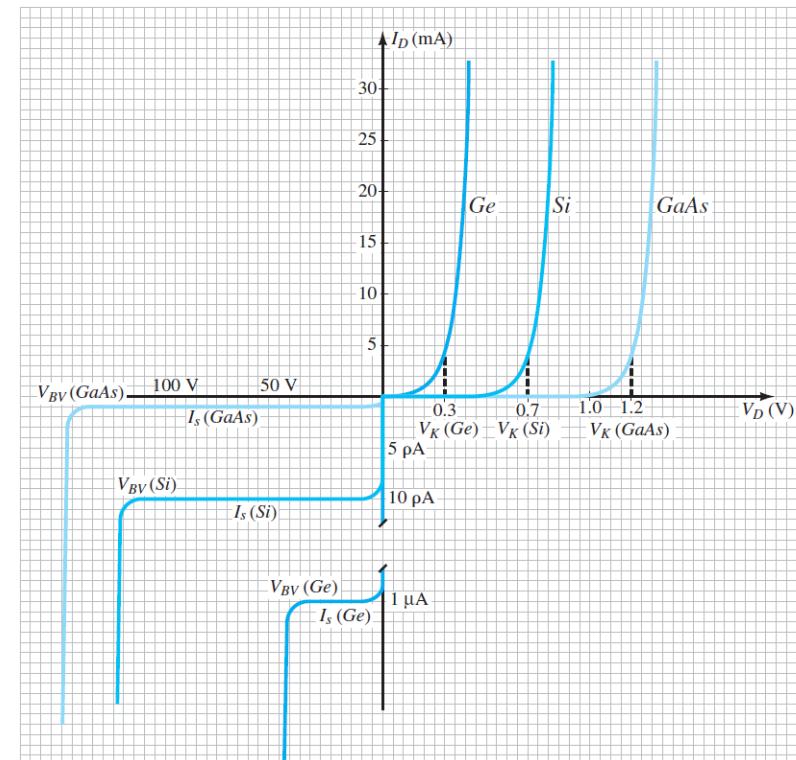


- As the voltage across the diode increases in the reverse-bias region, the velocity of the minority carriers responsible for the reverse saturation current (I_s) will also increase.
- Eventually, their velocity and associated kinetic energy will be sufficient to release additional carriers through collisions with otherwise stable atomic structures.

Diode: different semiconductors

Semiconductor	Intrinsic Carriers n_i
	Intrinsic Carriers (per cubic centimeter)
GaAs	1.7×10^6
Si	1.5×10^{10}
Ge	2.5×10^{13}

In presence of light?

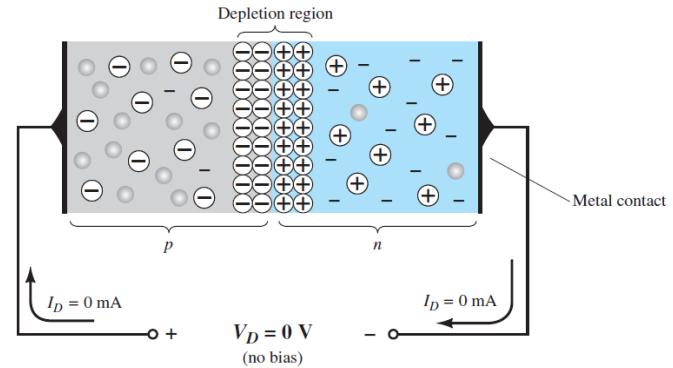
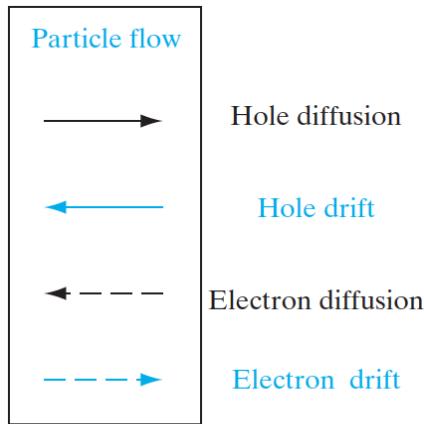
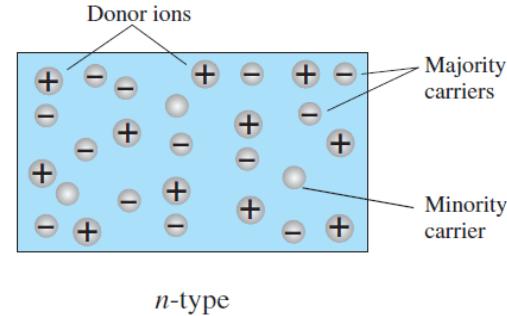
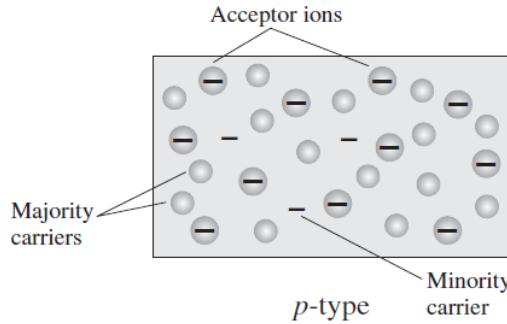


$$Si = 1.1 \text{ eV}$$

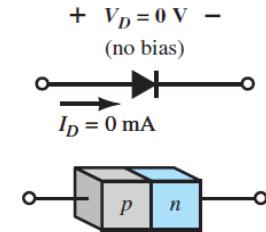
$$Ge = 0.67 \text{ eV}$$

$$GaAs = 1.42 \text{ eV}$$

Summary



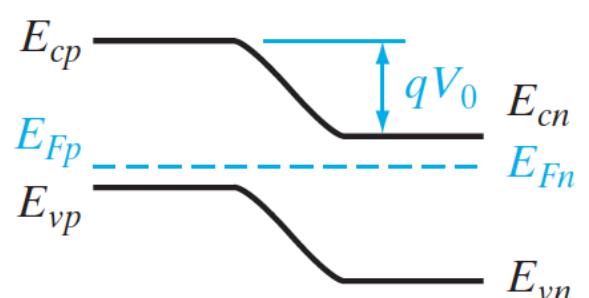
Equilibrium conditions



Static and mobile charge.

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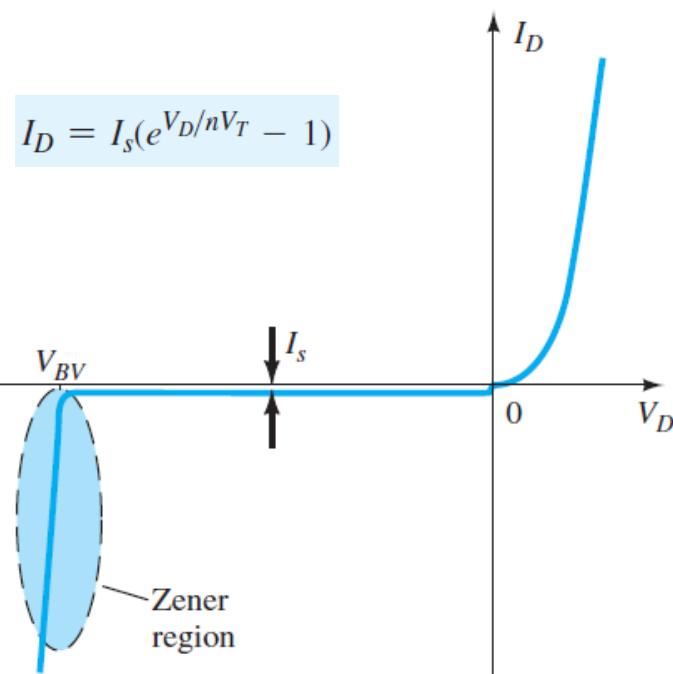


$$V_0 = \frac{kT}{q} \ln \frac{N_a}{n_i^2/N_d} = \frac{kT}{q} \ln \frac{N_a N_d}{n_i^2}$$

$$W = \left[\frac{2\epsilon V_0}{q} \left(\frac{N_a + N_d}{N_a N_d} \right) \right]^{1/2} = \left[\frac{2\epsilon V_0}{q} \left(\frac{1}{N_a} + \frac{1}{N_d} \right) \right]^{1/2}$$

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Diode IV characteristics and breakdown

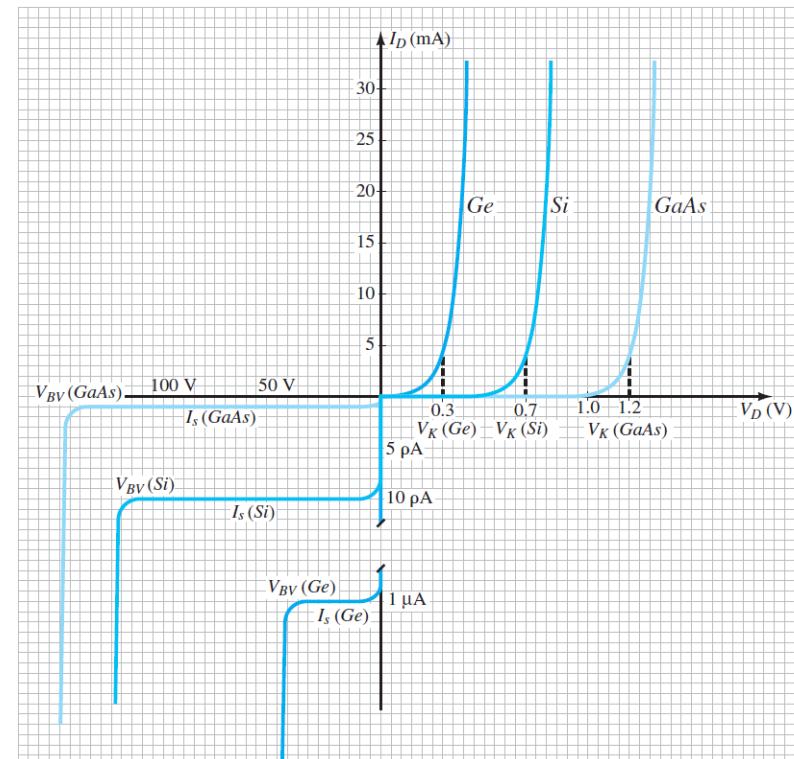


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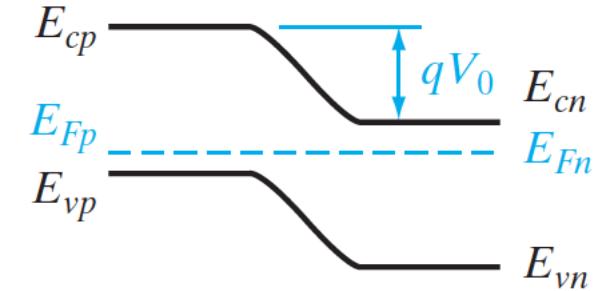
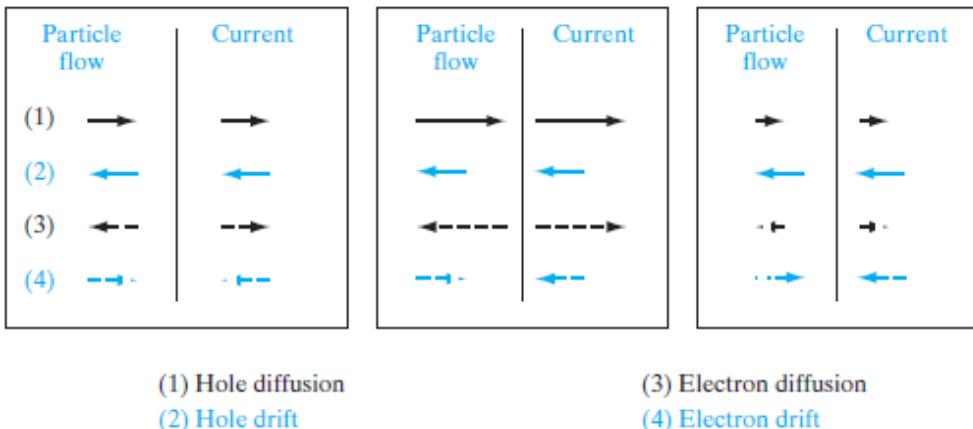
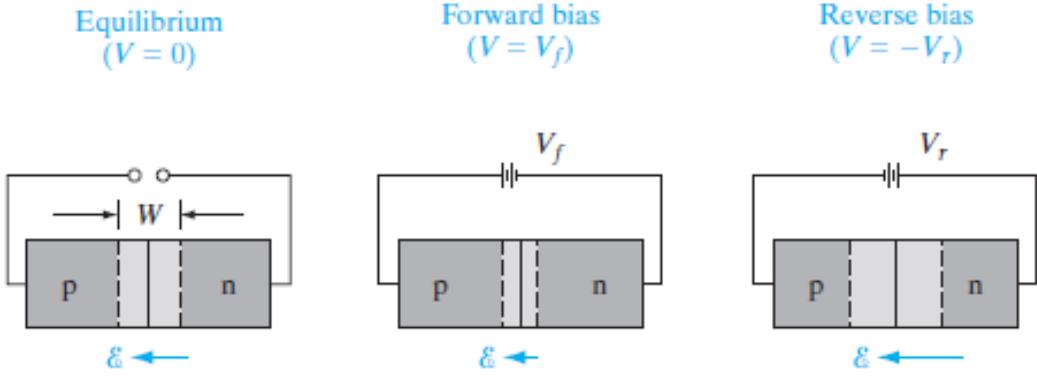
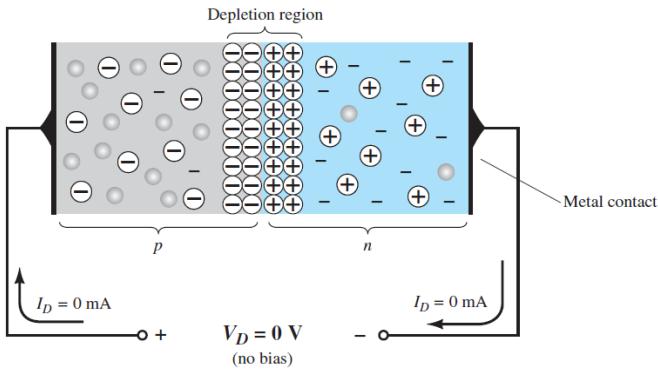


$$\text{Si} = 1.1 \text{ eV}$$

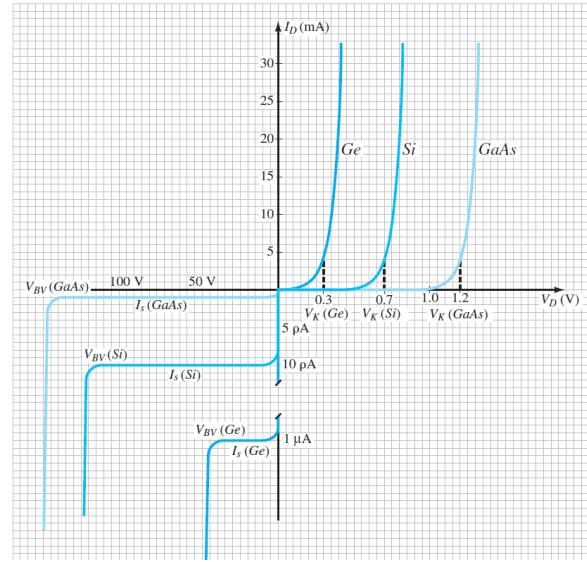
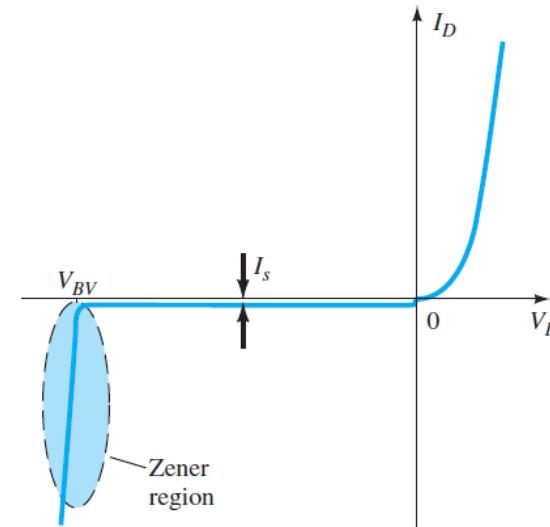
$$\text{Ge} = 0.67 \text{ eV}$$

$$\text{GaAs} = 1.42 \text{ eV}$$

Review



$$I_D = I_s(e^{V_D/nV_T} - 1)$$



Si = 1.1 eV

Ge = 0.67 eV

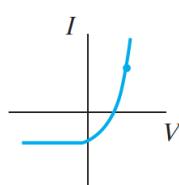
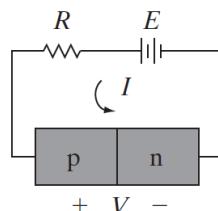
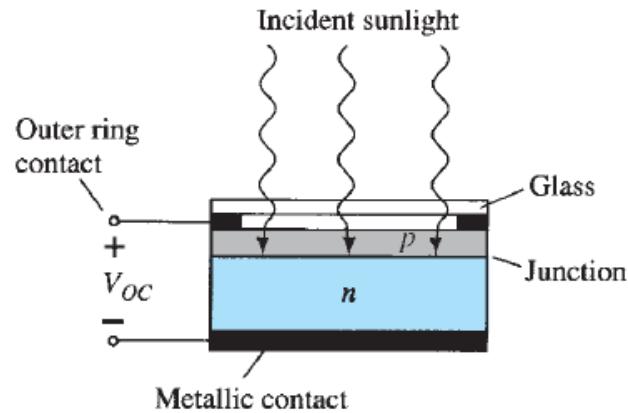
GaAs = 1.42 eV

Semiconductor	Intrinsic Carriers n_i	
	Intrinsic Carriers (per cubic centimeter)	
GaAs		1.7×10^6
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Ge		2.5×10^{13}

Diode applications

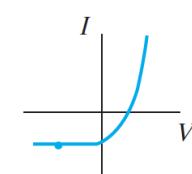
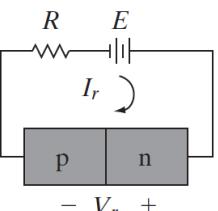
LEDs

Solar Cells

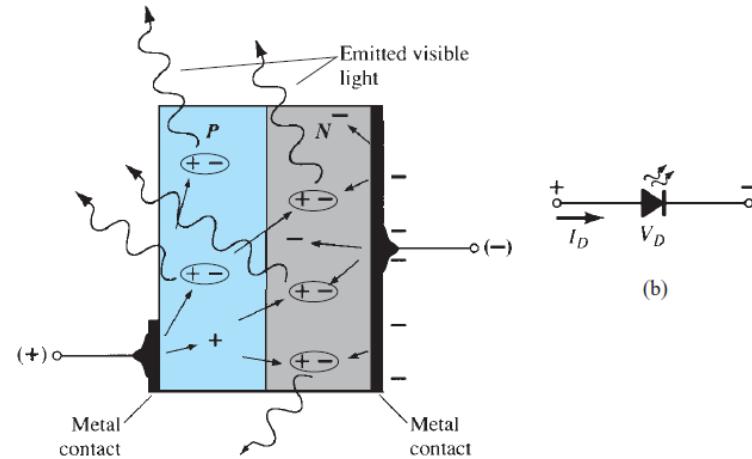


Open circuit voltage

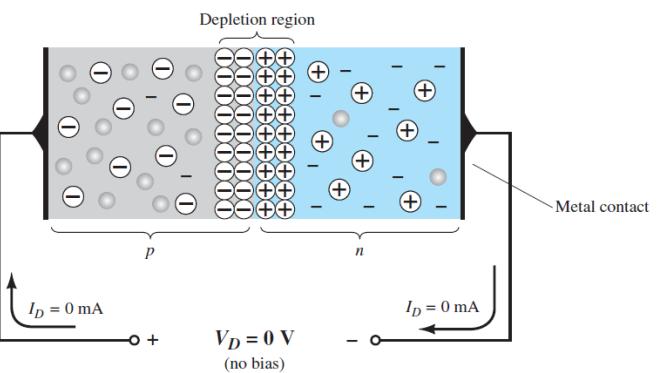
Short circuit current



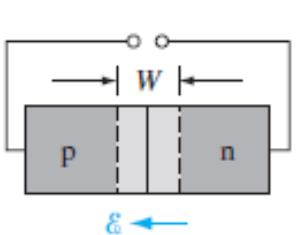
- In Si and Ge diodes the greater percentage of the energy converted during recombination at the junction is dissipated in the form of heat within the structure, and the emitted light is insignificant.
- Diodes constructed of GaAs emit light in the infrared (invisible) zone during the recombination process at the p–n junction.



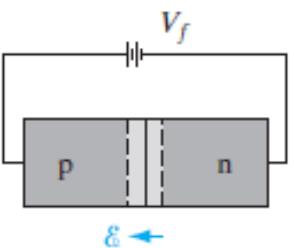
Summary



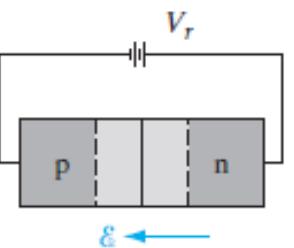
Equilibrium
($V = 0$)



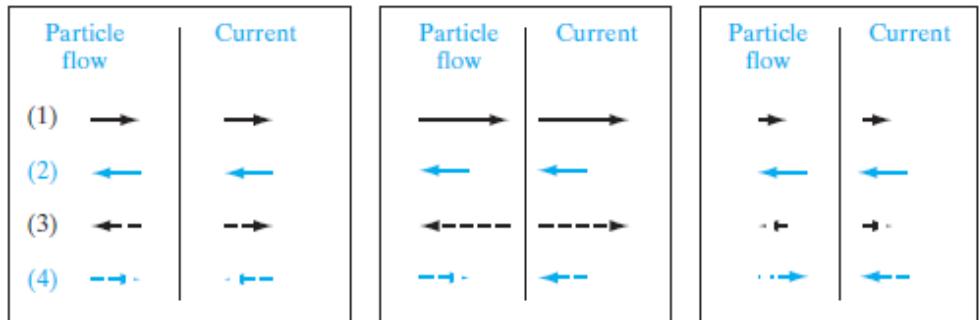
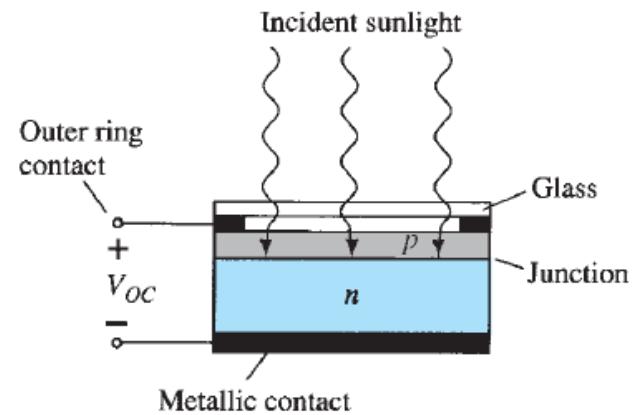
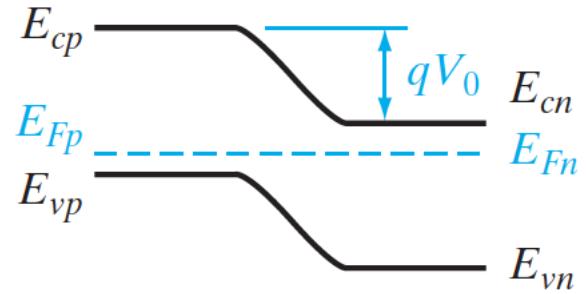
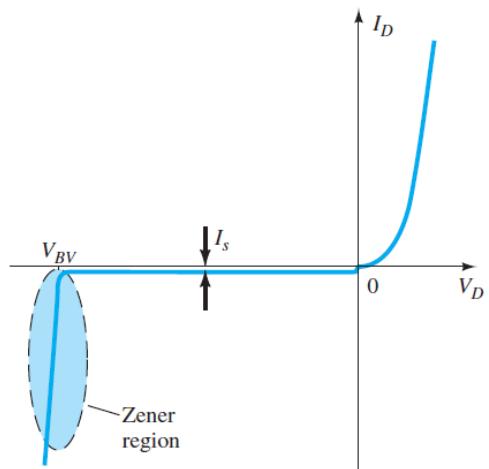
Forward bias
($V = V_f$)



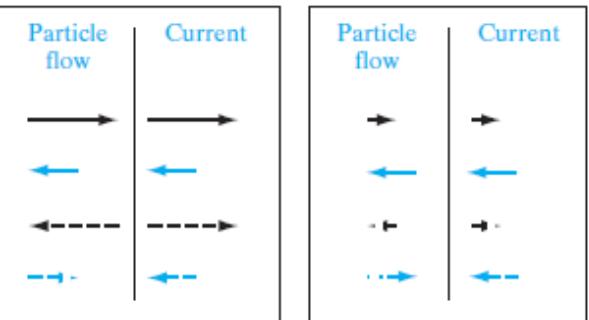
Reverse bias
($V = -V_r$)



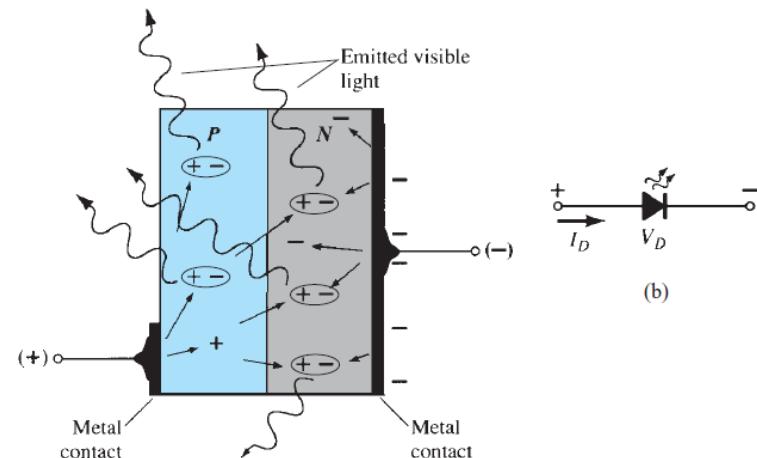
$$I_D = I_s(e^{V_D/nV_T} - 1)$$



(1) Hole diffusion
(2) Hole drift



(3) Electron diffusion
(4) Electron drift



Thank you