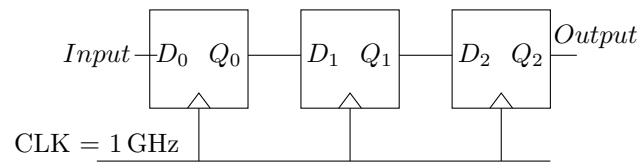


GATE EC 2023

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1. The synchronous sequential circuit shown below works at a clock frequency of 1 GHz. The throughput, in Mbit s^{-1} and the latency, in ns , respectively, are



- (a) 1000, 3
- (b) 333.33, 1
- (c) 2000, 3
- (d) 333.33, 3

(GATE EC 2023)