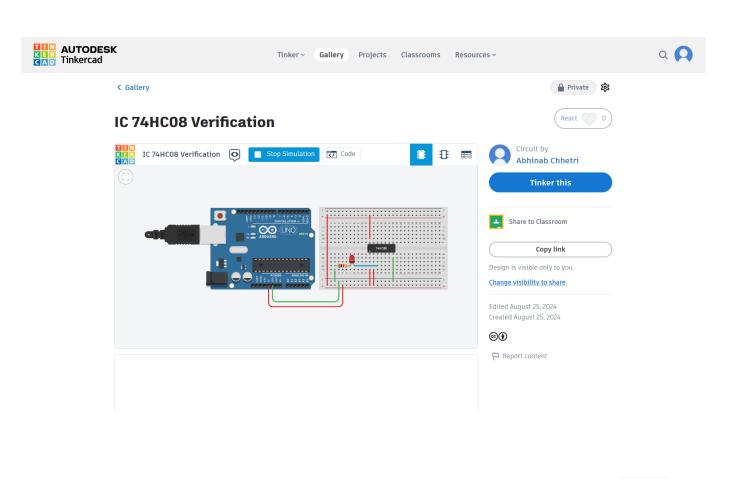


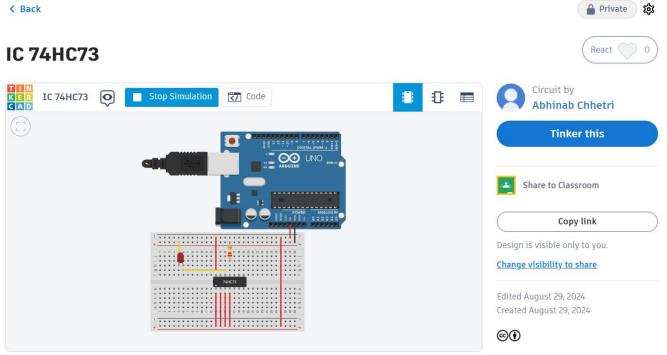
```
1  //AND GATE
2  void setup()
3  {
4     pinMode(6,INPUT);
5     pinMode(7,INPUT);
6     pinMode(8, OUTPUT);
7  }
8  
9  void loop()
10  {
11     if (digitalRead(7)==1 && digitalRead(6)==1)
12         digitalWrite(8,HIGH);
13     }
14     else{
15         digitalWrite(8,LOW);
16     }
17 }
```

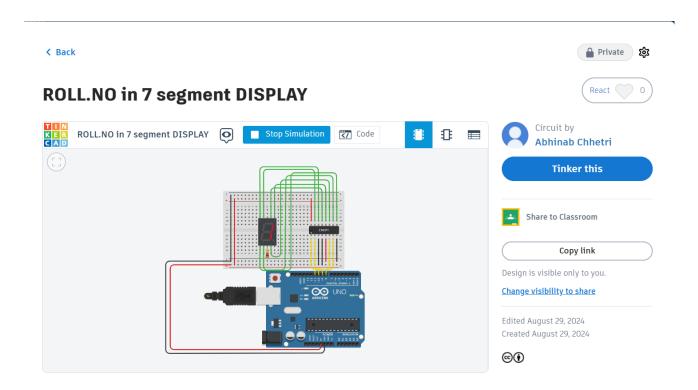
```
1  //XOR gate
2  void setup()
3  {
4     pinMode(6,INPUT);
5     pinMode(7,INPUT);
6     pinMode(8, OUTPUT);
7  }
8  
9  void loop() {
10     int x=digitalRead(7);
11     int y=digitalRead(6);
12
13     if (x==y) {
14         digitalWrite(8,LOW);
15     }
16     else {
17         digitalWrite(8,HIGH);
18     }
19 }
```

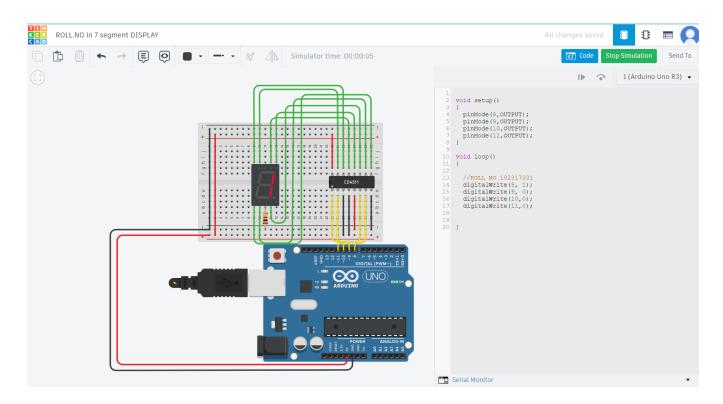
```
1  //NAND GATE
2  void setup()
3  {
4    pinMode(6,INPUT);
5    pinMode(7,INPUT);
6    pinMode(8, OUTPUT);
7  }
8  void loop()
10  {
11    if (digitalRead(7)==1 && digitalRead(6)==1) {
12        digitalWrite(8,LOW);
13    }
14    else{
15        digitalWrite(8,HIGH);
16    }
17  }z
```

Assignment 2-A (TRUTH TABLE VERIFICATION OF IC 74HC08 and IC 74H73)









Assignment 3-B (ONE DISPLAY COUNTER)

