Total No. of Questions : 6]				SEAT No.:	
P543			1 DD 10 MHZ 15	[Total No. of Pages : 3	
			APR - 18/TE/Insem		
T.E. (Information Technology)					
SYSTEMS PROGRAMMING					
(2015 Course) (Semester - II)					
Time: 1	Hour]	Ô		[Max. Marks: 30	
Instructi	ons to the	he candidat	es:	-	
1)			Q3 or Q4, Q5 or Q6.		
2)	Neat diagrams must be drawn wherever necessary.				
3) 4)	Figures to the right indicate full marks.  Assume suitable data, if necessary.				
4)	/155 <i>um</i>	e sunable a	uiu, ij necessury.		
<b>Q1)</b> a)	Wha dom	_	age processing? Explai	n application, PL and execution [4]	
b)	For the following piece of assembly language code, show the contents of symbol table, literal table, pool table and IC. Assume machine opcodes and size of instruction as 1. [6]  START 100				
	A D	OC 10			
		MOVER	AREG B		
		MOVEM	BREG = '1'		
			ADEC - 50°		
		ADD	AREG, = '2'	7.0	
	_	SUB	BREG, = '1'	· ·	
	В	EQU	A + 20		
		LTORG			
		STORE	AREG, NUM		
		MOVER	CREG, LOOP	0, 10,	
		ADD	BREG, = '1'	6	
	NUM	DS	5	A) \$	
	LOOP		10	O, 70.	
	2001	END		9.	
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				P.T.O.	

**Q2)** a) Compare 2-pass and single-pass assembler.

- [4]
- b) List and explain the types of assembly language statements with examples.

[6]

- Q3) a) Explain basic functions of loader. Also explain the working of compile & go loader.[4]
  - b) With format explain the following macro-directives: MACRO, MEND, AIF, AGO, LBL, GBL. [6]

OR

**Q4)** a) What is subroutine linkage? How is it resolved?

[4]

b) For the given assembly code generate MNT, MDT and expanded code [6]

**MACRO** 

MOVEM &R, &N

SUB &R, &A

ADD &R, &N

**MEND** 

MACRO

M2 &P, &Q = B, &
$$OPR = DIV$$

MOVER AREG, &P

&OPR AREG, &Q

MOVEM BREG, &P

**MEND** 

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START 100

**READ VAR** 

A, OPR = SUBM2

**ADD** AREG, VAR

LDA CREG, BREG

CREG, A **SUB** 

M1 C, R = BREG, A1 = A

A

VAR

C

END

- With a neat diagram explain the phases of compiler. **Q5)** a)
  - Using the RE to DFA algorithm generate the DFA for the given regular b) expression. (a + b) \* a \* b#. [5]

[5]

[4]

Explain the structure of lex program. **Q6)** a)

. three Explain the processing of given statement w.r.t. first three phases of b) compiler. [6]

R = (b \* b - 4 \* a \* c) / (2 \* a).

