DOIT ESP32 DEVKIT V1 PINOUT

ESP-WROOM-32

DOIT ESP32 DEVKIT V1

Left side (outer to inner), top pins:

#							Pin	
17			RTC_IO 0		ADC1_CH0	SENSOR_VP	GPI- 36	17
16			RTC_IO 3		ADC1_CH3	SENSOR_VN	GPI- 39	16
15			RTC_IO 4		ADC1_CH6	VDET_1	GPI- 34	15
14			RTC_IO 5		ADC1_CH7	VDET_2	GPI- 35	14
13			RTC_IO 9	TOUCH_9	ADC1_CH4	XTAL_32K_P	GPIO 32	13
12			RTC_IO 8	TOUCH_8	ADC1_CH5	XTAL_32K_N	GPIO 33	12
11		EMAC_RXD0	RTC_IO 6	DAC_1	ADC2_CH8		GPIO 25	11
10		EMAC_RXD1	RTC_IO 7	DAC_2	ADC2_CH9		GPIO 26	10
9		EMAC_RX_DV	RTC_IO 17	TOUCH_7	ADC2_CH7		GPIO 27	9
8	HS2_CLK	EMAC_TXD2	RTC_IO 16	TOUCH_6	ADC2_CH6	HSPI_CLK	GPIO 14	8
7	HS2_DATA2	EMAC_TXD3	RTC_IO 15	TOUCH_5	ADC2_CH5	HSPI_MISO	GPIO 12	7
6	HS2_DATA3	EMAC_RX_ER	RTC_IO 14	TOUCH_4	ADC2_CH4	HSPI_MOSI	GPIO 13	6
5			U1_RXD	HS1_DATA2	SPI_HD	SD2	GPIO 9	5
4			U1_TXD	HS1_DATA3	SPI_WP	SD3	GPIO 10	4
3			U1_RTS	HS1_CMD		CMD	GPIO 11	3
2							GND	2
1							VIN	1

Top center: RESET, ENABLE

Right side:

Pin							#
GPIO 23	VSPI_MOSI		HS1_STROBE				18
GPIO 22	VSPI_WP	SCL	U0_RTS		EMAC_TXD1		17
GPIO 1	TX0		CLK_OUT3		EMAC_RXD2		16
GPIO 3	RX0		CLK_OUT2				15
GPIO 21	VSPI_HD	SDA			EMAC_TX_EN		14
GPIO 19	VSPI_MISO		U0_CTS		EMAC_TXD0		13
GPIO 18	VSPI_CLK		HS1_DATA8				12
GPIO 5	VSPI_CS		HS1_DATA6		EMAC_RX_CLK		11
GPIO 17	TX2		HS1_DATA5		EMAC_CLK_180		10
GPIO 16	RX2		HS1_DATA4		EMAC_CLK_OUT		9
GPIO 4	HSPI_HD	ADC2_CH0	TOUCH_0	RTC_IO 10	EMAC_TX_ER	HS2_DATA1	8
GPIO 2	HSPI_WP	ADC2_CH2	TOUCH_1	RTC_IO 12		HS2_DATA0	7
GPIO 15	HSPI_CS	ADC2_CH3	TOUCH_2	RTC_IO 13	EMAC_RXD3	HS2_CMD	6
GPIO 0		ADC2_CH1		CLK_OUT1	EMAC_TX_CLK		5
GPIO 8	SD1	SPI_MOSI	HS1_DATA1	U2_CTS			4
GPIO 7	SD0	SPI_MISO	HS1_DATA0	U2_RTS			3
GPIO 6	CLK	SPI_CLK	HS1_CLK	U1_CTS			2
3.3V							1

EN · BOOT

- All GPIOs support PWM and interrupts
- GPIOs 34 - 39 are input only
- Do not use GPIOs 6 - 11

Board layout design is for illustrative purpose only. It may vary depending on the manufacturer.

Pin assignments are as per ESP32 Technical Reference Manual version 3.1

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