

# **Specification Sheet**

Model No.: ZJY117

**Description**: 1.8inch TFT LCD Screen resolution 160\*128 Size:34\*45.83\*2.25mm Driver Chip ST7735 interface SPI 18Pin Voltage 3.3V Font Color Option ZJY117

# **TABLE OF Contents**

1.	General Descripti	3
2.	Mechanical Specification	3
3.	Mechanical Dimension	4
4.	Electrical Maximum Ratings	5
5.	Backlight Characteristic	5
6.	Module Function Description	6
7.	Response time&Contrast ratio	.8
8.	Viewing Angle	9
9.	Reliability Trial	10
	Inspection Standards	
11.	Package Method	12

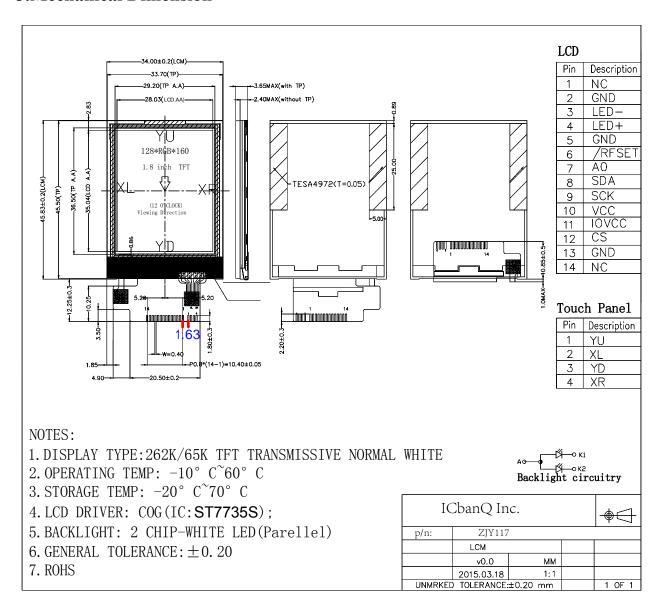
# 1.General Description

MODEL NO	ZJY117
Display Mode	Transmissive
Display Format	Graphic 128RGB*160 Dot-matrix
Input Data	SPI-4wire interface
Viewing Direction	12 o'clock
Drive	ST7735S

# 2. Mechanical Specification

Item	Specifications	Unit
Dimensional outline	34.00(W)*45.83(H)*2.40max(T)	
Without touch	(FPC not include)	mm
Dimensional outline	34.00(W)*45.83(H)*3.65max(T)	
with touch	(FPC not include)	mm
Resolution	128RGB*160	dots
LCD Active area	28.03 (W)*35.04 (H)	mm
Pixel size	0.219(W)*0.219(H)	mm

#### 3. Mechanical Dimension



### 4. Electrical Maximum Ratings

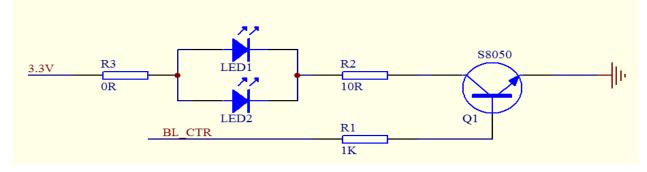
Item	Symbol	Min	Max	Unit	Note
Supply voltage (IOVCC)	V	1.8	3.3	V	
Supply voltage (VCC)	V	2.8	3.3	V	
Operating temperature I	Topr	-20	70	°C	
Storage temperature	T <sub>STR</sub>	-30	80	°C	

NOTE: IOVCC and VCC can be directly connected together to share a single (2.8V~3.3V) voltage supply.

### 5. Backlight Characteristic

Item	Symbol	Min	Typical	Max	Unit
LED module Forward voltage	V <sub>LED</sub>	2. 9	3.1	3.3	V
LED module current	I <sub>LED</sub>	-	30	-	mA
LCD Surface Luminance	Ls	150	180	-	Cd/m²
LCM Surface brightness uniform	$L_{D}$	80	-	-	%

Attachment: backlight reference circuit



## 6. Module Function Description Display module pin definition

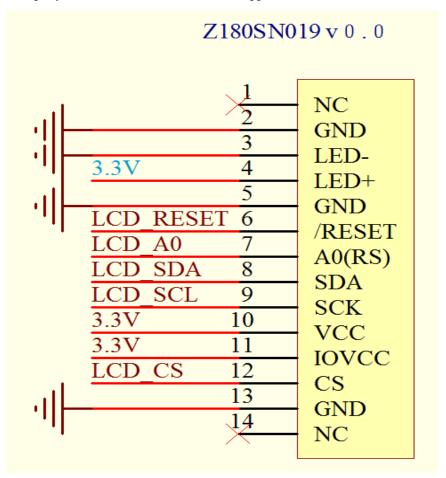
#### 6.1 Display pin definition (Display screen)

PIN No.	Symbo1	Description		
1	NC	No connection		
2	GND	Ground		
3	LED-	Cathode of Backlight		
4	LED+	Anode of Backlight (Backlight positive 2.9-3.3 volt supply)		
5	GND	Ground		
6	/RESET	LCM Reset pin. Signal is active low		
7	A0	Register select pin RS='0': Display data. RS='1': Display data.		
8	SDA	Serial data input / output.		
9	SCK	Serial clock pin.		
10	VCC	Power supply for LCM (Display power supply pin 2.8-3.3V)		
11	IOVCC	Power supply for LCM (Display power supply pin 1.8-3.3V)		
12	Chip select pin("Low" enable)			
13	GND	Ground		
14	NC	No connection		

6.2 Touch screen pin definition (touch panel definition)

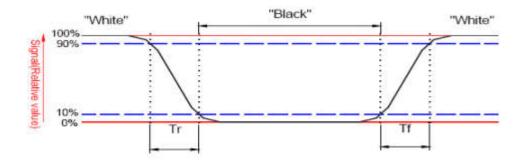
PIN No.	Symbol	Description	
		Touch penal centual pin	
1	YU	Touch panel control pin	
2	XL	Touch panel control pin	
3	YD	Touch panel control pin	
4	XR	Touch panel control pin	

Attachment: Display Z180ST029 v0.0 Reference application circuit



7. Response time&Contrast ratio

Item	Symbol Condition		Remark			Unit
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Response time	Tr+Tf	θ =0°	-	30	60	ms
Contrast ratio	CR	θ =0 °	200	300	-	



Response time icon

## 8. Viewing Angle

Itom	Item Symbol Condition			Remark		Unit
Item	Symbol Condition	Min.	Typ.	Max.	Unit	
	Тор	CR≥10	20	30	-	
Viewing	Bottom	CR≥10	40	45	-	Deg.
angle	Left	CR≥10	40	45	-	
	Right	CR≥10	40	45	-	

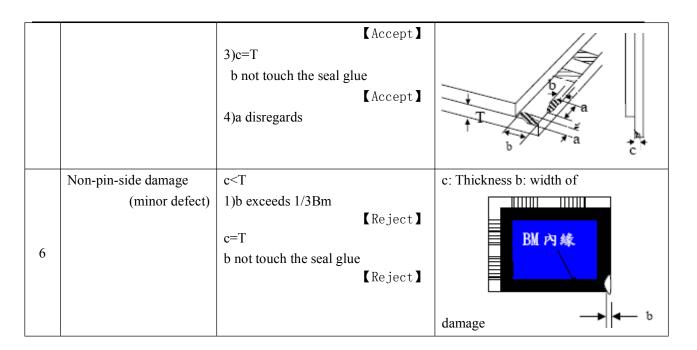
# 9. Reliability Trial

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Non-Operating Test	80°C*120Hrs	No Defect Of Operational
2	Low Temperature Non-Operating Test	-30℃*120Hrs	Function In Room Temperature Are
3	High Temperature/Humidity Non Operating Test	60℃*90%RH*120Hrs	Allowable
4	High Temperature Operating Test	70°C*72Hrs	
5	Low Temperature Operating Test	-20°C*72Hrs	
6	Thermal Shock Test	-20 °C (30Min) v 70 °C (30Min) *10CYCLES	

# 10.Inspection standards

#### 10.1 Glass defect

NO	Defect item	Criteria	Remark
1	Dimension Unconformity (Major defect)	By Engineering Drawing	
2	Cracks (Major defect)	<ol> <li>Linear cracks panel</li> <li>Reject</li> <li>Nonlinear crack contrast by limited sample</li> </ol>	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influence assemblage. 1) b≤1/3Pin width(non bonding area) 【Accept】 2)bonding area≤0.5mm	A: Length, b: Width
4	Pin-side ,conductive area damaged (minor defect)	(a c: disregards) b≤1/3of effective length for bonding electrode  【Accept】	a: length, b: Width, c: Thickness
5	Pin-side,non-conductive area damaged (minor defect)	1)Damage area don't touch the ITO (Inclueling contraposition mark, except scribing mark)  【Accept】 2)C <t 3of="" b≤bm1="" th="" width<=""><th>a: Length, b: Width c: Thickness</th></t>	a: Length, b: Width c: Thickness



10.2LCD appearance defect(View area)

NO	Defect item	Criteria		Remark
		Specification	Allowable	note1:L: Length, W: Width
	Eiler -1	$W \leq 0.03 \text{mm}$	disregard	note2: disregard if out of AA
	Fiber glass cratch polarizer	$0.03 \text{mm} < W \le 0.05 \text{mm};$	2	<b>←</b> т →
1	scratch/folded	L≦3.0mm		
	(minor defect)	$0.05$ mm $\leq$ W $\leq$ 0.1mm;	1	
	(minor derect)	L ≦ 3. 0mm	1	w
		W>0.1mm;L>3.0mm	0	**
	Polarizer bubble	Φ ≤ 0.2mm	disregard	note1: $\Phi = (L+W)/2$ , L:Length,
2	concave and convex (minor defect)	$0.2$ mm $< \phi \le 0.3$ mm	2	W :Width
2		$0.3$ mm $< \phi \le 0.5$ mm	1	note2:disregard if out of AA
	(minor derect)	0.5mm< ф	0	
		φ ≤ 0.15mm	disregard	note2:disregard if out of AA
	Black dots, dirty dots,	$0.15 \text{mm} < \phi \le 0.25 \text{mm}$	2	
3	impurities, eye winker	$0.25$ mm $< \phi \le 0.3$ mm	1	<u></u>
	(minor defect)	0.3mm< φ	0	$\phi$
		φ ≤ 0.1mm	disregard	note1: $\Phi = (L+W)/2$ , L=Length,
4	Polarizer prick (minor defect)	$0.1$ mm $< \phi \le 0.25$ mm	3	W=Width
_		φ>0.25mm	0	note2:the distance between two dots>5mm

## 11.Package Method

Module shipping package schematic:

