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# Pin Numbering - Raspberry Pi 4B

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## **Numbering Scheme**

Pi4J (by default) uses an abstract pin numbering scheme to help insulate software from hardware changes. Pi4J implements the same pin number scheme as the Wiring Pi project. More information about the WiringPi pin number scheme can be found here: http://wiringpi.com/pins/ (http://wiringpi.com/pins/)

Pi4J provides a RaspiPin (../apidocs/index.html?com/pi4j/io/gpio/RaspiPin.html) enumeration that is used to manage the accessible GPIO pins.

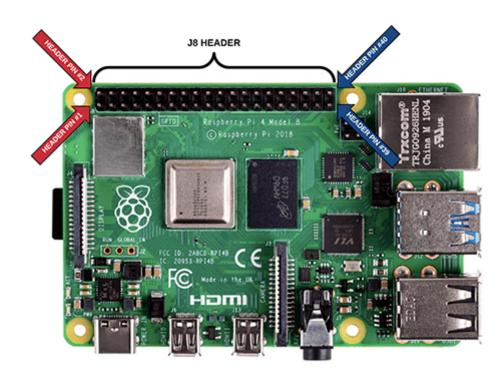
(NOTE: Pi4J also can be configured to use the Broadcom Pin numbering scheme.)

Please see this page for more information on both the WiringPi and Broadcom pin numbering schemes: >> Pin Numbering Schemes (../pin-numbering-scheme.html)

### **Expansion Header**

The Raspberry Pi 4B board contains a single 40-pin expansion header labeled as 'J8' providing access to 28 unique GPIO pins.

(Pins 1, 2, 39 & 40 are also labeled below.)



## **GPIO Pinout (40-pin J8 Header)**

The diagram below illustrates the GPIO pinout using the Pi4J/WiringPi GPIO numbering scheme.

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GPIO#	NAME				-	NAME	GPIO
	3.3 VDC Power	1		0	2	5.0 VDC Power	
8	GPIO 8 SDA1 (I2C)	က	0	0	4	5.0 VDC Power	
9	GPIO 9 SCL1 (I2C)	5	0	0	6	Ground	
7	GPIO 7 GPCLK0	7	0	0		GPIO 15 TxD (UART)	15
	Ground	6	0	0	10	GPIO 16 RxD (UART)	16
0	GPIO 0	11	0	0	12	GPIO 1 PCM_CLK/PWM0	1
2	GPIO 2	13	0	0	14	Ground	
3	GPIO 3	15	0	0	16	GPIO 4	4
	3.3 VDC Power	17	0	0	18	GPIO 5	5
12	GPIO 12 MOSI (SPI)	19	0	0	20	Ground	
13	GPIO 13 MISO (SPI)	21	0	0	22	GPIO 6	6
14	GPIO 14 SCLK (SPI)	23	0	0	24	GPIO 10 CE0 (SPI)	10
	Ground	25	0	0	26	GPIO 11 CE1 (SPI)	11
30	SDA0 (I2C ID EEPROM)	27	0	0	28	SCL0 (I2C ID EEPROM)	31
21	GPIO 21 GPCLK1	29	0	0	30	Ground	
22	GPIO 22 GPCLK2	31	0	0	32	GPIO 26 PWM0	26
23	GPIO 23 PWM1	33	0	0	34	Ground	
24	GPIO 24 PCM_FS/PWM1	35	0	0	36	GPIO 27	27
25	GPIO 25	37	0	0	38	GPIO 28 PCM_DIN	28
	Ground	39	0	0	40	GPIO 29 PCM_DOUT	29
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(click here for hi-resolution image) (../images/pi4j-rpi-4b-pinout.png)

#### **Known Issues**

On Raspberry Pi models starting with model 3B (including Raspberry Pi Model 4B) the hardware-based serial/UART device /dev/ttyAMA0 has been re-purposed to communicate with the the built-in Bluetooth modem and is no longer mapped to the serial RX/TX pins on the GPIO header. Instead, a new serial port "/dev/ttyS0" has been provided which is implemented with a software-based UART (miniUART). This software-based UART ("/dev/ttyS0") does not support PARITY and some have experienced some stability issues using this port at higher speeds. If you don't need Bluetooth functionality, you can disable the BT modem and configure the RPi to use a device-tree overlay to re-map the hardware-based serial UART ("/dev/ttyAMA0") back to the GPIO header pins for TX/RX. See the instructions on this page for details on how to configure the device-tree overlay and disable the bluetooth modem/service:

Disable Bluetooth Modem (https://openenergymonitor.org/emon/node/12311)

#### **Additional Resources**

- Please visit the usage (../usage.html) page for additional details on how to control these pins using Pi4J.
- Click here for more information on the Raspberry Pi Model 4B. (https://www.raspberrypi.org/products/raspberry-pi-4-model-b/)
- Click here for more information the Raspberry Pi pin functions. (http://elinux.org/RPi\_BCM2835\_GPIOs)

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