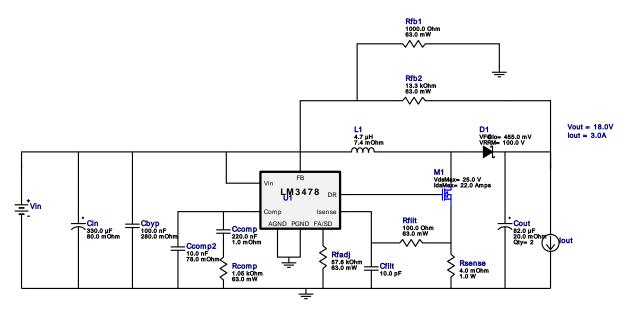


WEBENCH® Design Report

VinMin = 5.0V VinMax = 14.0V Vout = 18.0V lout = 3.0A Device = LM3478MM/NOPB Topology = Boost Created = 2024-08-02 11:06:07.033 BOM Cost = NA BOM Count = 17 Total Pd = 4.6W

Design: 84 LM3478MM/NOPB LM3478MM/NOPB 5V-14V to 18.00V @ 3A

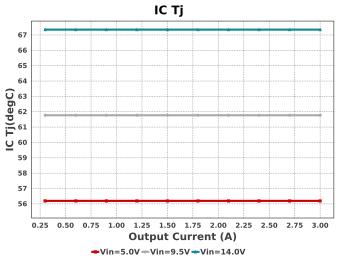


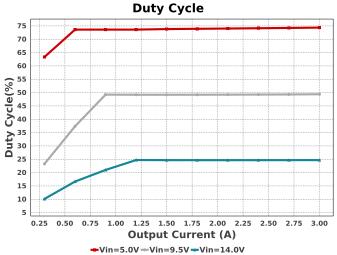
1. With the low turn of voltage of the LM34x8 your power supply may current limit before you reach your working input voltage. If this happens, or to preempt this from happening, you can include a low pass RC filter from input voltage to Vin on the IC. Make sure the rise time on the RC network is slower than your supply's rise time.

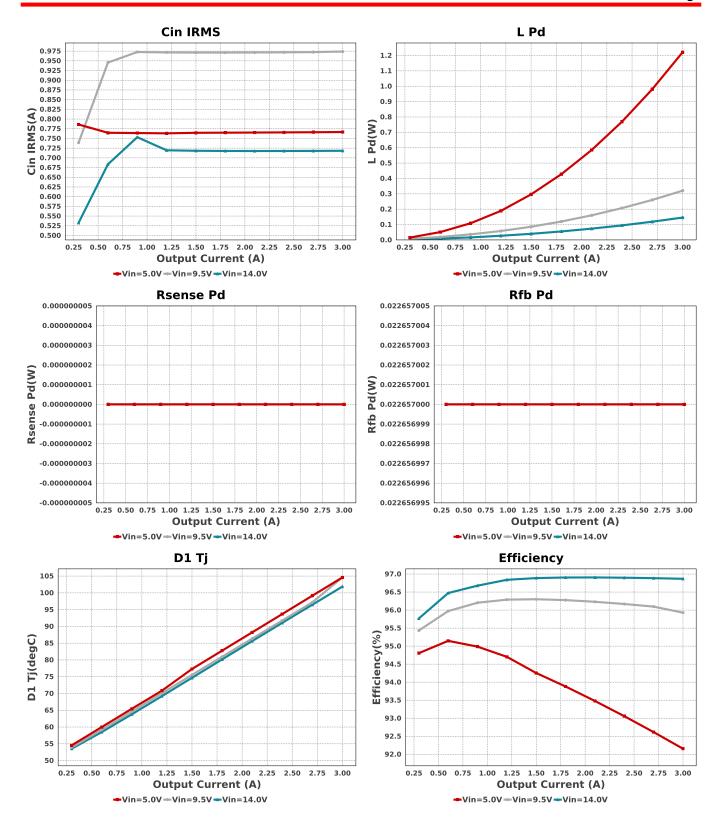
Electrical BOM

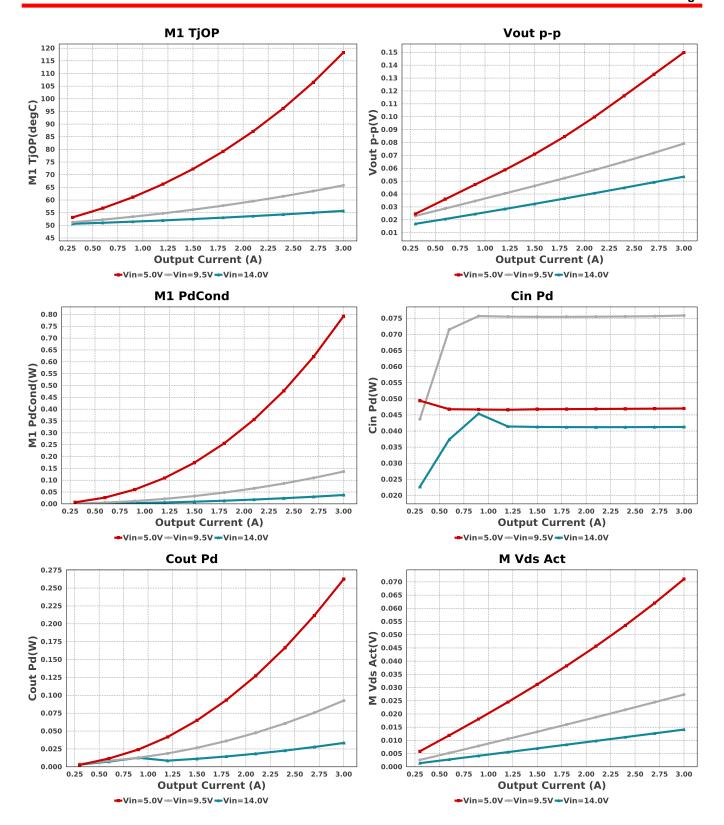
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cbyp	AVX	08053C104KAT2A Series= X7R	Cap= 100.0 nF ESR= 280.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm ²
Ccomp	Kemet	C0603C224Z4VACTU Series= Y5V	Cap= 220.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Ccomp2	AVX	08055C103KAT2A Series= X7R	Cap= 10.0 nF ESR= 78.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm ²
Cfilt	Yageo	CC0805JRNPO9BN100 Series= C0G/NP0	Cap= 10.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm ²
Cin	Panasonic	EEE-FK1V331P Series= FK	Cap= 330.0 uF ESR= 80.0 mOhm VDC= 35.0 V IRMS= 850.0 mA	1	\$0.42	
Cout	Panasonic	35SVPF82M	Cap= 82.0 uF	2	\$1.17	SM_RADIAL_G 172 mm²
		Series= SVPF	ESR= 20.0 mOhm VDC= 35.0 V IRMS= 4.0 A			CAPSMT 62 E12 106 mm ²

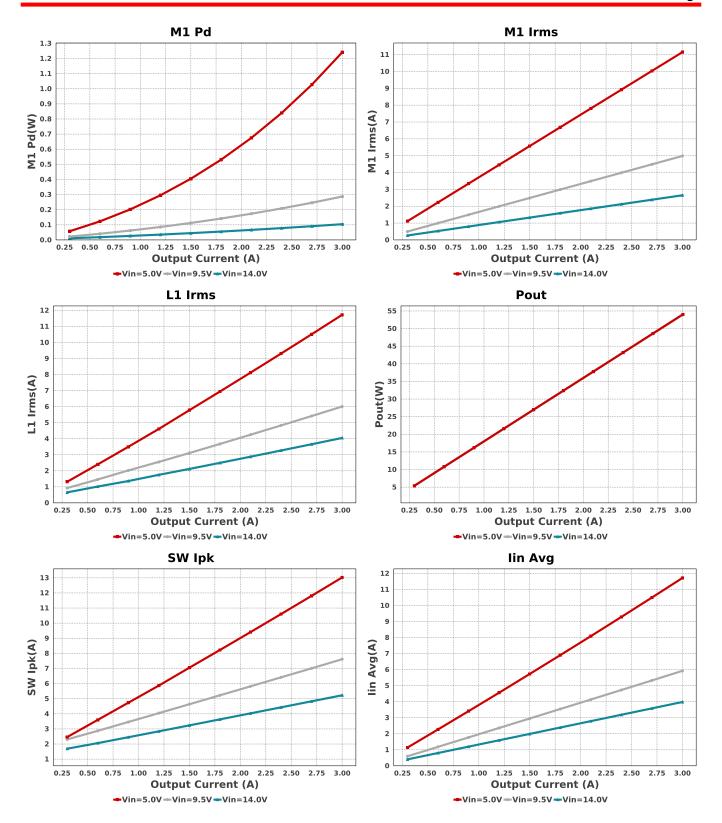
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
D1	STMicroelectronics	STPS20M100SG-TR	VF@Io= 455.0 mV VRRM= 100.0 V	1	\$1.94	DDPAK 210 mm ²
L1	CUSTOM	CUSTOM	L= 4.7 μH 7.4 mOhm	1	NA	XAL1010 0 mm ²
M1	Texas Instruments	CSD16327Q3	VdsMax= 25.0 V ldsMax= 22.0 Amps	1	\$0.32	DQG0008A 18 mm²
Rcomp	Vishay-Dale	CRCW04021K05FKED Series= CRCWe3	Res= 1.05 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfadj	Vishay-Dale	CRCW040257K6FKED Series= CRCWe3	Res= 57.6 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfb1	Vishay-Dale	CRCW04021K00FKED Series= CRCWe3	Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfb2	Vishay-Dale	CRCW040213K3FKED Series= CRCWe3	Res= 13.3 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfilt	Vishay-Dale	CRCW0402100RFKED Series= CRCWe3	Res= 100.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rsense	Panasonic	ERJ-M1WTF4M0U Series= ERJ	Res= 4.0 mOhm Power= 1.0 W Tolerance= 1.0%	1	\$0.17	2512 43 mm ²
U1	Texas Instruments	LM3478MM/NOPB	Switcher	1	\$1.05	MUA08A 24 mm ²

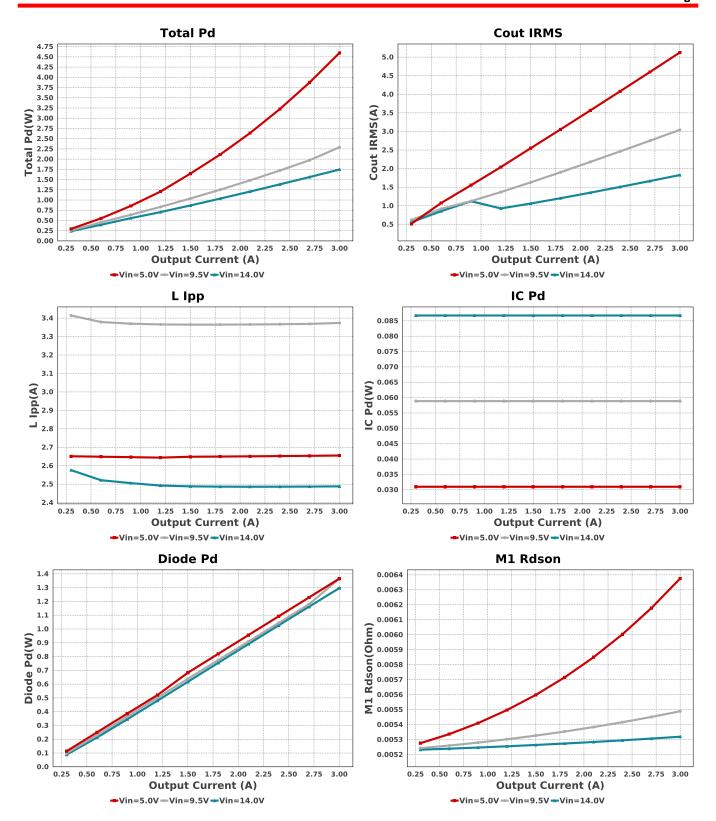


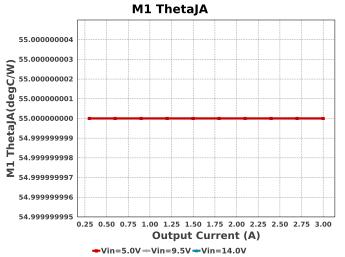


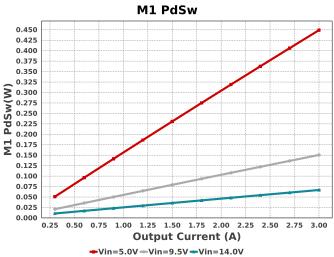


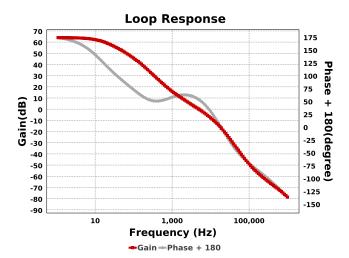












Operating Values

raining varaoo			
Name	Value	Category	Description
BOM Count	17		Total Design BOM count
Total BOM	NA		Total BOM Cost
Cin IRMS	766.504 mA	Capacitor	Input capacitor RMS ripple current
Cin Pd	47.002 mW	Capacitor	Input capacitor power dissipation
Cout IRMS	5.123 A	Capacitor	Output capacitor RMS ripple current
Cout Pd	262.46 mW	Capacitor	Output capacitor power dissipation
D1 Tj	104.6 degC	Diode	D1 junction temperature
Diode Pd	1.365 W	Diode	Diode power dissipation
IC Pd	30.978 mW	IC	IC power dissipation
IC Tj	56.196 degC	IC	IC junction temperature
IC Tolerance	24.3 mV	IC	IC Feedback Tolerance
ICThetaJA	200.0 degC/W	IC	IC junction-to-ambient thermal resistance
lin Avg	11.719 A	IC	Average input current
L lpp	2.655 A	Inductor	Peak-to-peak inductor ripple current
L Pd	1.22 W	Inductor	Inductor power dissipation
L1 Irms	11.723 A	Inductor	Inductor ripple current
M Vds Act	71.07 mV	Mosfet	M Vds
M1 Irms	11.147 A	Mosfet	M1 MOSFET Irms
M1 Pd	1.241 W	Mosfet	M1 MOSFET total power dissipation
M1 PdCond	792.19 mW	Mosfet	M1 MOSFET conduction losses
M1 PdSw	449.14 mW	Mosfet	M1 MOSFET switching losses
M1 Rdson	6.376 mOhm	Mosfet	Drain-Source On-resistance
M1 ThetaJA	55.0 degC/W	Mosfet	MOSFET junction-to-ambient thermal resistance
M1 TjOP	118.273 degC	Mosfet	M1 MOSFET junction temperature
Cin Pd	47.002 mW	Power	Input capacitor power dissipation
Cout Pd	262.46 mW	Power	Output capacitor power dissipation
Diode Pd	1.365 W	Power	Diode power dissipation
IC Pd	30.978 mW	Power	IC power dissipation
L Pd	1.22 W	Power	Inductor power dissipation
M1 Pd	1.241 W	Power	M1 MOSFET total power dissipation
M1 PdCond	792.19 mW	Power	M1 MOSFET conduction losses
M1 PdSw	449.14 mW	Power	M1 MOSFET switching losses
	BOM Count Total BOM Cin IRMS Cin Pd Cout IRMS Cout Pd D1 Tj Diode Pd IC Pd IC Tj IC Tolerance ICThetaJA Iin Avg L Ipp L Pd L1 Irms M Vds Act M1 Irms M1 Pd M1 PdCond M1 PdSw M1 Rdson M1 ThetaJA M1 TjOP Cin Pd Cout Pd Diode Pd IC Pd L Pd M1 TjOP Cin Pd Cout Pd Diode Pd IC Pd L Pd M1	BOM Count 17 Total BOM NA Cin IRMS 766.504 mA Cin Pd 47.002 mW Cout IRMS 5.123 A Cout Pd 262.46 mW D1 Tj 104.6 degC Diode Pd 1.365 W IC Pd 30.978 mW IC Tj 56.196 degC IC Tolerance 24.3 mV ICThetaJA 200.0 degC/W lin Avg 11.719 A L Ipp 2.655 A L Pd 1.22 W L1 Irms 11.723 A M Vds Act 71.07 mV M1 Irms 11.147 A M1 Pd 1.241 W M1 PdSw 449.14 mW M1 PdSw 449.14 mW M1 TjOP 118.273 degC Cin Pd 47.002 mW Cout Pd 262.46 mW Diode Pd 1.365 W IC Pd 30.978 mW L Pd 1.22 W M1 Pd 1.241 W M1 Pd 1.241 W	BOM Count 17 Total BOM NA Cin IRMS 766.504 mA Capacitor Cin Pd 47.002 mW Capacitor Cout IRMS 5.123 A Capacitor Cout Pd 262.46 mW Capacitor D1 Tj 104.6 degC Diode Diode Pd 1.365 W Diode IC Pd 30.978 mW IC IC Tolerance Pd. 1.365 Mg IC IC Tolerance Pd. 24.3 mV IC IC Tolerance Pd. 26.48 mV Inductor IC Pd. 1.22 W Inductor IC Pd. 1.22 W Inductor IC Pd. 1.22 W Power IC Pd. 1.22 W

#	Name	Value	Category	Description
33.	Rfb Pd	22.657 mW	Power	Rfb Power Dissipation
34.	Rsense Pd	0.0 W	Power	LED Current Rsns Power Dissipation
35.	Total Pd	4.597 W	Power	Total Power Dissipation
36.	Rfb Pd	22.657 mW	Resistor	Rfb Power Dissipation
37.	Rsense Pd	0.0 W	Resistor	LED Current Rsns Power Dissipation
38.	Cross Freq	1.901 kHz	System Information	Bode plot crossover frequency
39.	Duty Cycle	74.355 %	System Information	Duty cycle
40.	Efficiency	92.155 %	System Information	Steady state efficiency
41.	FootPrint	879.0 mm ²	System Information	Total Foot Print Area of BOM components
42.	Frequency	295.118 kHz	System Information	Switching frequency
43.	Gain Marg	-15.708 dB	System Information	Bode Plot Gain Margin
44.	lout	3.0 A	System Information	lout operating point
45.	Low Freq Gain	54.598 dB	System Information	Gain at 1Hz
46.	Mode	CCM	System Information	Conduction Mode
47.	Phase Marg	58.018 deg	System Information	Bode Plot Phase Margin
48.	Pout	54.0 W	System Information	Total output power
49.	SW lpk	13.026 A	System Information	Peak switch current
50.	Vin	5.0 V	System Information	Vin operating point
51.	Vout	18.0 V	System Information	Operational Output Voltage
52.	Vout Actual	18.018 V	System Information	Vout Actual calculated based on selected voltage divider resistors
53.	Vout Tolerance	3.844 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
54.	Vout p-p	149.796 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

9 1			
Name	Value	Description	
lout	3.0	Maximum Output Current	
VinMax	14.0	Maximum input voltage	
VinMin	5.0	Minimum input voltage	
VinTyp	12.0	Typical input voltage	
Vout	18.0	Output Voltage	
base_pn	LM3478	Base Product Number	
source	DC	Input Source Type	
Та	50.0	Ambient temperature	

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 5.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum lout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

- 1. Master key: F9884934EAC7C183[v1]
- 2. LM3478 Product Folder: http://www.ti.com/product/LM3478: contains the data sheet and other resources.

Important Notice and Disclaimer

TI provides technical and reliability data (including datasheets), design resources (including reference designs), application or other design advice, web tools, safety information, and other resources AS IS and with all faults, and disclaims all warranties. These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

Providing these resources does not expand or otherwise alter TI's applicable Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with TI products.