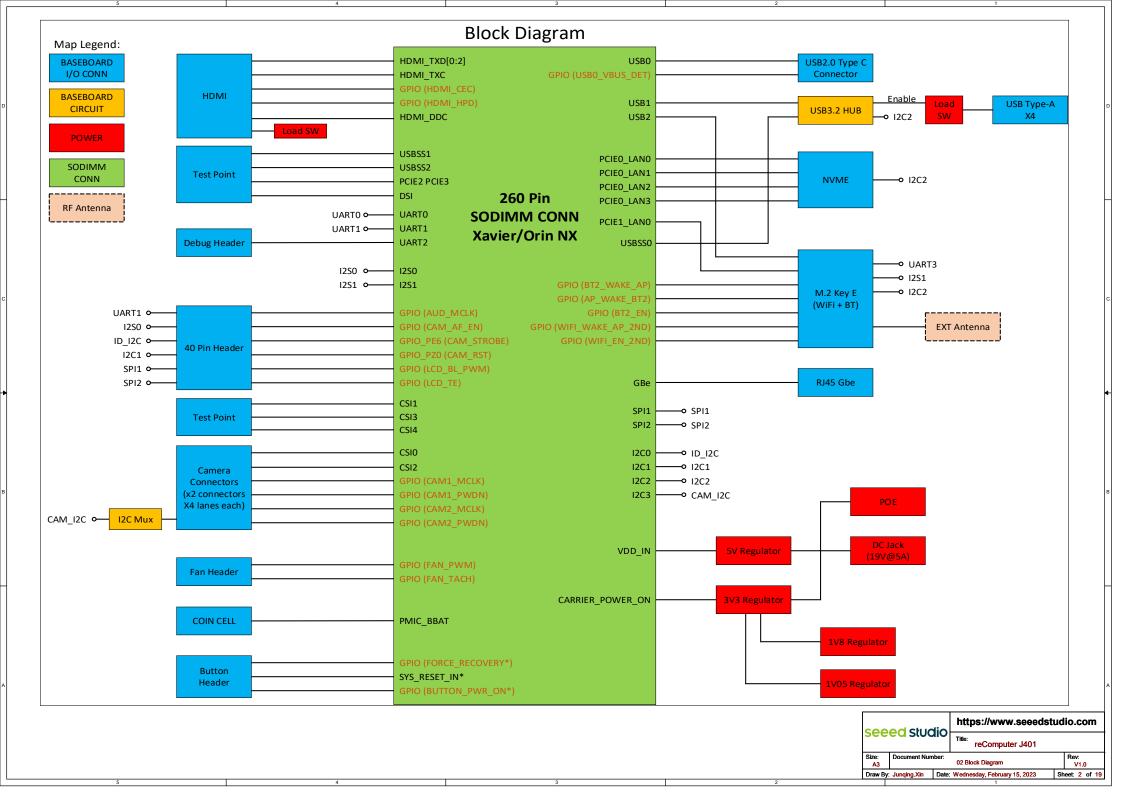
# Schematic: reComputer J401

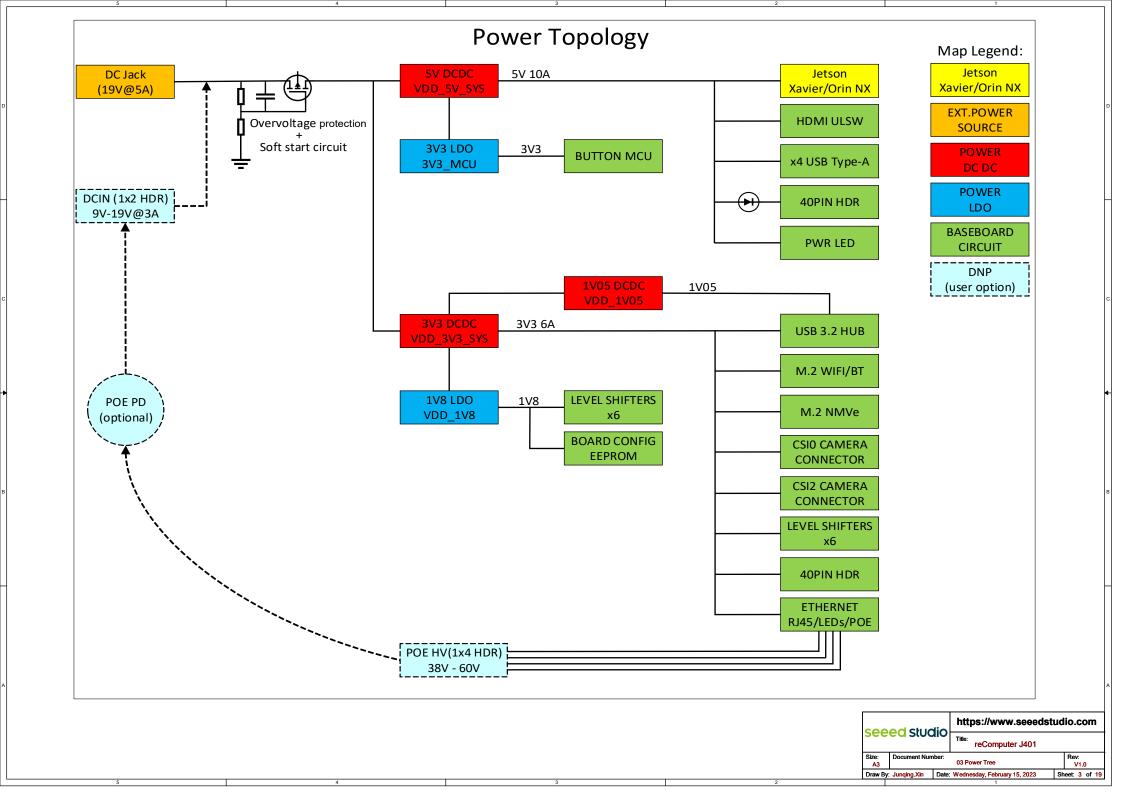
SHEET	SHEET NAME
01	Table of Contents
02	Block Diagram
03	Power Tree
04	SODIMM Connector 1/3
05	SODIMM Connector 2/3
06	SODIMM Connector 3/3
07	Power In & 5V & 3.3V_MCU
08	3.3V & 1.8V & 1.05V
09	Button MCU For Power Up
10	USB3.2 HUB
11	USB3.2 Type-A x2 (A)
12	USB3.2 Type-A x2 (B)
13	HDMI
14	CAM Connector
15	Gigabit Ethernet, Type C
16	M.2 KEY-E (WIFI/BT)
17	M.2 KEY-M (NVME)
18	40Pin GPIO Expansion Header
19	Fan, Debug and EEPROM

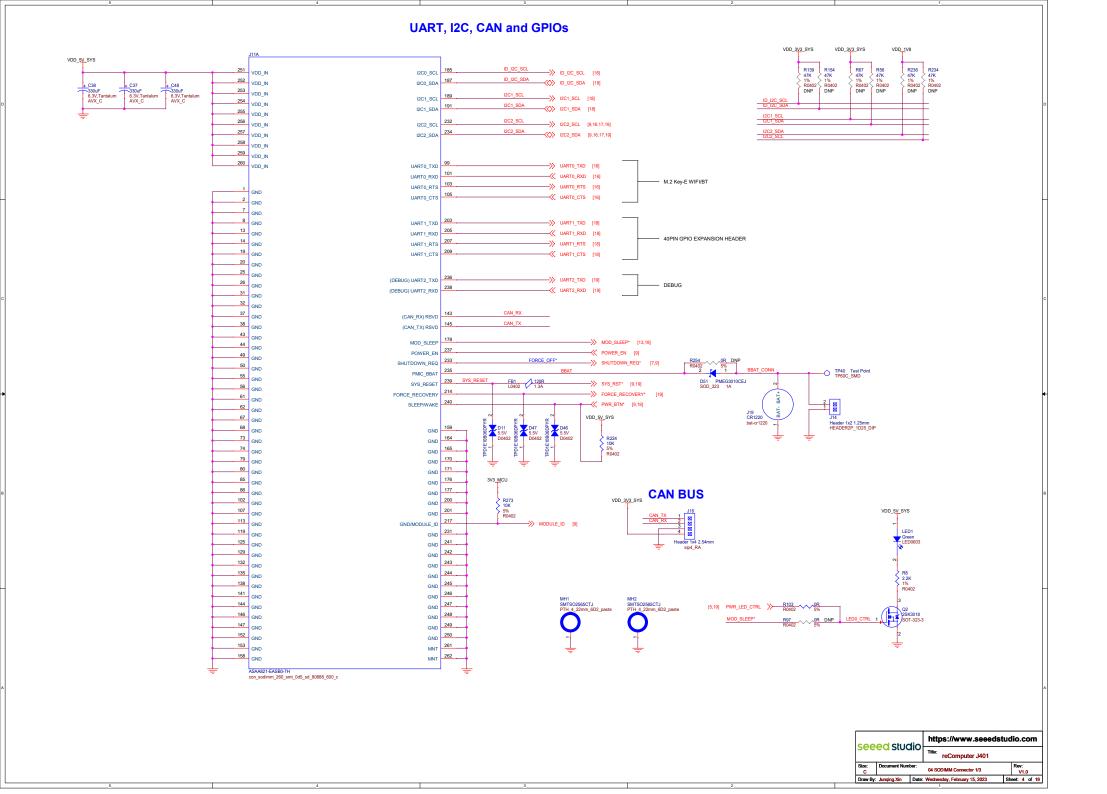
# Revision History

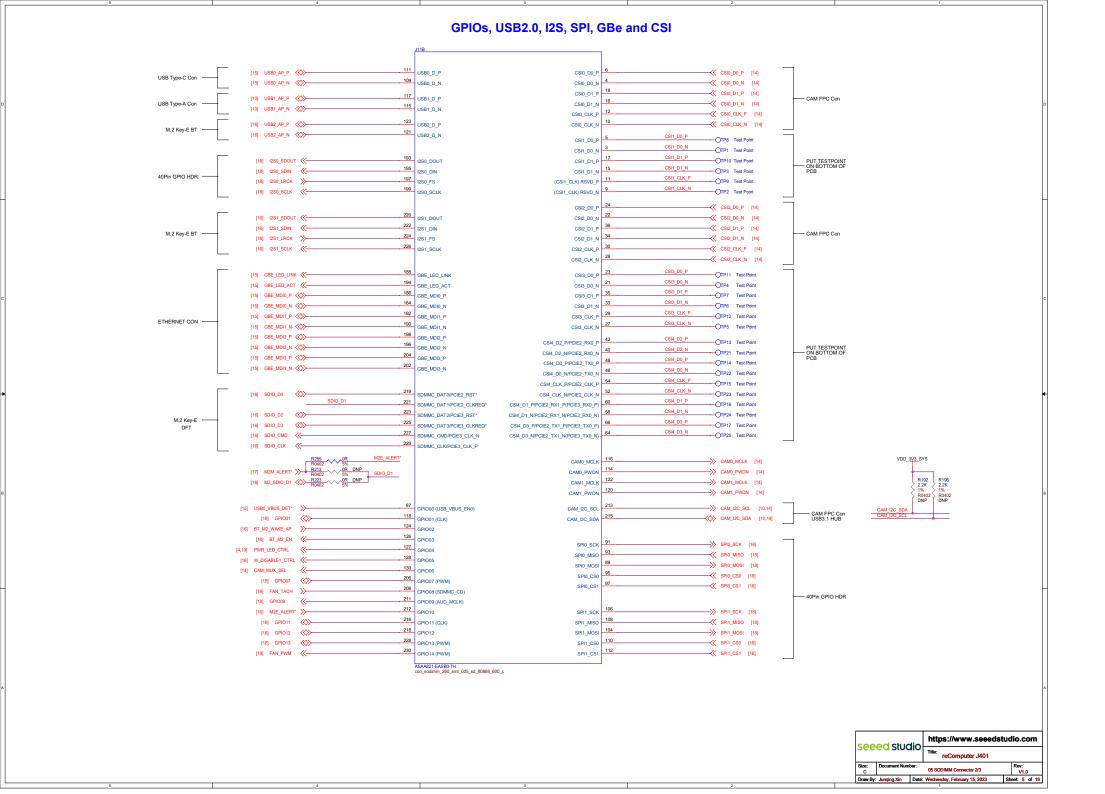
DATE	REVISION	DESCRIPTION		
01/05/2023	reComputer J401_V1.0_SCH_230105	Initial Release.		



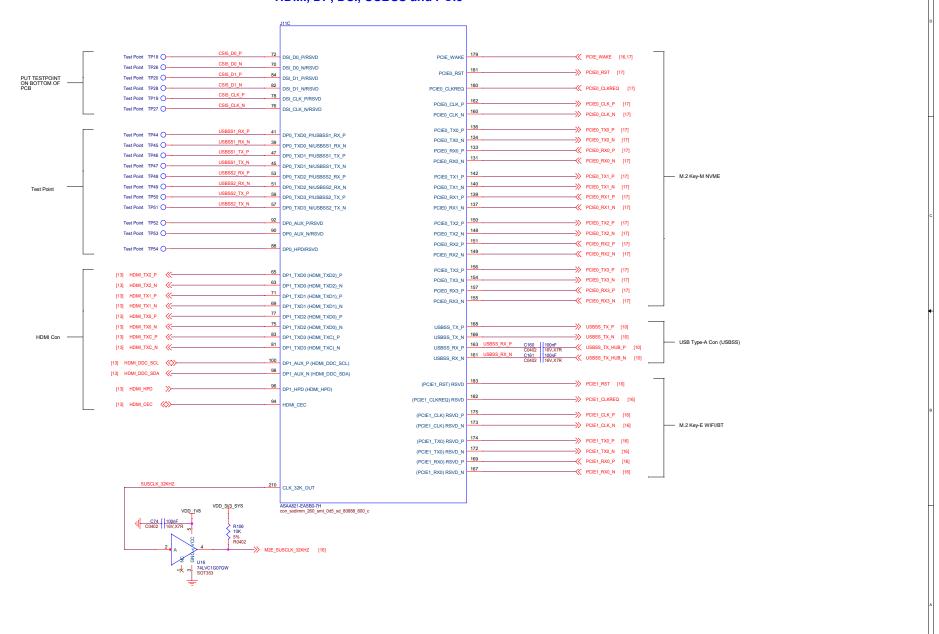


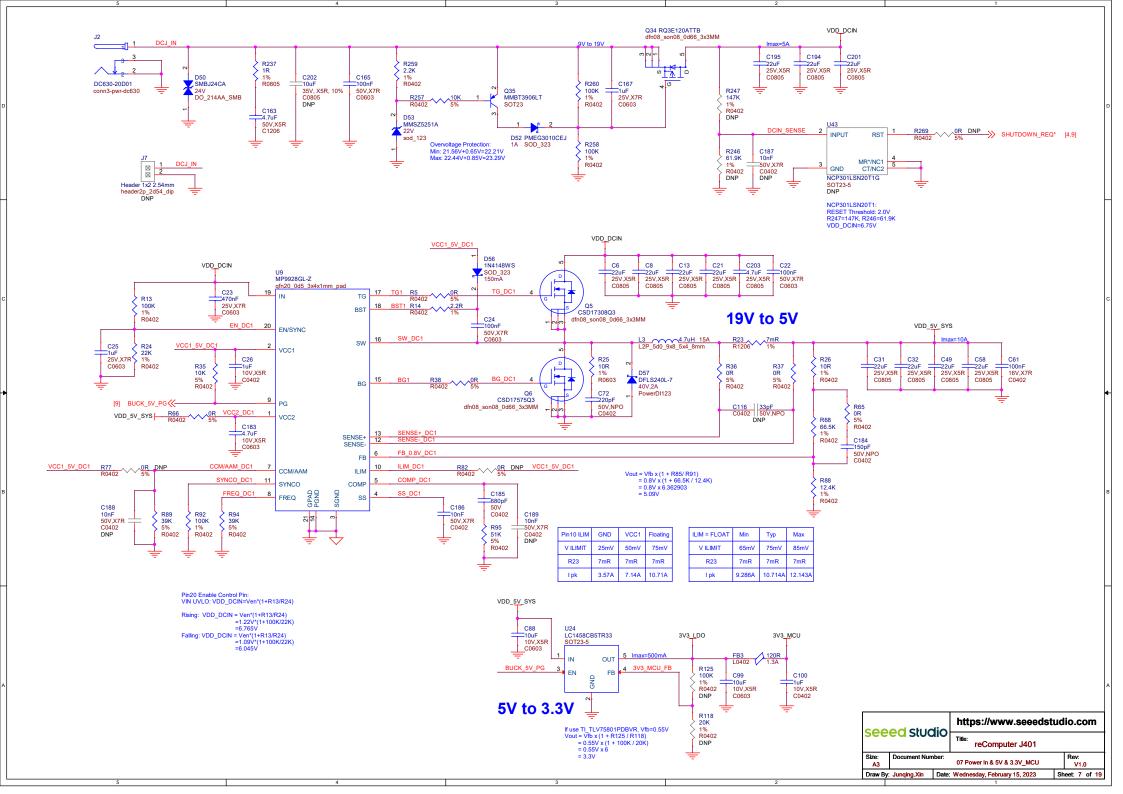


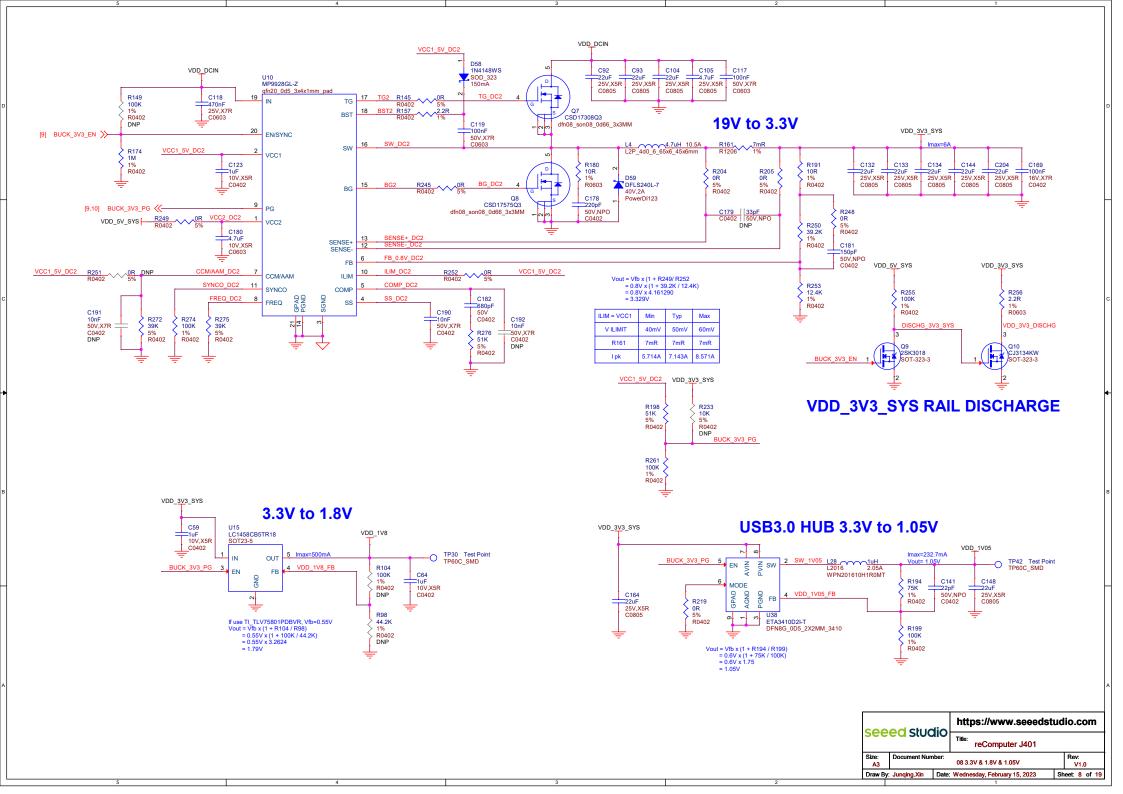


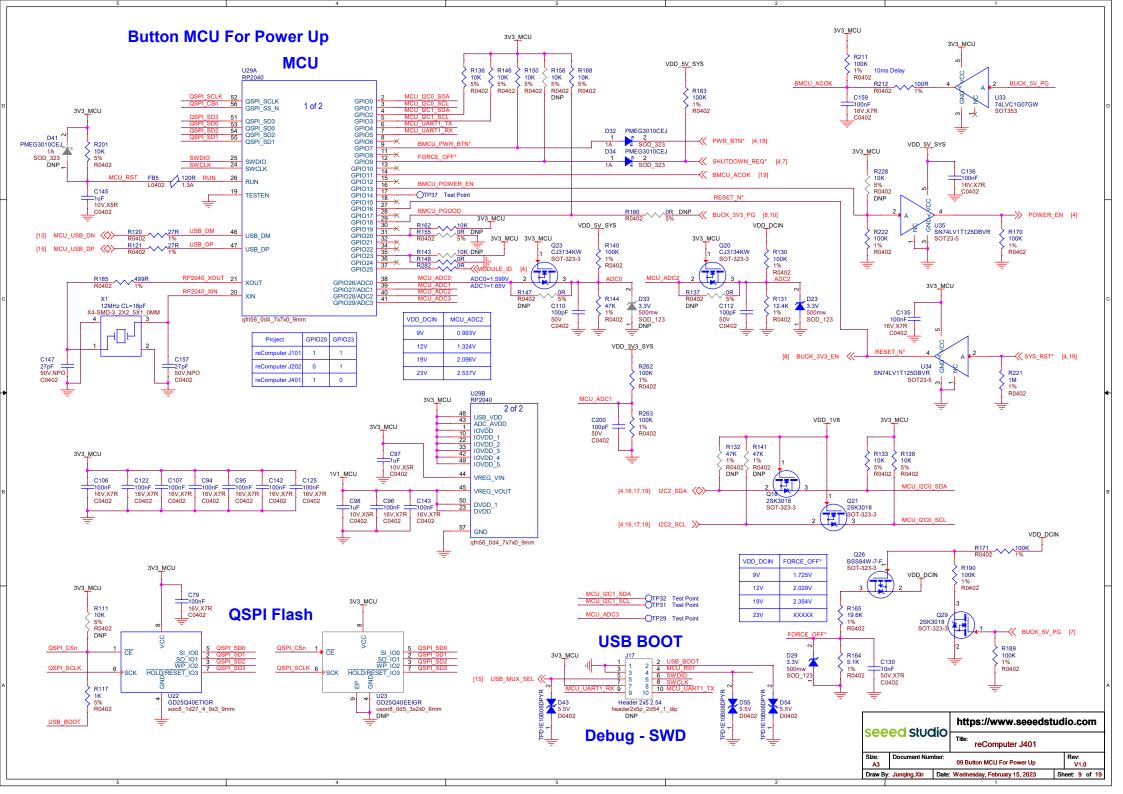


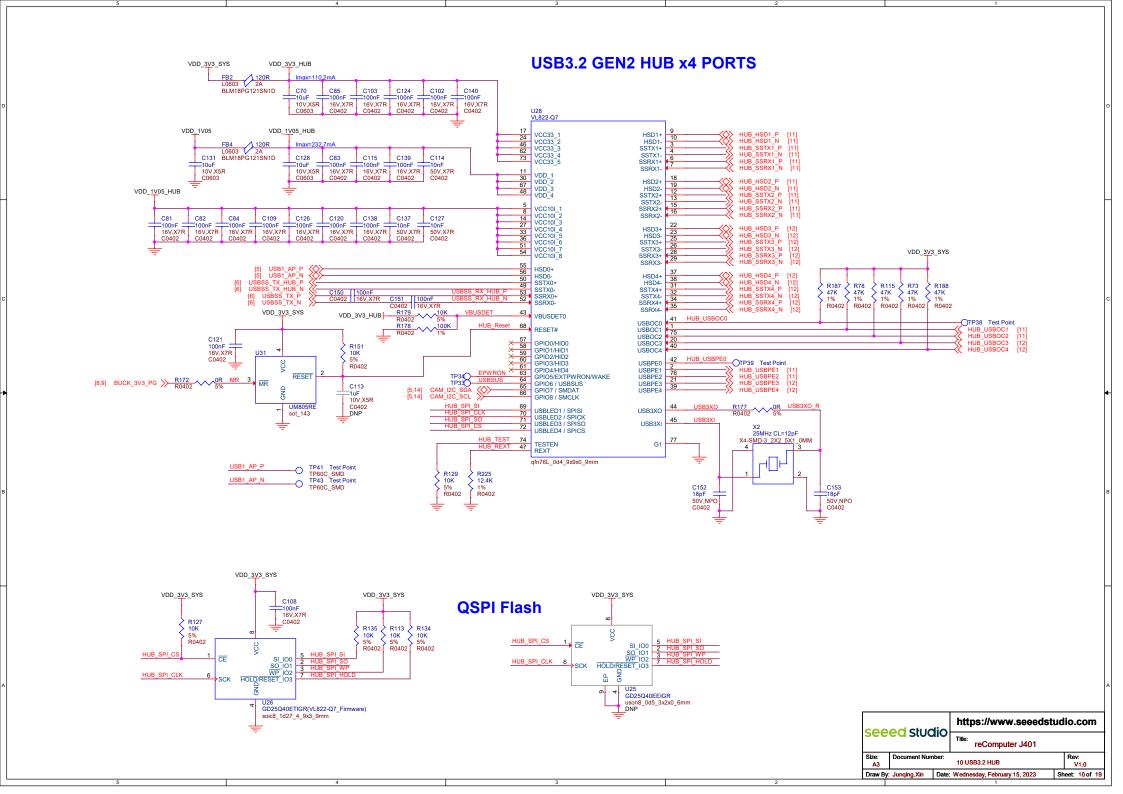
#### HDMI, DP, DSI, USBSS and PCIe

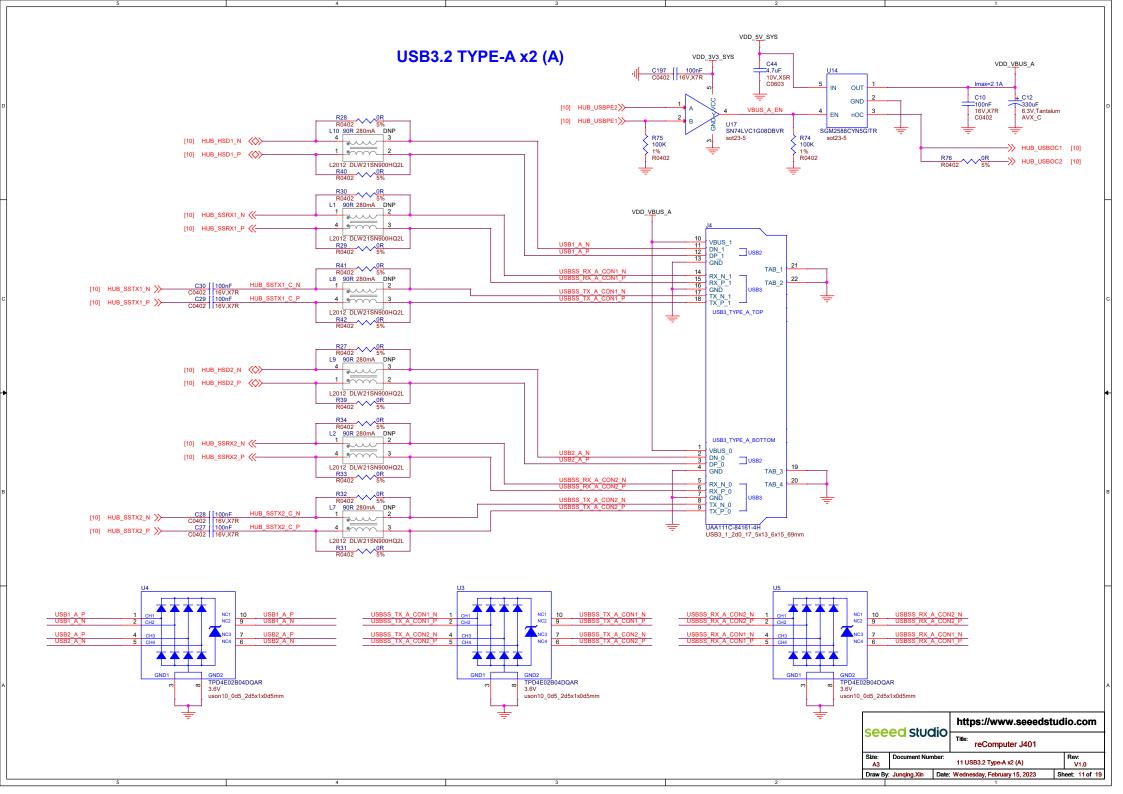


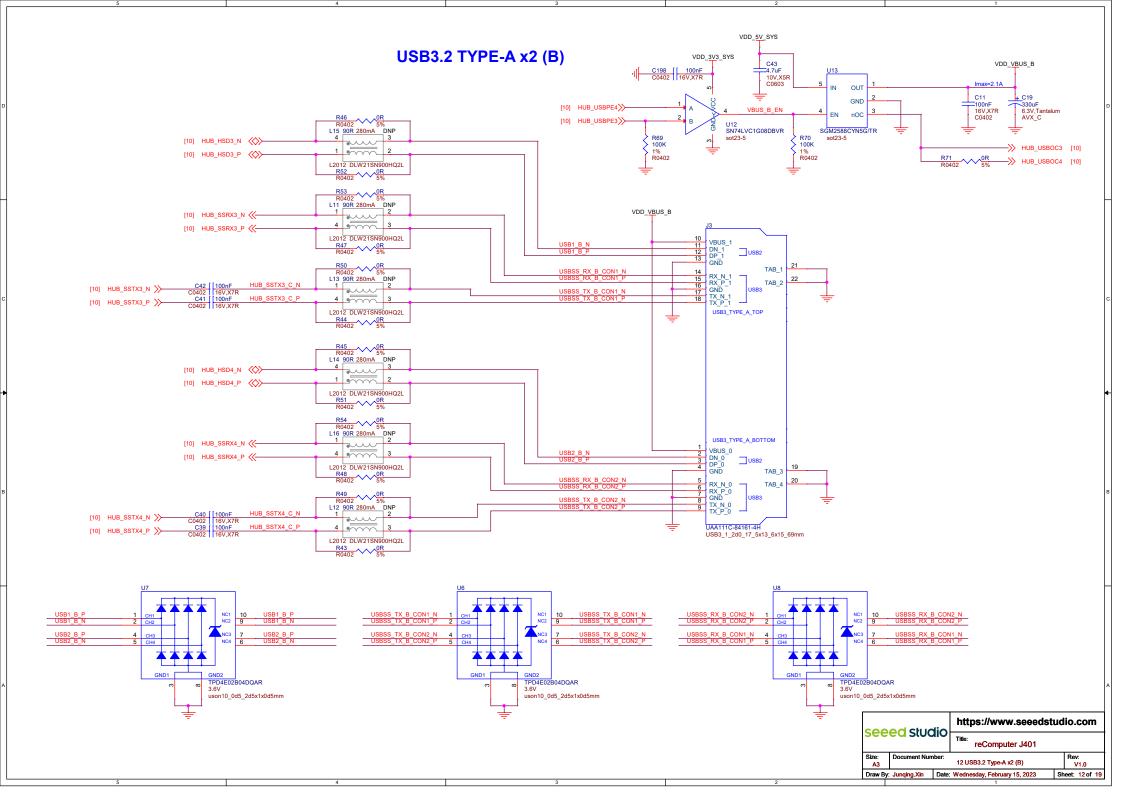


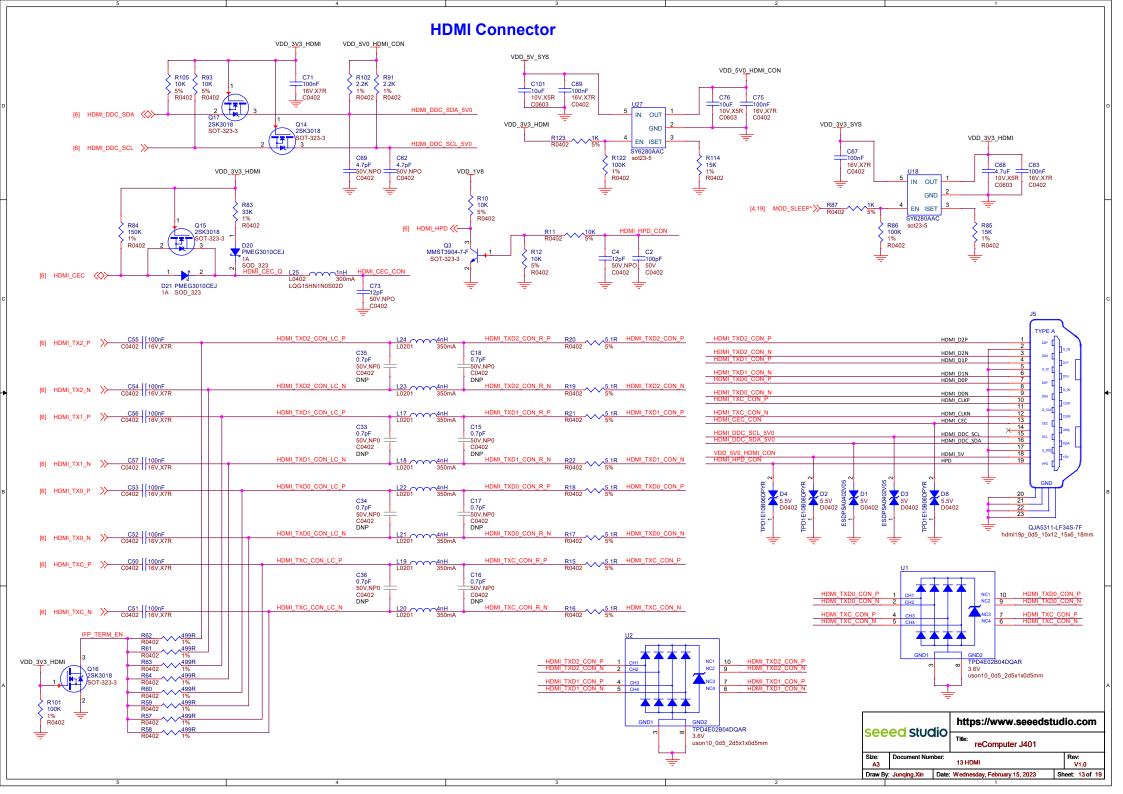


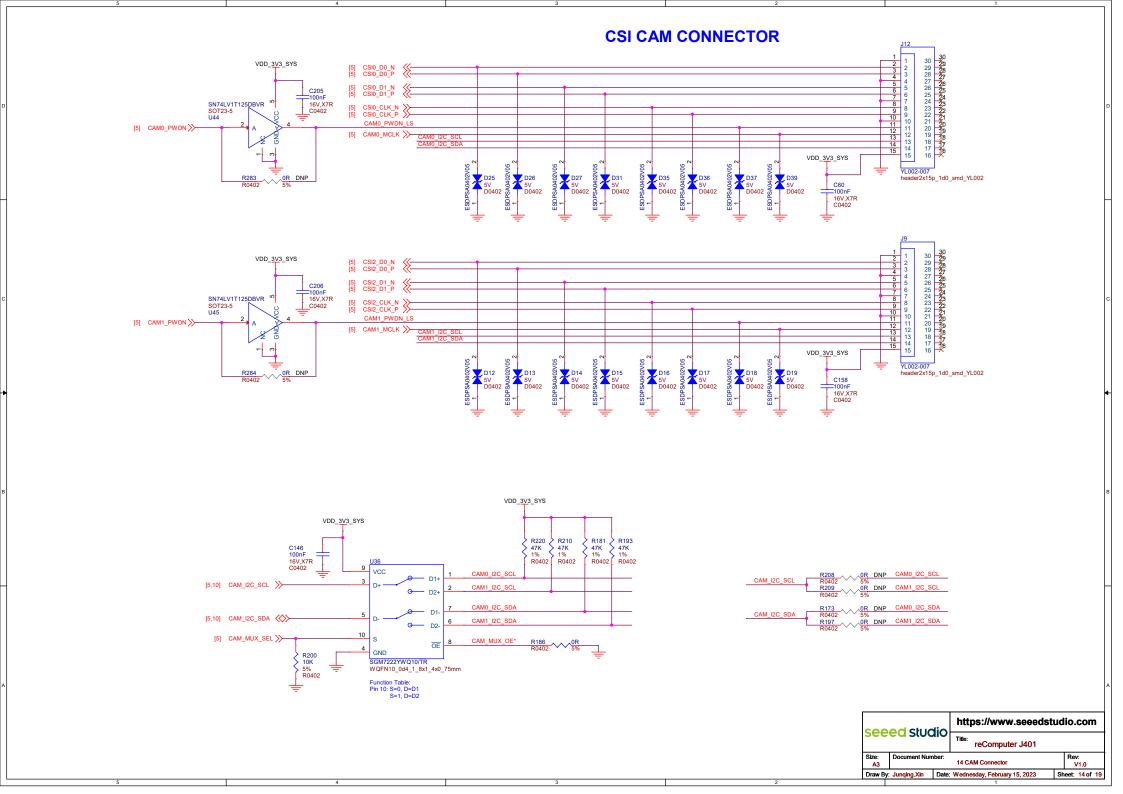


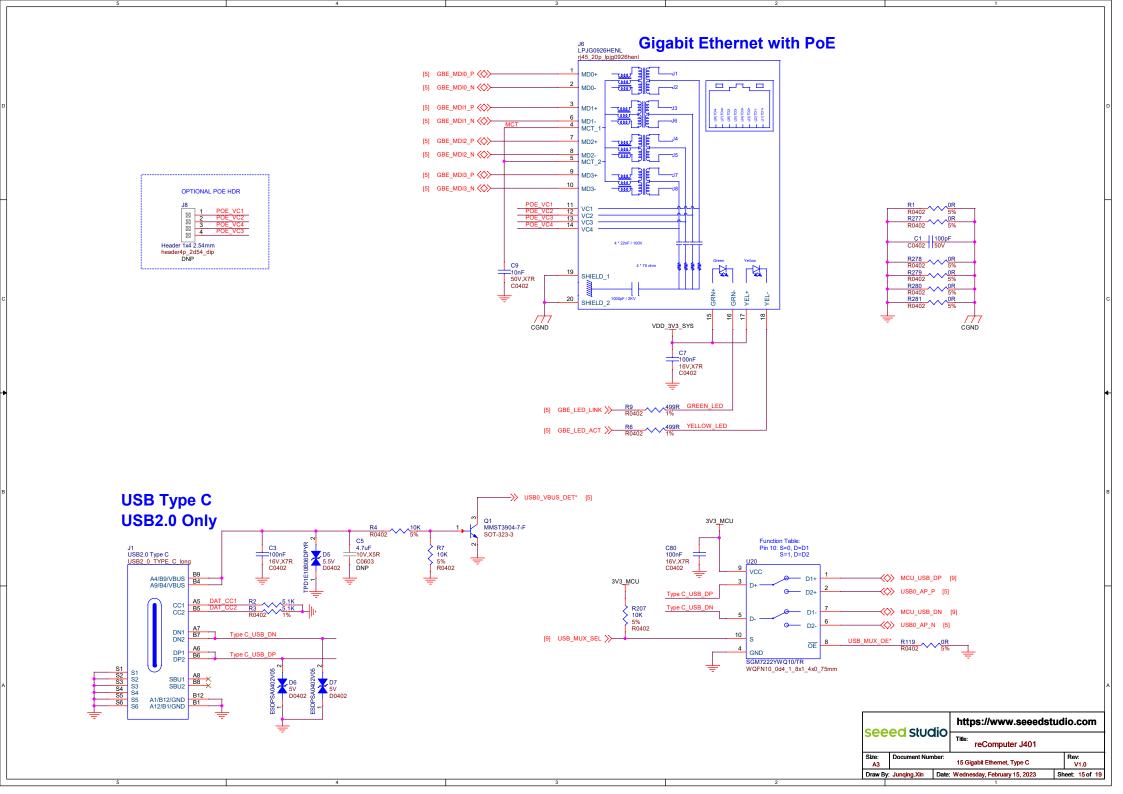




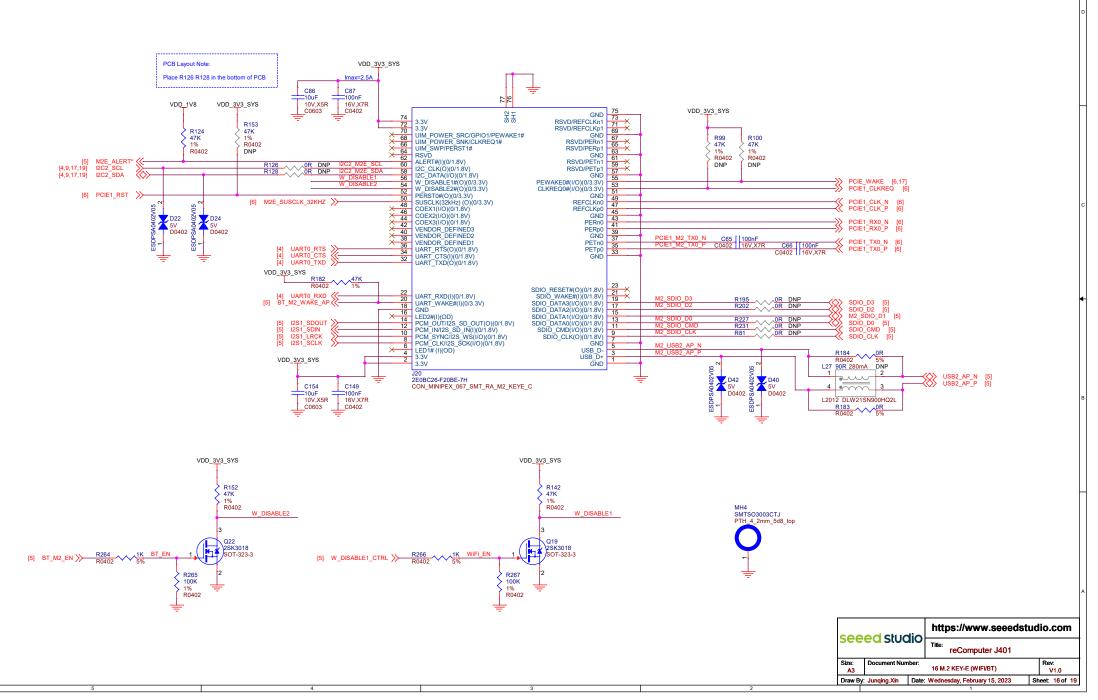




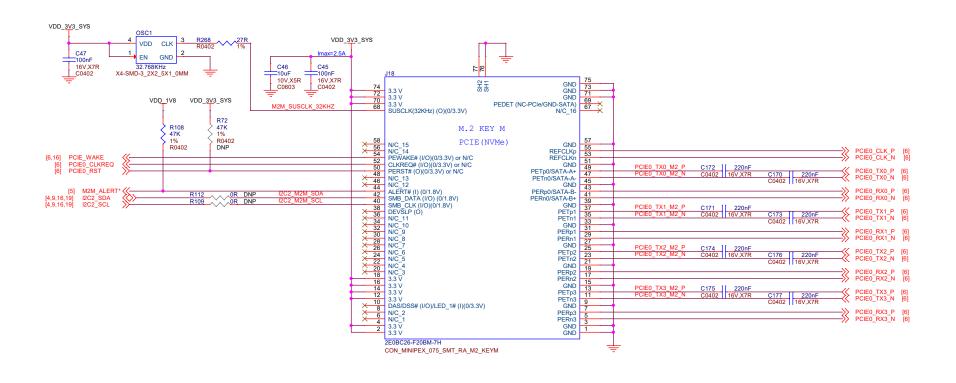




### M.2 KEY-E (WIFI/BT)



## M.2 KEY-M (NVME)





500	seeed studio		https://www.seeedstudio.com		
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Size: A3	Document Num	nber:	17 M.2 KEY-M (NVME)		Rev: V1.0
Draw By	Draw By: Junqing.Xin Date:		: Wednesday, February 15, 2023 She		eet: 17 of 19
			1		

#### **40PIN GPIO EXPANSION HEADER & LEVEL SHIFTERS** VDD\_3V3\_SYS R116 > 100K > 1% R0402 VDD 1V8 VDD\_3V3\_SYS VDD 1V8 VDD\_3V3\_SYS R169 100K 1% R0402 U21 U19 C78 | 100nF C0402 | 16V,X7R VCCA VCCA VCCB VCCB SPIO\_SCK SPIO\_MISO SPIO\_MOSI SPIO\_CS0 SPIO\_CS1 GPIO07 GPIO12 SPI1\_SCK SPI1\_MISO SPI1\_MOSI SPI1\_CS0 SPI1\_CS1 B1 B2 B1 B2 B3 B4 B5 B6 B7 B8 A2 A3 A4 A5 A6 4 A3 5 A4 7 A5 8 A6 B3 B4 B5 B6 B7 B8 A7 A8 9 A7 A8 10 OE 10 OE VDD\_1V8 VDD\_1V8 GND GPAD GND GPAD R110 TXB0108RGYR TXB0108RGYE VQFN-20 VQFN-20 VDD\_3V3\_SYS VDD\_5V\_GPIO C14 C20 100nF 16V,X7R C0402 VDD\_1V8 VDD\_3V3\_SYS 100nF 16V X7R VCCA VCCB 5V\_1 5V\_2 GND\_8 3V3 1 I2S0\_SCLK I2S0\_SDOUT I2S0\_SDIN I2S0\_LRCK GPI009 GPI013 GPI011 B1 B2 B3 B4 B5 GPIO2/SDA1 GPIO3/SCL1 UART1\_TXD\_LS UART1\_RXD\_LS I2S0\_SCLK\_LS A2 A3 A4 A5 A6 A7 GPIO4/GCKL TXD0/GPIO14 UART1 RTS LS GND\_1 GPIO17/GEN0 RXD0/GPIO15 GPIO18 B6 B7 GPIO27/GEN2 GPIO22/GEN3 GND\_7 GEN4/GPIO23 SPI1\_CS1\_LS 3V3\_2 GPIO10/MOSI GEN5/GPIO24 GND 6 19 21 23 25 27 29 31 33 35 37 GEN6/GPIO25 CE0/GPIO8 CE1/GPIO7 OE GPIO9/MISO GPIO11/SCLK VDD\_1V8 GPIO11/ GND\_2 ID\_SD GPIO5 GPIO6 GPIO13 GND GPAD ID\_I2C\_SDA ID\_SC GND\_5 GPIO07 LS TXB0108RGYR VQFN-20 GPIO12 GND\_4 UART1\_CTS\_LS I2S0\_SDIN\_LS I2S0\_SDOUT\_LS GPIO19 GPIO26 GPIO16 GPIO20 GND\_3 GPIO21 Header 2x20 2.54mm header2x20p\_2d54\_dip IDEAL DIODE USED TO BLOCK BACK POWER MAX: 1A VDD\_5V\_SYS VDD\_5V\_GPIO Q4 CJ2101 [4] ID\_I2C\_SDA ( ) [4] ID\_I2C\_SCL Q11A BC856S SOT363 BC856S SOT363 EN\_PWR\_PATH <sup>©</sup> D28 5V D0402 R80 100K 1% R0402 R79 100K 1% R0402 https://www.seeedstudio.com seeed studio reComputer J401 18 40Pin GPIO Expansion Header V1.0 Sheet: 18 of 19 Draw By: Junqing.Xin Date: Wednesday, February 15, 2023

