



Testplan Design DesUnits

- + ram_tb_top
- + ram_pkg
- + ram_tb_top_sv_unit (no cover)

Questa Coverage Report

Number of tests run:	4
Passed:	3
Warning:	0
Error:	1
Fatal:	0

[List of tests included in report...](#)

Coverage Summary by Structure:

Design Scope ◀	Coverage (%) ◀
ram_tb_top	96.77%
inf	98.21%
DUT	94.44%
ram_pkg	94.22%
ram_sb	100.00%

Coverage Summary by Type:

Weighted Average:				94.05%
Coverage Type ◀	Bins ◀	Hits ◀	Misses ◀	Coverage (%) ◀
Covergroup	11	11	0	100.00%
Statement	178	172	6	96.62%
Branch	42	35	7	83.33%
FEC Condition	14	14	0	100.00%
Toggle	124	112	12	90.32%

Report generated by [Questa](#) on Sunday 28 May 2023 13:00:22