

HPS

FPGA

GIC

IRQ

H2F LW

Mailbox
NIOS2ARM

Shared Memory
Master NIOS
2kB

Mutex
Master NIOS

Mailbox
ARM2NIOS

Shared Memory
Master ARM
2kB

Mutex
Master ARM

HPS
CPU1&
CPU2
etc

F2H

H2F

Avalon Bus

NIOS2

IRQ

