

Department Of Electronics and Communications, NIT Srinagar

Embe-Sys

Description:

Participants in this event will be tested on their understanding of basic embedded systems and programming skill. The basic objective would be to generate an audio signal of desired frequency from the microcontroller board interfaced with external audio speaker.

Format:

Event will consist of three rounds.

1. Round 0 :- This round will be an eliminator round. This round will be MCQ based.
2. Round 1:- This round will be simulator based and will test the designing skill of participants. This round is also an eliminator round.
3. Round 2:- In this round participants will be asked to design an embedded system around AVR ATmega8/ATmega16 series which will be required to perform the desired task, provided at the venue of the competition.
Further details will be revealed on the spot

Rules and regulations:

- Each teams consists at most 2 players.
- Any unfair practices will be dealt with disqualification.

Judging:

Judging will be based on the following factors:

- Team that produce max. tones in given time will be declared winner
- In case of a tie, the better design in terms of operation and component placement will be given priority.

Certificates:

- Merit Certificates to 3 toppers
- Participation Certificates to everyone else

Prize money:

- 1st prize: ₹ 5000
- 2nd prize: ₹ 3000
- 3rd prize: ₹ 2000