

Requirements

Overview

- 32 or 64-bit architecture is recommended for optimal performance.
- RAM and ROM requirements are dependent on the underlying media technology and performance expectations. The strict minimum is 4 KiB but more RAM can be used to improve overall performances. ROM usage is approximately 30 KiB.
- ISO/IEC 9899:1999 (a.k.a C99) compliant compiler.
- Hardware Teensy 3.5
- Arduino framework
- 2.9 inch e-Paper Modul
- Dual targeting strategy
- Testable SOLID design
- Code Coverage
- Portable
- Function coverage

Requirement Specification

1. Set date and time of the RTC of teensy via terminal.
2. Get the date and time of the RTC in the terminal.
3. Program shows the date and time (from the RTC) on display.
4. Use Serial for IO
5. Data in time format (YYYY-MM-DD HH:MM:SS)

Modules/Design

