**AIM:**

*To verify Kirchhoff’s voltage law and Kirchhoff’s current law both theoretically and practically for a given DC circuit.*

**APPARATUS REQUIRED:**

*1. Regulated Power supply ( RPS) (0-30 V)*

*2. Voltmeter (0-30 V) MC 3*

*3. Ammeter ( 0 - 10 mA) MC 3*

*4. Resistors 1 KΩ 3*

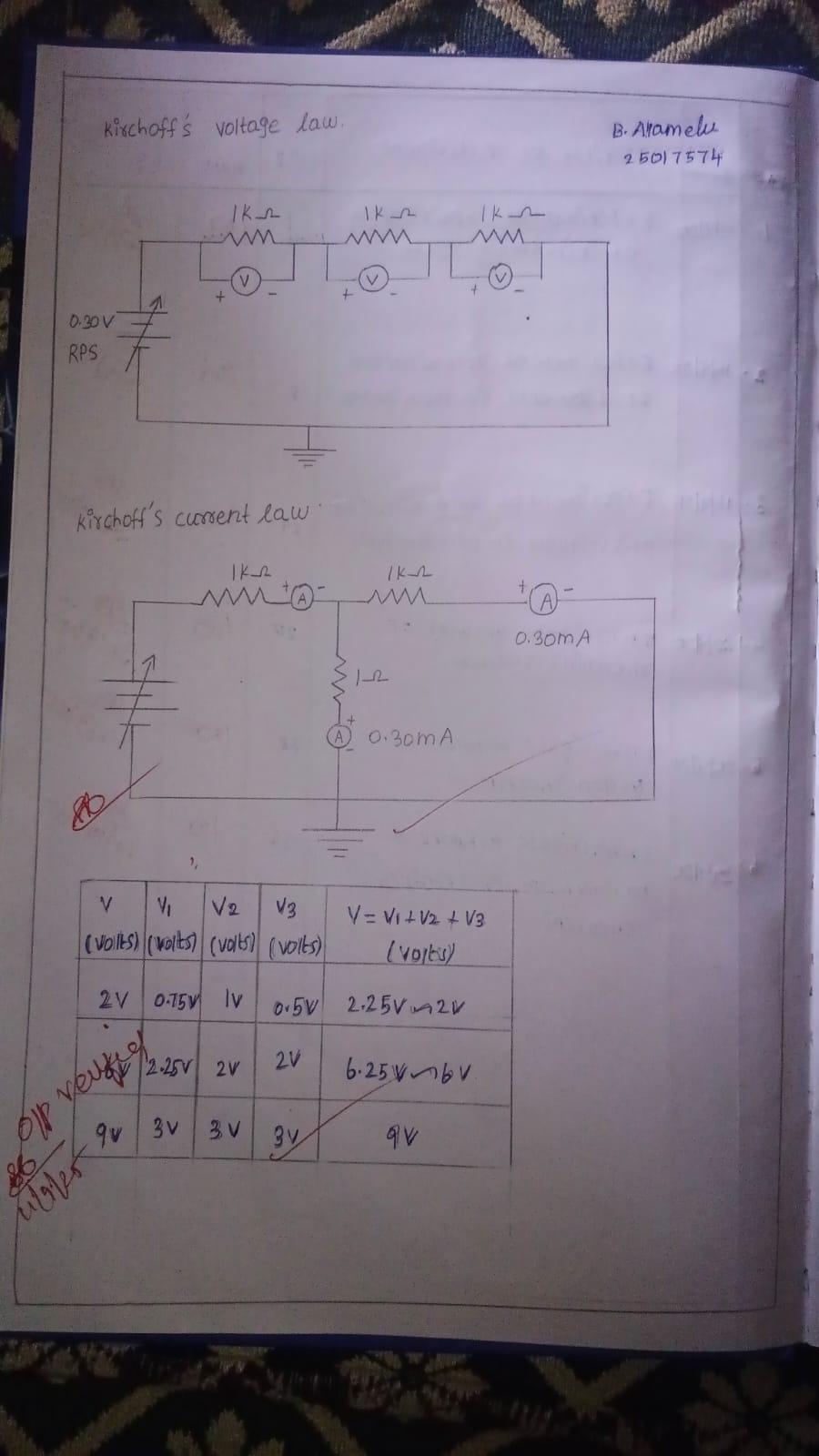
*5. Bread board --- 1*

**THEORY:**

*Kirchhoff’s Current Law (KCL) states that, ‘in an electric network, the algebraic sum of currents meeting at any junction is zero’. Since charges can never accumulate at any point, the charges flowing towards a junction must be equal to the charges flowing away from the junction.*

*Kirchhoff’s Voltage Law (KVL) states that, ‘in an electric network, the algebraic sum of voltages in a closed path is zero’. Since potential at a point is a single value, the sum of all potential drops must be equal when traversing a closed path and coming back to the same point*

**CIRCUIT DIAGRA M:**



**PROCEDURE:**

*1.Give connections as per the circuit diagram.*

*2.Switch ON the supply, vary the RPS (Regulated Power Supply) and set a particular input voltage.*

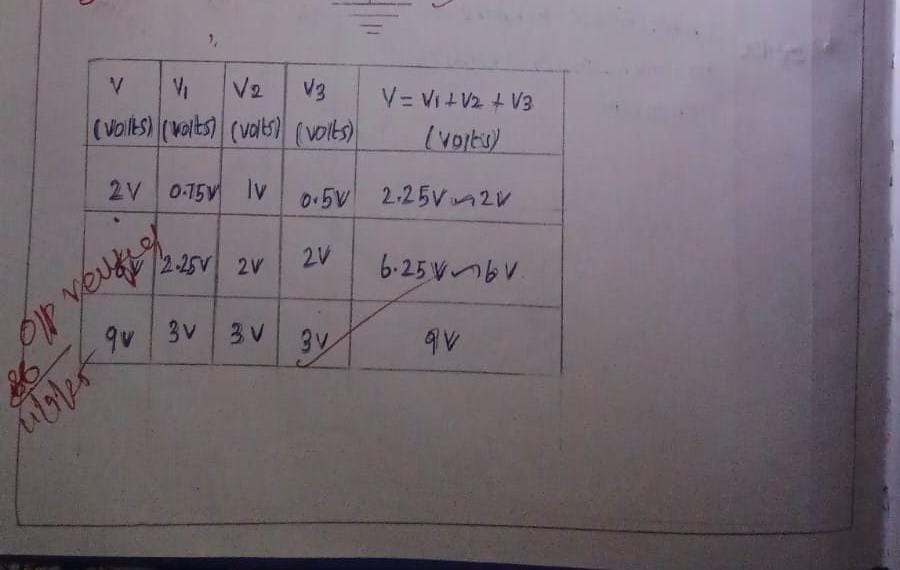
*3.Note down the readings of ammeters and voltmeters and tabulate them.*

*4.Vary the RPS for different input voltages and note down the readings of all the meters.*

*5.Reduce the RPS to its minimum value and switch OFF the supply.*

*6.Using the tabulated values, verify Kirchoff’s laws practically, and verify it theoretically.*

***TABULATION****:*

*v*

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**MARK SPLIT**

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**RESULT:**

*ThusKirchoff’s Current Law and Kirchoff’s Voltage law are verified practically and theoretically.*