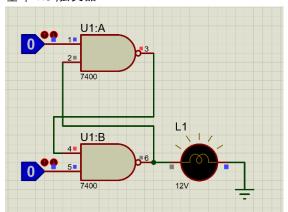
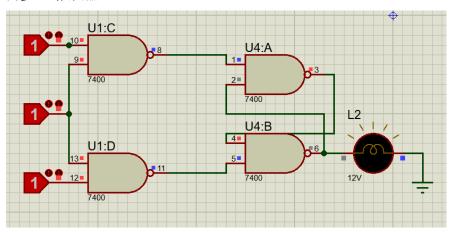
实验 1: 7400 制作 RS 触发器和钟控 RS 触发器基本 RS 触发器:

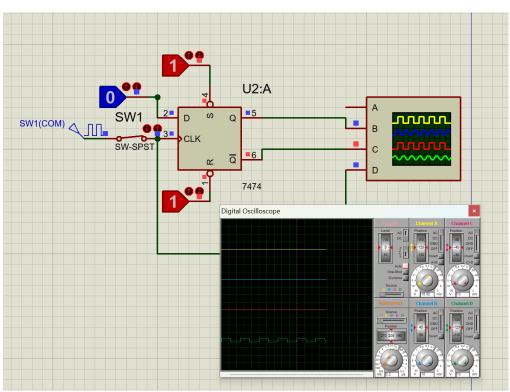


同步 RS 触发器:

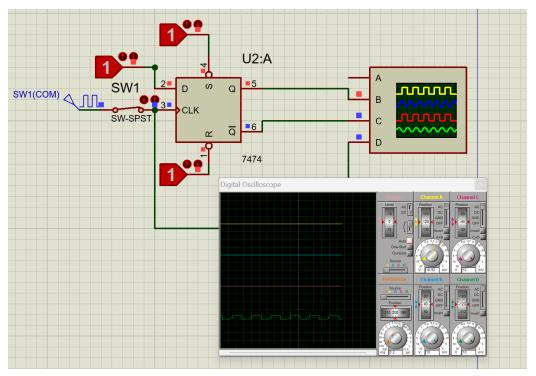


实验 2: D 触发器特点

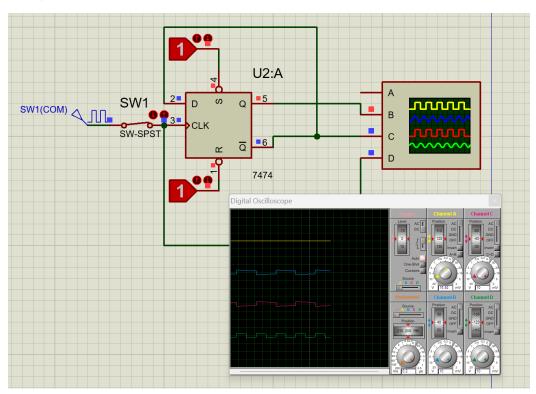
D=0:



D=1:

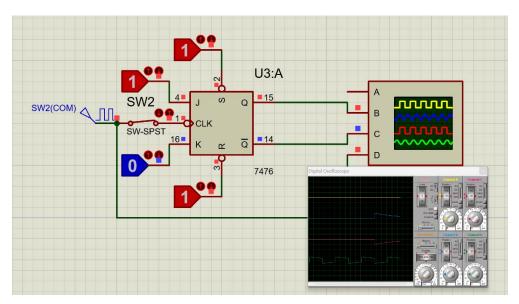


$D = \overline{Q}$:

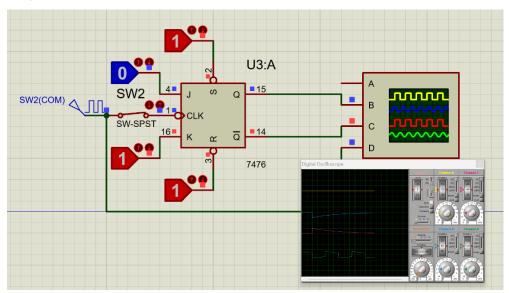


实验 3: J-K 触发器特点:

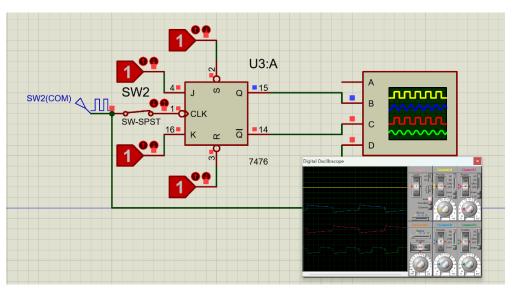
J=1, K=0:



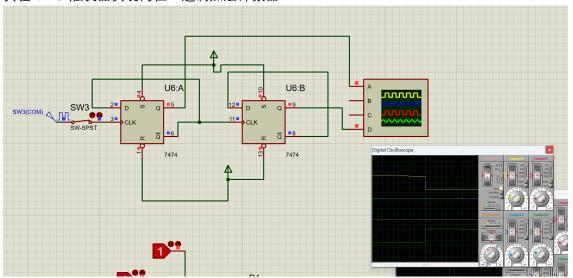
J=0, K=1:



J=1, K=1:

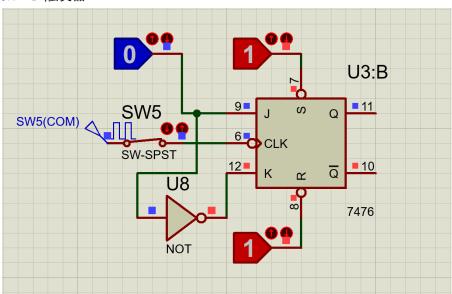


实验 4: D 触发器实现两位二进制加法计数器

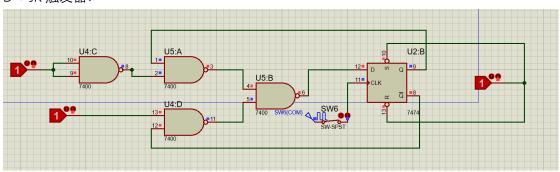


实验 5:

JK->D 触发器:



D->JK 触发器:



实验 6: 单脉冲产生电路

