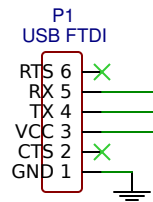
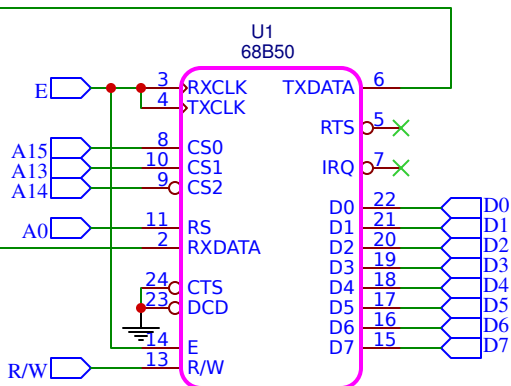


SERIAL CONSOLE 115200 BPS 8N1

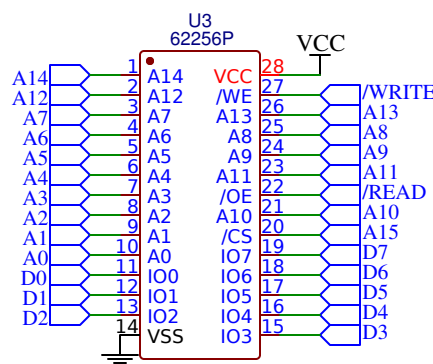


UART

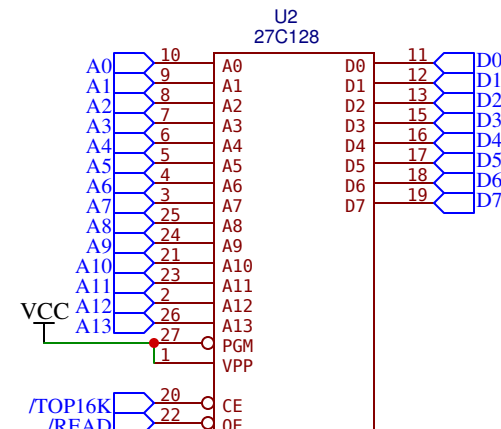


TO DO:
ADD OPTIONAL CIRCUITRY
FOR HARDWARE HANDSHAKING.

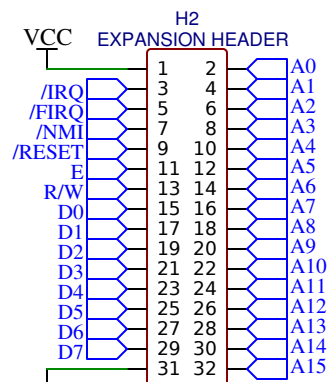
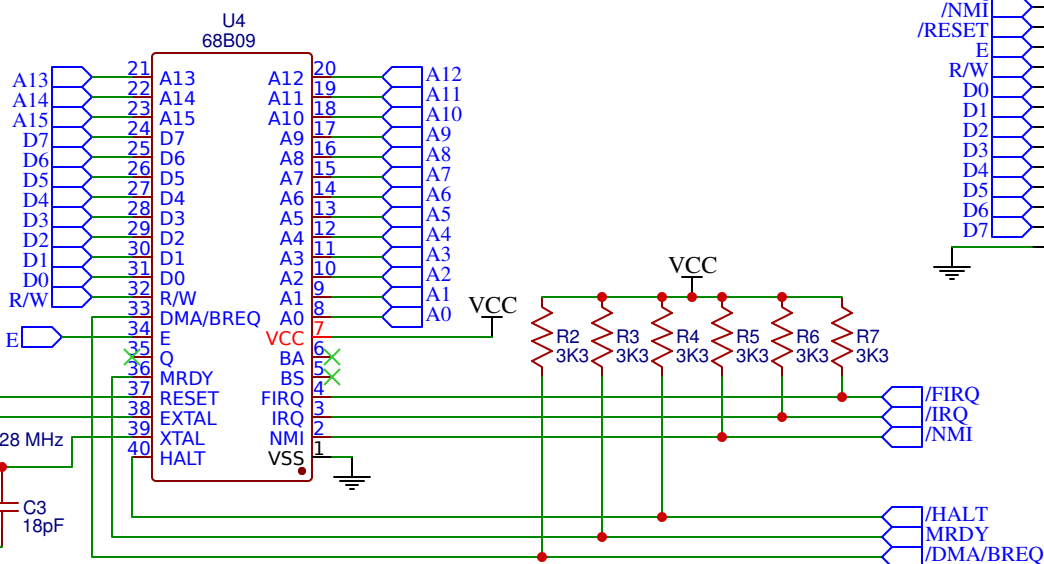
32K RAM



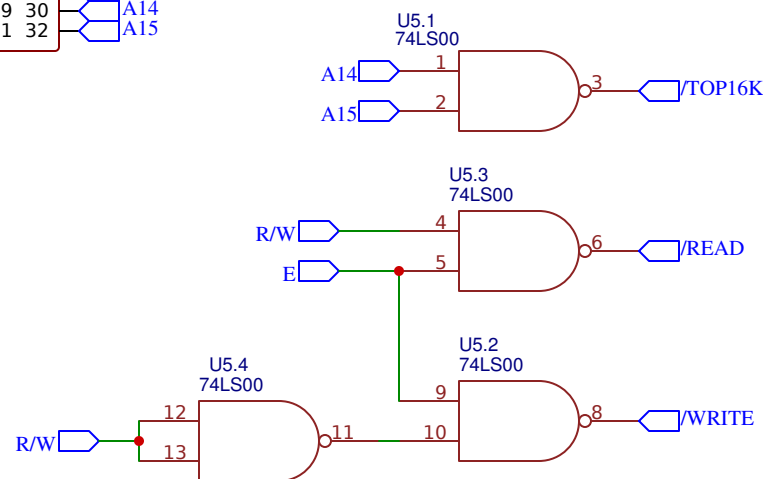
16K ROM



CPU

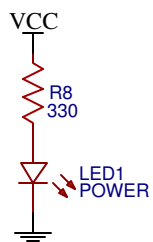
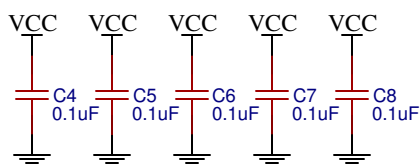


ADDRESS DECODING



MEMORY MAP
0000-7FFF 32K RAM
8000-9FFF UNUSED (8K)
A000-BFFF SERIAL INTERFACE
C000-FFFF 16K ROM

BYPASS CAPS (ONE PER IC)



TITLE: 6809 Single Board Computer		REV: 1.0
Date: 2018-12-24	Sheet: 1/1	
EasyEDA V5.8.22		Drawn By: Jeff Tranter