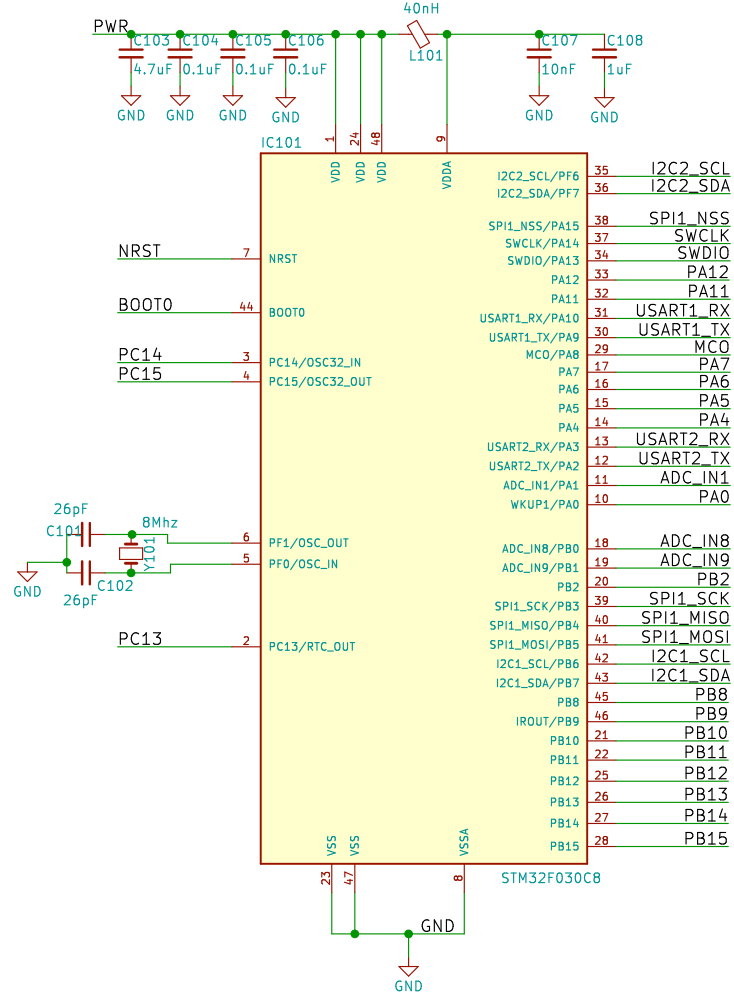


ARM VDD Filtering Requirements: 100nF on each VDD + 4.7uF
ARM VDDA Filtering Requirements: 10nF + 1uF

TODO: check footprints for large value caps
TODO: make real footprint for the inductor
40nH

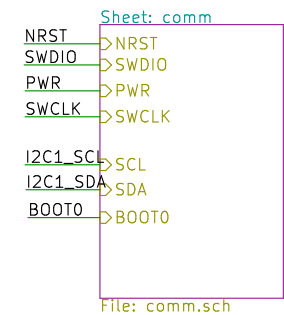
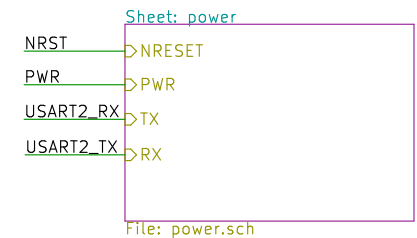
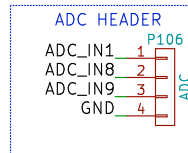
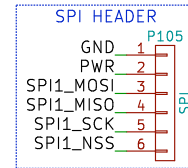
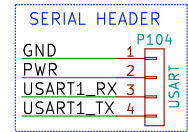
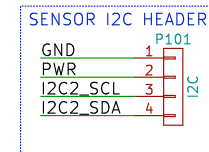
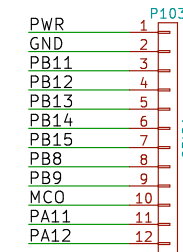
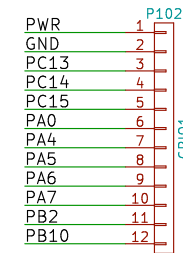


Note: Change of pins

Note: Change of pins

Note: Change of pins

Note: Change of pins



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File: CentralCommand.sch

Title:

Size: A4
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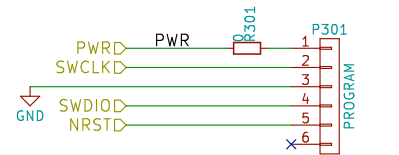
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Rev:
Id: 1/3

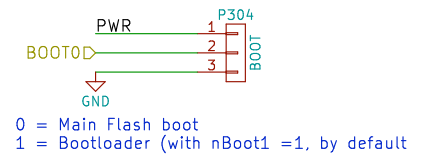
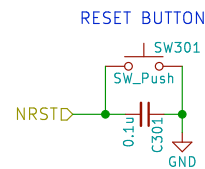
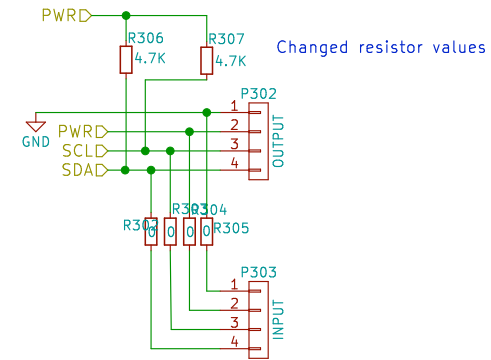
FTDI Power Filtering Requirements: 100nF and 4.7uF
FTDI 3V3 Pin needs 0.1uF cap

Regulator Power Filtering Requirements: 1uF on input and output





NOTE: TRACE is reserved and not connected



Sheet: /comm/
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Id: 3/3