



Rev: D

P/N:

Title: PB500 Powerpack FRU DHR

DEVICE HISTORY RECORD

10140875

10131548		Powerpack assy				
UUT S/N / Hotbed number:						
(Blln #16) Keypad L/C:			REV:			
Keypad Test:		PASS / FAIL				
	DMM Cal ID:					
PCI	BA to DC cable Test:	VDC	PASS / FAIL			
Result for test	t on pins of AC OUT:	PASS / FAIL				
	AC LED:	PASS / FAIL				
Hipot & Lea	akage Current Tests:	PASS / FAIL				
Verify test report has been placed in package:						
Verify Number on packaging carton label matches DHR:						
10131169		FRU PCBA (10153315)				
UUT S/N / Hotbed number:						
(Blln #15) PowerPack PCBA S/N:	S/N:		REV:			
	S/W Version:		Verify PP2A 🗌			
Unit System Test UUT		PASS / FAIL				
10132667		FRU housing cover (10153316)				
(Blln#24) PowerPack ID Label Revision:		REV				
	Quantity:					
10132668		Housing Door(10153317)				
Battery Change Label Rev:		REV:				
	Quantity:					
FRU completed by: Signature:		Date:				
10121549 PRODUCT BELEACE CERTIFICATION						

This device has met the requirements of the PB500 Powerpack Release Authorisation Procedure.

Production Release Personnel:

Name: _____ Date: _____



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DEVICE HISTORY RECORD

Discrepancy Log					
DISCRE	PANCY DETAIL:				
SIGN:			DATE:		
CORRECTIVE ACTION:					
SIGN:			DATE:		
RE-ASSEMBLY COMPLETED BY:					
SIGN:			DATE:		
			•		
Discrepancy Log					
DISCREPANCY DETAIL:					
SIGN:			DATE:		
CORRECTIVE ACTION:					
SIGN:			DATE:		
RE-ASSEMBLY COMPLETED BY:					
SIGN:			DATE:		

Ref Procedure: 10099745