

## ECE 485/585 Project 1 Evaluation Form

**Student Name:** Alan Palayil

SECTIONS		SCORE	
<b>Abstract + Introduction (5)</b>		5	5
<b>Background &amp; Design Descriptions (35)</b>	VHDL Structural & Behavioral Models	5	5
	VHDL Testbench	5	5
	VHDL simulator & environment	5	5
	8-input MUX	10	10
	8-input MUX Testbench	10	10
<b>Simulation Results (30)</b>	Screenshots with descriptions	20	20
	Correctness of the result	10	10
<b>Discussion (15)</b>	Issues Faced, Improvements or others	15	15
<b>Conclusion (5)</b>		5	5
<b>Reference (5)</b>		5	5
<b>Appendix (5)</b>		5	5
<b>TOTAL (100)</b>			100

**Comments:** Excellent work