PORT	AF0	AF1		AF2			AF3			AF4			AF5			AF6			AF7			AF8	AF9		AF10	AF12	ADC		配置
	SYS	TIM1	TIM2	TIM3	TIM4	TIM5	TIM9	TIM10	TIM11	I2C1	I2C2	I2C3	SPI1	SPI2	SPI4	SPI3	SPI4	SPI5	SPI3	USART1	USART2	USART6	I2C2	I2C3	OTG_FS	SDIO	ADC		
PA 0			CH1/ETR			CH1															CTS						ADC1_IN0	O	EA0
1			CH2			CH2															RTS						ADC1_IN1	O	EA1
2			CH3			CH3	CH1														TX						ADC1_IN2	O	EA2
3			CH4			CH4	CH2														RX						ADC1_IN3	O	EA3
4													NSS			NSS					CK						ADC1_IN4	O	IMU_CSM
5			CH1/ETR										SCK														ADC1_IN5	O	IMU_SCK
6		BKIN		CH1									MISO														ADC1_IN6	O	IMU_SDO
7		CH1N		CH2									MOSI														ADC1_IN7	O	IMU_SDI
8	MCO1	CH1										SCL								CK					SOF	D1		O	EA8
9		CH2										SMBA								TX					VBUS	D2		O	EA9
10		CH3																MOSI		RX					ID			O	EA10
11		CH4															MISO			CTS		TX			DM			X	USB_DM
12		ETR																MISO		RTS		RX			DP			X	USB_DP
13	SDIO																											O	SWDIO
14	SCLK																											O	SWCLK
15	JTDI		CH1/ETR										NSS			NSS				TX								O	EA15
PB 0		CH2N		CH3														SCK									ADC1_IN8	O	EB0
1		CH3N		CH4														NSS									ADC1_IN9	O	EB1
2	BOOT																											O	KEY
3	JTDO		CH2										SCK			SCK				RX			SDA					O	EB3
4	JTRST			CH1									MISO			MISO								SDA				O	EB4
5				CH2									MOSI			MOSI										D3		O	EB5
6					CH1					SCL										TX								O	EB6
7					CH2					SDA										RX						D0		O	EB7
8					CH3			CH1		SCL								MOSI						SDA		D4		O	EB8
9					CH4				CH1	SDA				NSS									SDA			D5		O	EB9
10			CH3								SCL			SCK												D7		O	IMU_CSB
12		BKIN									SMBA			NSS			NSS		SCK									O	EP12
13		CH1N												SCK			SCK											O	EB13
14		CH2N												MISO												D6		O	EB14
15	RTC	CH3N												MOSI												CK		O	EB15
PC 13																												O	LED_R
14																												O	LED_G
15																												O	LED_B