

Alarik Damrow

Dr. Rezaei

CECS 301 Tues/Thurs 2:00PM – 4:30PM

5 November 2021

Lab4 Write Up

I certify that this submission is my original work

X *Alarik Damrow*

This lab was straight forward to do and allowed me to work with a situation that deals with bus masters. I did have one issue with cathodes and anodes in which I resolved by messing around with the 7 segment display properties from the file that deals with the board. Other than that, the board ran fine and the simulation was great for what was expected out of this lab.









