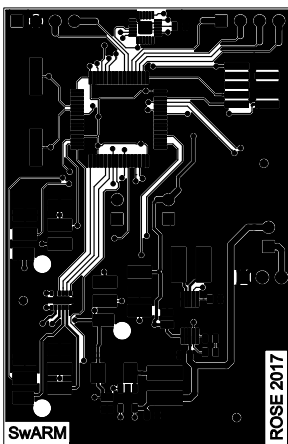
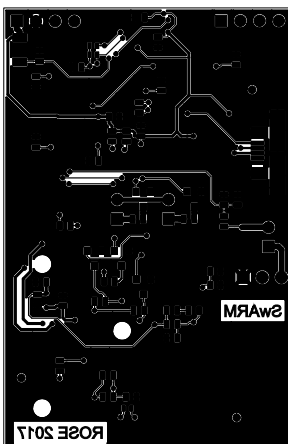


Top layer



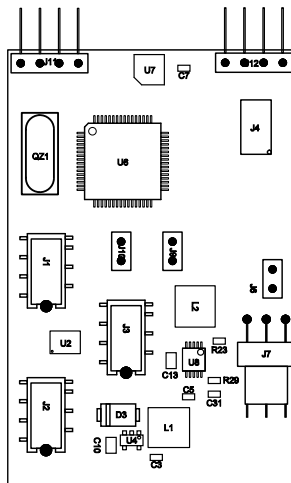
PCB

Bottom Layer



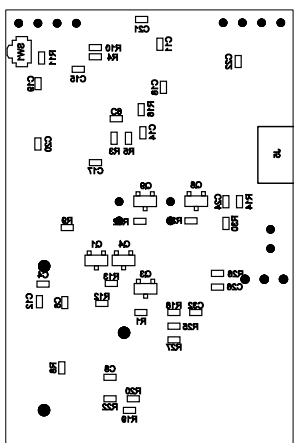
PCB

Assembly Top



PCB

## Assembly Bottom



PCB