

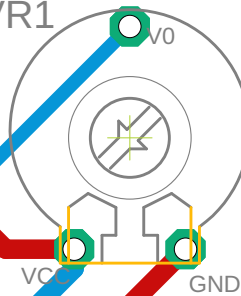
Analog In
5 4 3 2 1 0

Vin Gnd 5v 3v RST

VR1



LCD Display Project
Albert Joudi
Sheridan College



VSS VDD V0 RS RW E DB0 DB1 DB2 DB3 DB4 DB5 DB6 DB7 A K

0 1 2 3 4 5 6 7 8 9 10 11 12 13 gnd ARef
Digital I/O Digital I/O

