

Static Segment LCD Code Example

Objective

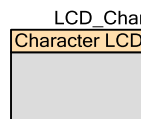
This example shows the use of the Static Segment LCD in Sleep mode. It shows operation of 7 Segment helper.

Schematic

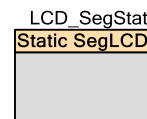
Static Seg LCD pin assignment.
(Zoom In for better visibility)

Name	Pin
\LCD_SegStat: Com\	P0[4]
\LCD_SegStat: Seg\[31]	P3[0]
\LCD_SegStat: Seg\[30]	P6[7]
\LCD_SegStat: Seg\[29]	P6[6]
\LCD_SegStat: Seg\[28]	P6[5]
\LCD_SegStat: Seg\[27]	P6[4]
\LCD_SegStat: Seg\[26]	P6[3]
\LCD_SegStat: Seg\[25]	P6[2]
\LCD_SegStat: Seg\[24]	P6[1]
\LCD_SegStat: Seg\[23]	P6[0]
\LCD_SegStat: Seg\[22]	P4[7]
\LCD_SegStat: Seg\[21]	P4[6]
\LCD_SegStat: Seg\[20]	P4[5]
\LCD_SegStat: Seg\[19]	P4[4]
\LCD_SegStat: Seg\[18]	P4[3]
\LCD_SegStat: Seg\[17]	P4[2]
\LCD_SegStat: Seg\[16]	P4[1]
\LCD_SegStat: Seg\[15]	P4[0]
\LCD_SegStat: Seg\[14]	P5[7]
\LCD_SegStat: Seg\[13]	P5[6]
\LCD_SegStat: Seg\[12]	P5[5]
\LCD_SegStat: Seg\[11]	P5[4]
\LCD_SegStat: Seg\[10]	P5[3]
\LCD_SegStat: Seg\[9]	P5[2]
\LCD_SegStat: Seg\[8]	P5[1]
\LCD_SegStat: Seg\[7]	P5[0]
\LCD_SegStat: Seg\[6]	P3[7]
\LCD_SegStat: Seg\[5]	P3[6]
\LCD_SegStat: Seg\[4]	P3[5]
\LCD_SegStat: Seg\[3]	P3[4]
\LCD_SegStat: Seg\[2]	P3[3]
\LCD_SegStat: Seg\[1]	P3[2]
\LCD_SegStat: Seg\[0]	P3[1]

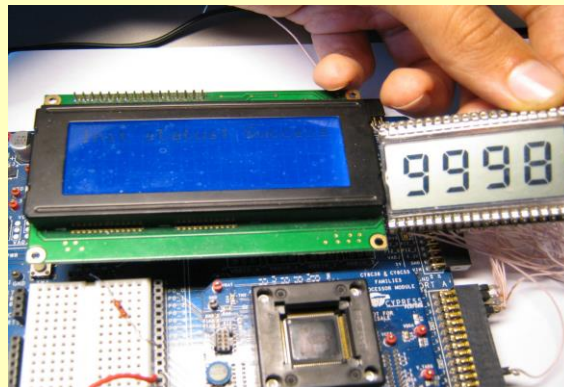
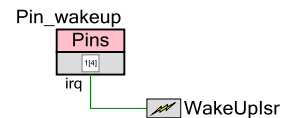
Char LCD will display a warning if any error occurs in StaticSegLCD component.
Char LCD is configured in default mode.



Static Segment LCD component is driving 4 digit 7 Segment LCD glass.



Pin 1[4] should be wired to a button which will wake the part from sleep.



This example project displays 4 digit decimal value, decrements the value and displays updated value after a small delay. Then the part goes to a Sleep mode, during which the LCD is turned off (LCD glass is blank). After a button press it wakes and continues its normal work.

You will need DVK CY8CKIT-001 and VIM-402 LCD module for this example project !!!

PSoC Resources

Cypress provides a wealth of data at www.cypress.com to help you to select the right PSoC device for your design, and quickly and effectively integrate the device into your design. For a comprehensive list of resources, see [KBA86521](#), [How to Design with PSoC 3](#), [PSoC 4](#), and [PSoC 5LP](#). The following is an abbreviated list for PSoC:

- **Overview:** [PSoC Portfolio](#), [PSoC Roadmap](#)
- **Product Selectors:** [PSoC 1](#), [PSoC 3](#), [PSoC 4](#), or [PSoC 5LP](#). In addition, [PSoC Creator](#) includes a device selection tool.
- **Datasheets:** Describe and provide electrical specifications for the PSoC 3, PSoC 4, and PSoC 5LP device families.
- **CapSense Design Guides:** Learn how to design capacitive touch-sensing applications with the PSoC 3, PSoC 4, and PSoC 5LP families of devices.
- **Application Notes** and **Code Examples:** Cover a broad range of topics, from basic to advanced level. Many of the application notes include code examples.
- **Technical Reference Manuals (TRM):** Provide detailed descriptions of the architecture and registers in each of the PSoC 3, PSoC 4, and PSoC 5LP device families.
- **PSoC Training Videos:** These videos provide step-by-step instructions on getting started building complex designs with PSoC.
- **Development Kits:**
 - [CY8CKIT-042](#) and [CY8CKIT-040](#), PSoC 4 Pioneer kits, are easy-to-use and inexpensive development platforms. These kits include connectors for Arduino™ compatible shields and Digilent® Pmod™ daughter cards.
 - [CY8CKIT-049](#) is a series of very low-cost prototyping platform for sampling PSoC 4 devices.
 - [CY8CKIT-030](#) and [CY8CKIT-050](#) are designed for analog performance. They enable you to evaluate, develop, and prototype high-precision analog, low-power, and low-voltage applications powered by PSoC 3 and PSoC 5LP, respectively.
 - [CY8CKIT-001](#) is a common development platform for all PSoC family devices.
- The [MiniProg3](#) device provides an interface for flash programming and debug.

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