

# INPUTS

The diagram illustrates the input connections for three microcontrollers:

- U2 (ADPS):** A microcontroller with pins Vdd, Gnd, O/P, SD, and Gnd. It is connected to Vdd and Gnd. The O/P pin is connected to LDR OUT. The SD pin is connected to LDR EN. A 100k resistor (R11) is connected between the SD pin and Gnd.
- U7 (MCP3421):** A microcontroller with pins Vdd, Gnd, SDA, SCL, and TC. It is connected to Vdd and Gnd. The SDA pin is connected to SDA. The SCL pin is connected to SCL. A 10k resistor (R14) is connected between the TC pin and Gnd. A 100nF capacitor (C16) is connected between the TC pin and Gnd. A 10uF capacitor (C15) is connected between the Vdd pin and Gnd. A 100nF capacitor (C14) is connected between the Vdd pin and Gnd.
- U6 (MCP9800):** A microcontroller with pins Vdd, Gnd, SDA, SCL, and Alert. It is connected to Vdd and Gnd. The SDA pin is connected to SDA. The SCL pin is connected to SCL. The Alert pin is connected to Gnd.

The diagram also shows the connection of the microcontrollers to the Vdd and Gnd rails, and the connection of the SDA and SCL lines to the microcontrollers.

[illegible]

# OUTPUTS

The diagram illustrates the output stage of a 74VHC04 hex inverters. It features three input pins: BLUE, GREEN, and RED, each connected to a 330 ohm resistor (R4, R5, R6) and a 1k ohm resistor. These inputs are connected to the BA, GA, and RA pins of a 74VHC04 hex inverter (D1). The outputs of the inverter are connected to the Vdd\_I/O pin of a 74VHC04 hex inverter (U3). A pull-up resistor R10 (10k) is connected between the Vdd\_I/O pin and the output of the inverter. The output of the inverter is connected to the FLASH R/B pin. A NAND gate is also shown, connected to the output of the inverter and the Vdd\_I/O pin.

[illegible]