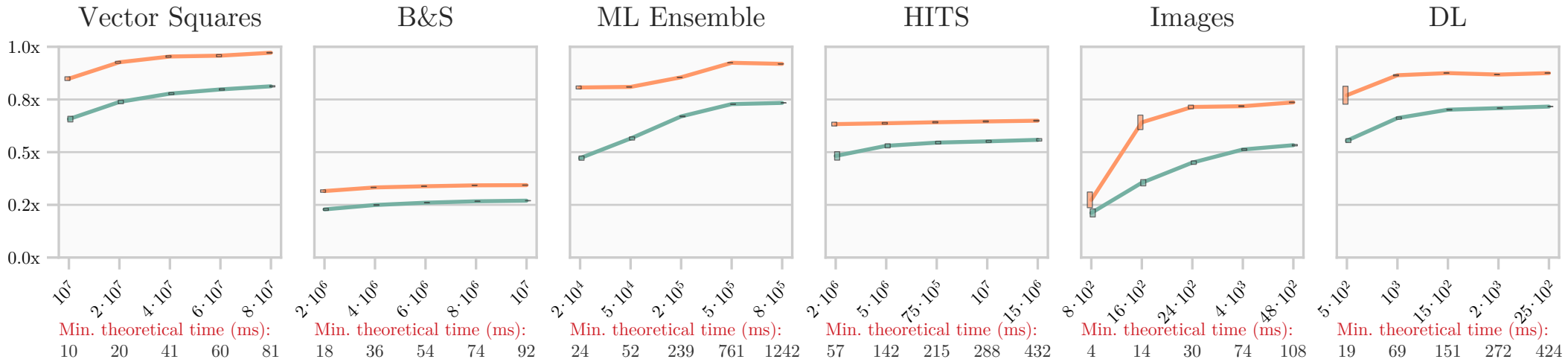


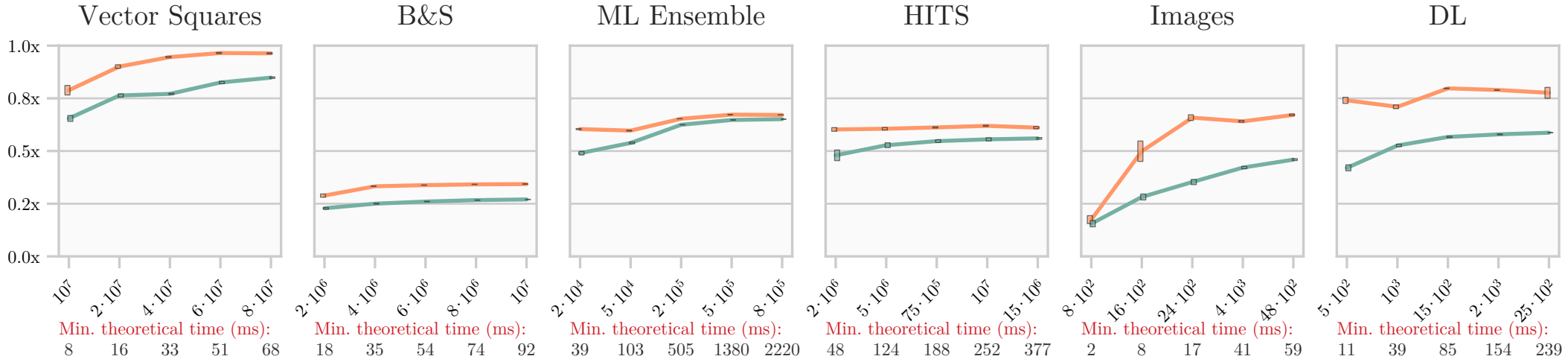
# Speedup w.r.t minimum theoretical time

- DAG Scheduling
- Serial Scheduling

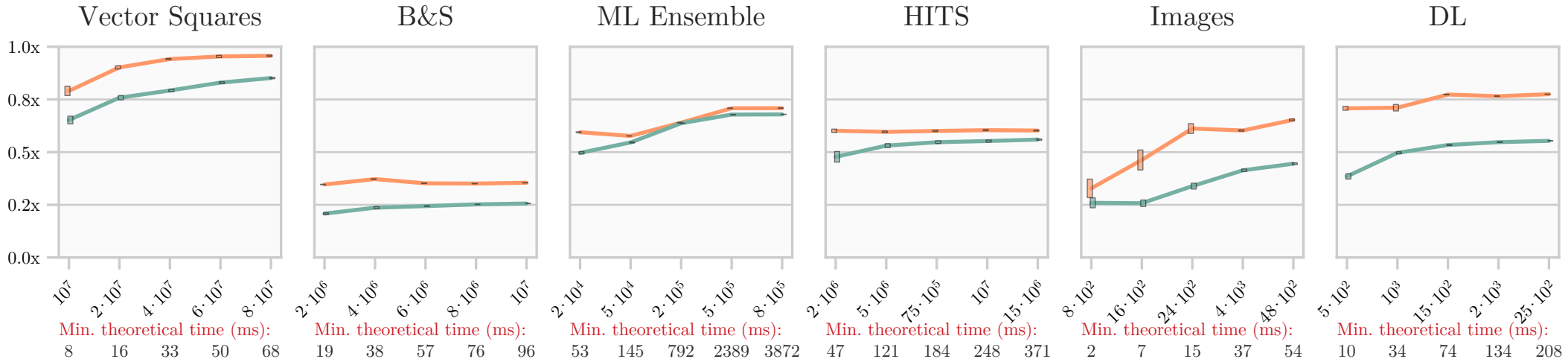
Block size:  
1D=32, 2D=8x8



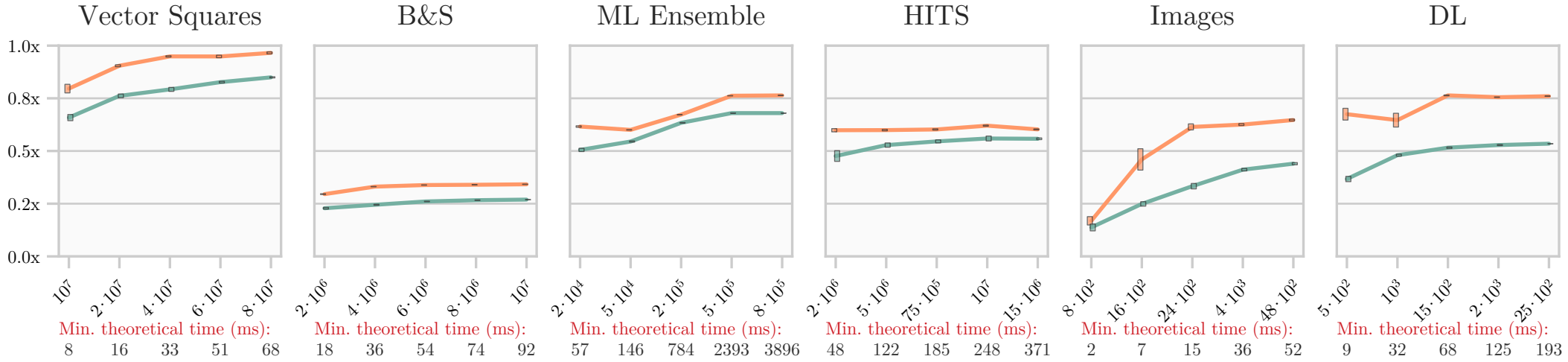
Block size:  
1D=128, 2D=8x8



Block size:  
1D=256, 2D=8x8



Block size:  
1D=1024, 2D=8x8



Speedup

Input number of elements