

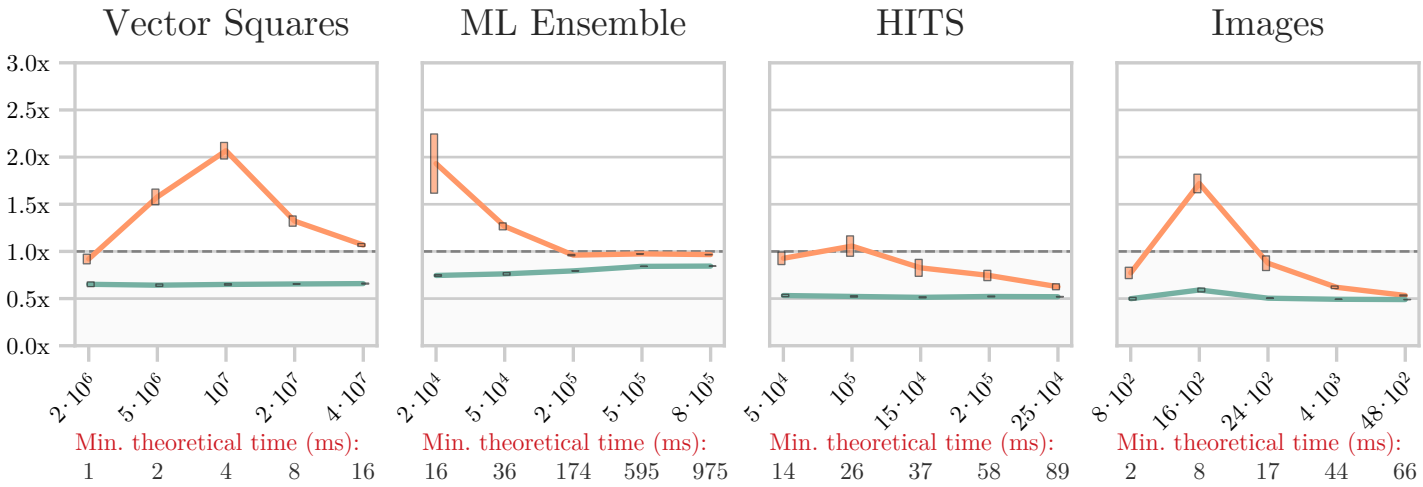
Speedup w.r.t minimum theoretical time

- DAG Scheduling

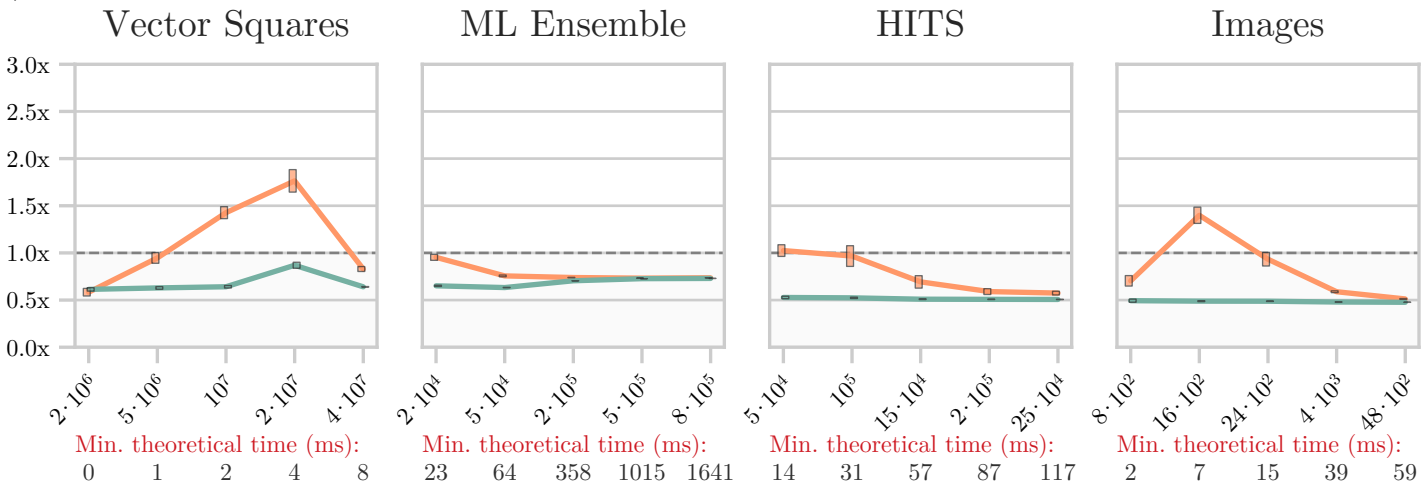
Serial Scheduling

Speedup

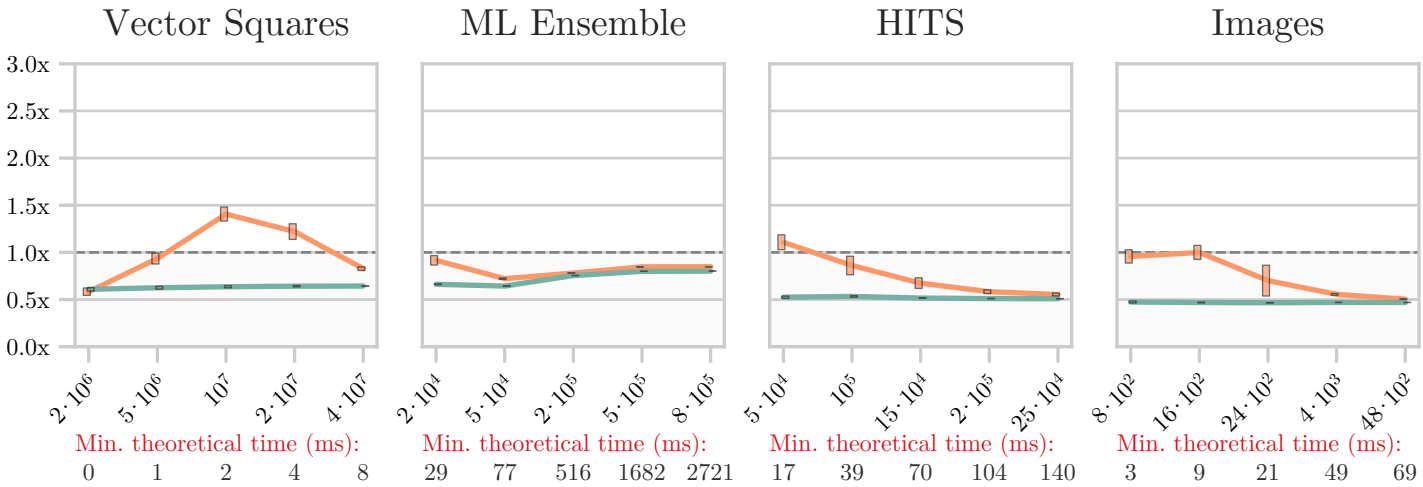
Block size:
1D=32, 2D=8x8



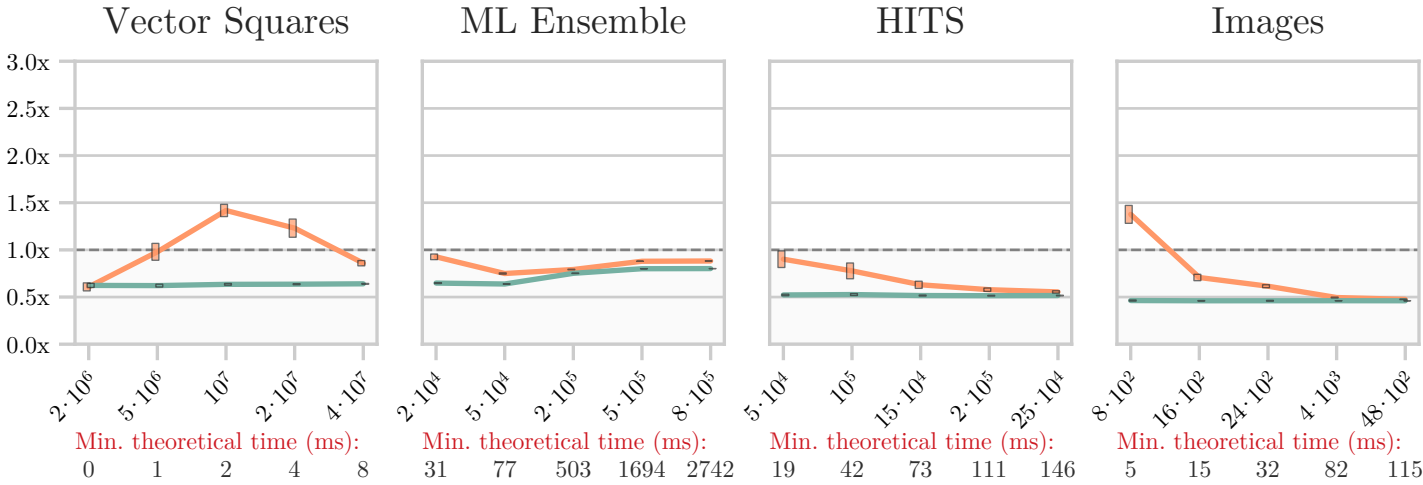
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Block size:
1D=256, 2D=16x16



Block size:
1D=1024, 2D=32x32



Input number of elements