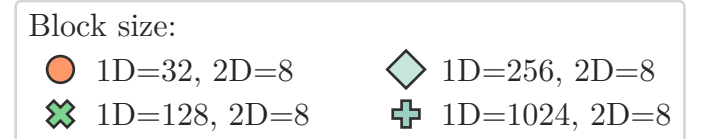
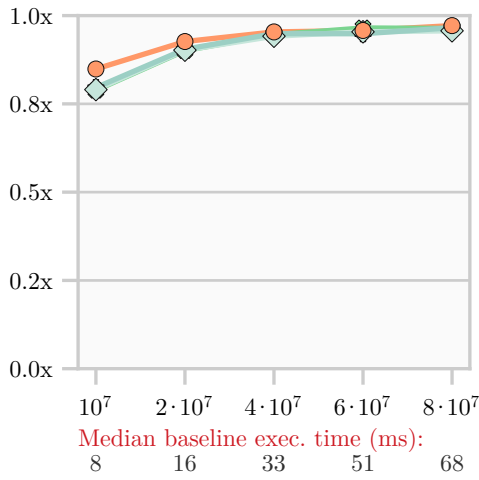


Speedup w.r.t minimum theoretical time

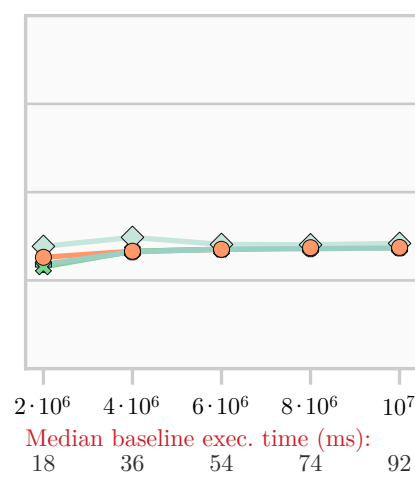


DAG Scheduling

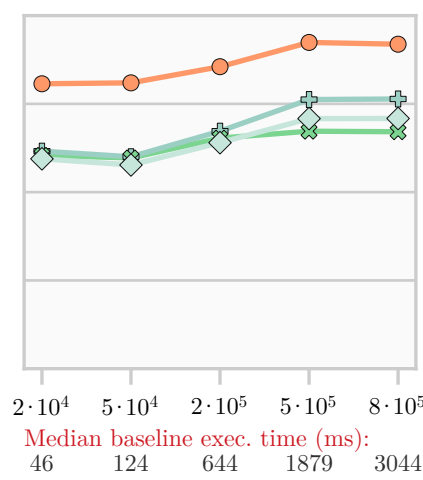
Vector Squares



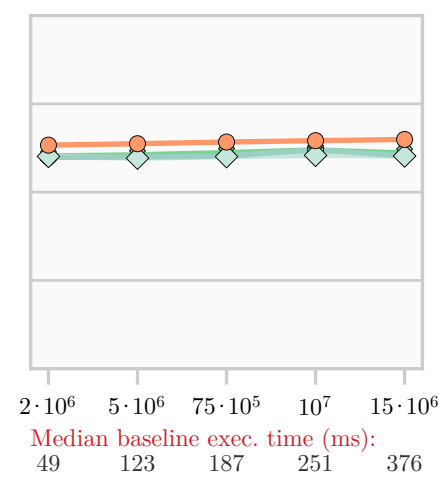
B&S



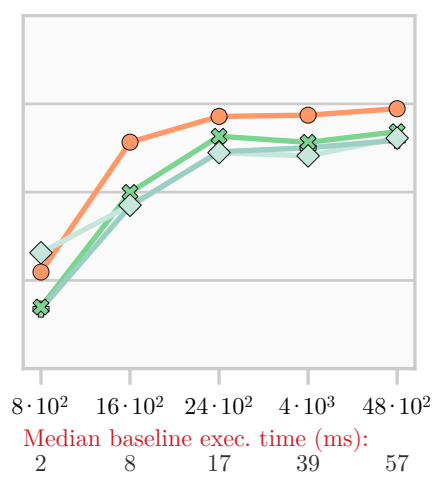
ML Ensemble



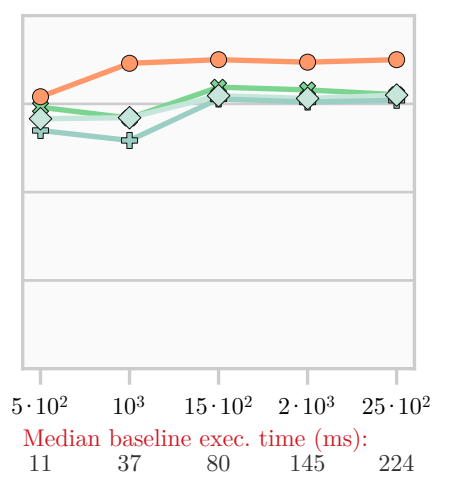
HITS



Images

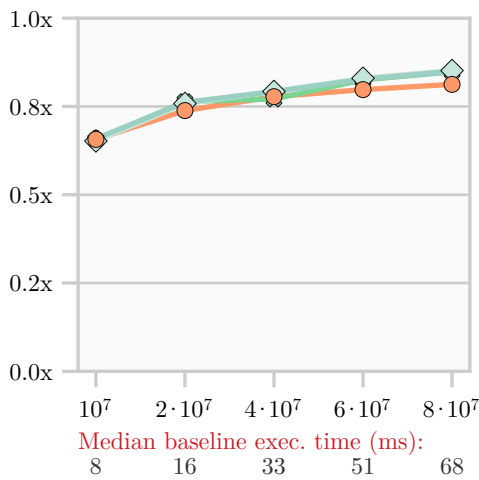


DL

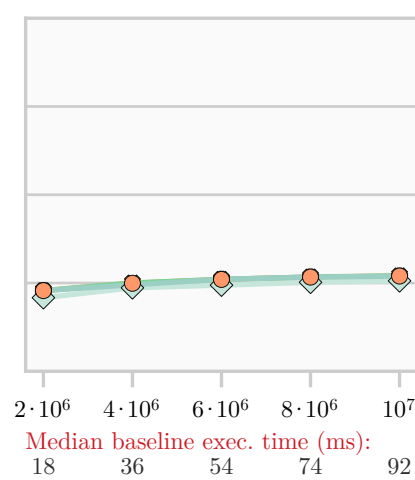


Serial Scheduling

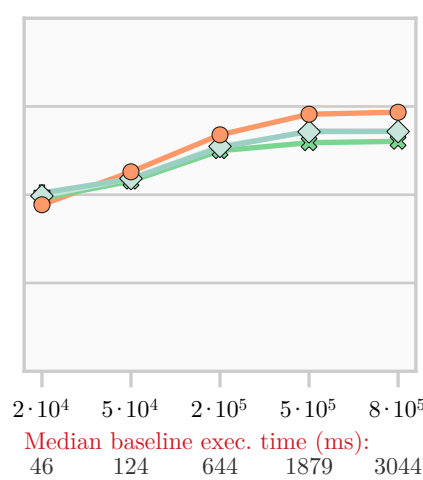
Vector Squares



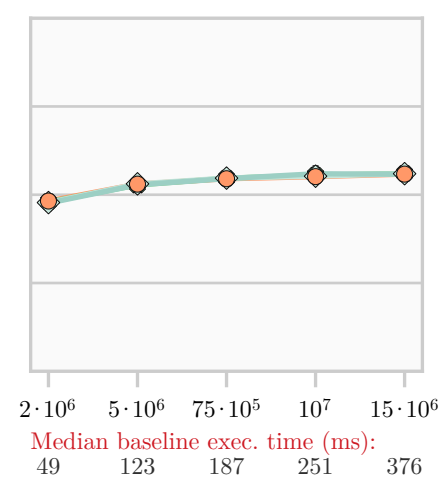
B&S



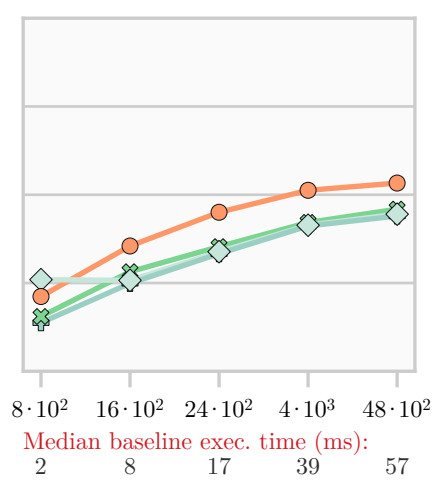
ML Ensemble



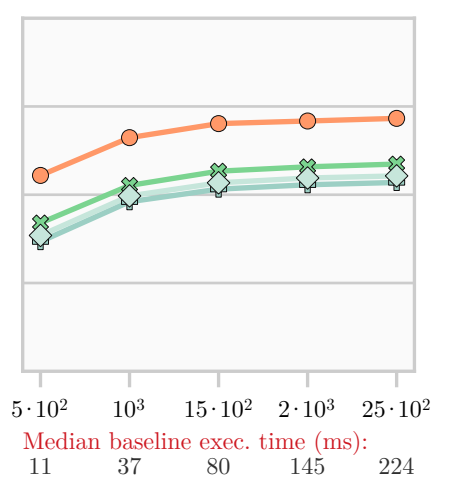
HITS



Images



DL



Speedup

Input number of elements