Hardware metrics Serial Scheduling for each benchmark and execution policy DAG Scheduling Device memory throughput L2 cache throughput IPC 50 GB/s250 GB/s1.50 1.00x1.32x1.32x1.25 $40~\mathrm{GB/s}$ 200 GB/s -1.38x1.09x1.00 30 GB/s150 GB/s 1.32x0.7520 GB/s100 GB/s0.501.38x1.09x1.00x 1.12x1.00x1.09x50 GB/s10 GB/s 1.12x0.251.38x1.12x0 GB/s0 GB/s0.00 VEC VEC B&S VEC B&S Serial Serial Serial throughput: IPC: