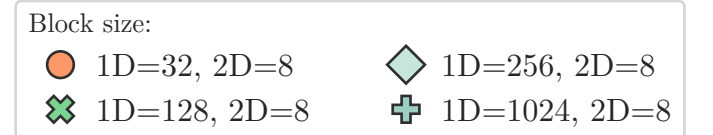
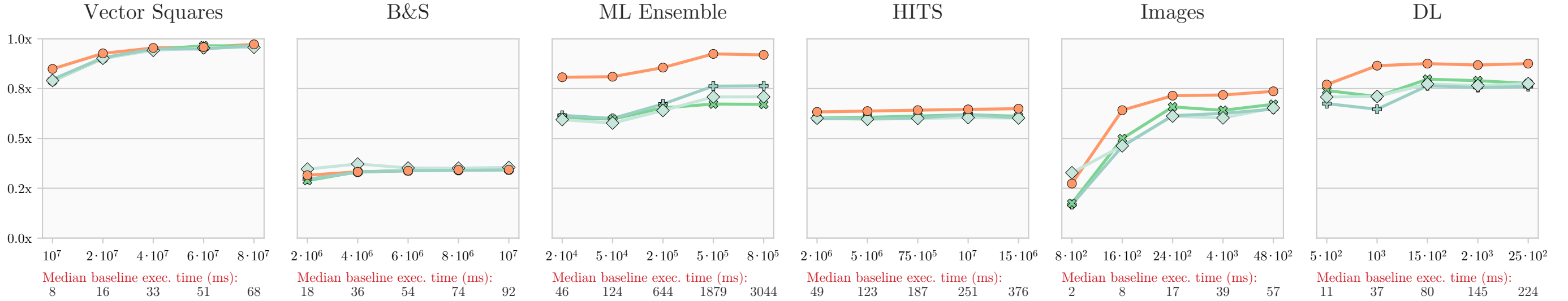


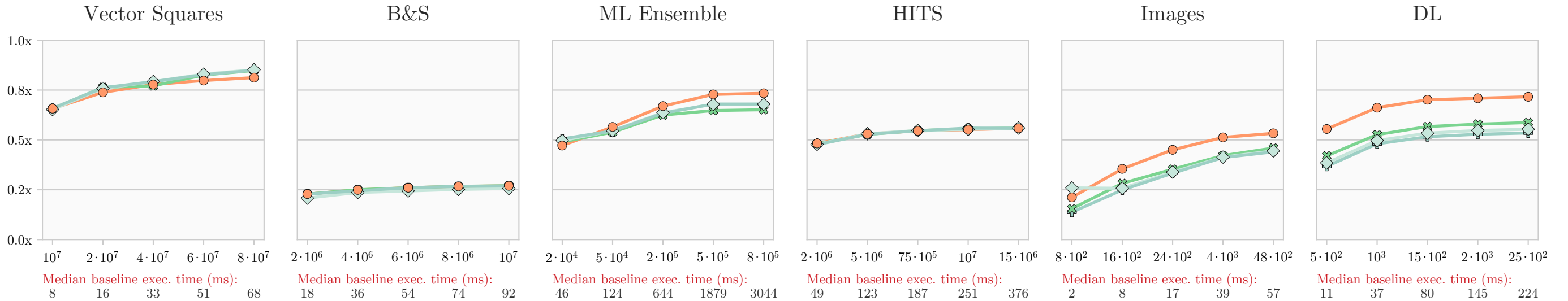
Speedup w.r.t minimum theoretical time



DAG Scheduling



Serial Scheduling



Input number of elements