

Speedup w.r.t minimum theoretical time

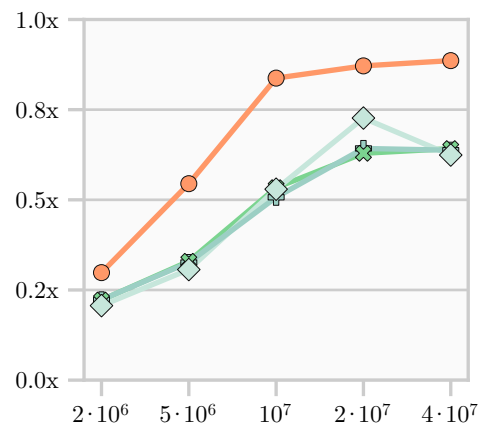
Block size:

- 1D=32, 2D=8
- 1D=128, 2D=8
- 1D=256, 2D=16
- 1D=1024, 2D=32

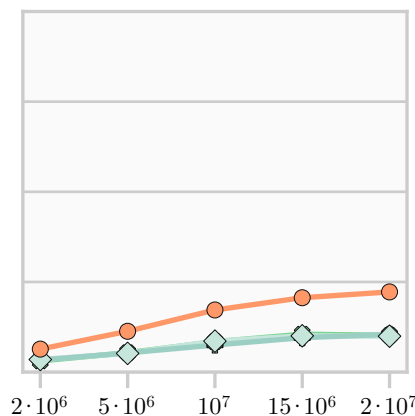
Speedup

DAG Scheduling

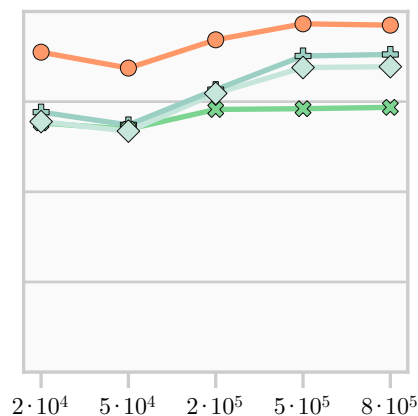
Vector Squares



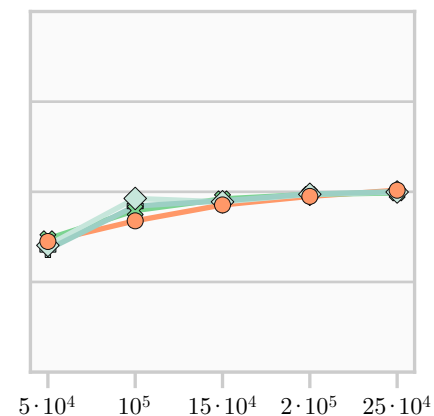
B&S



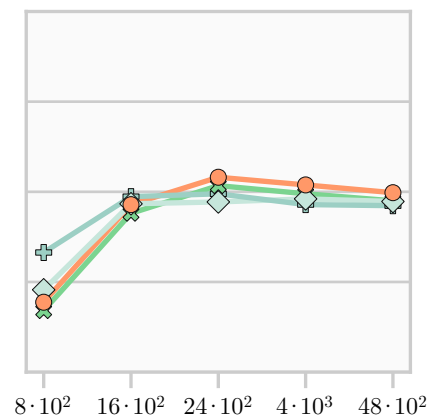
ML Ensemble



HITS

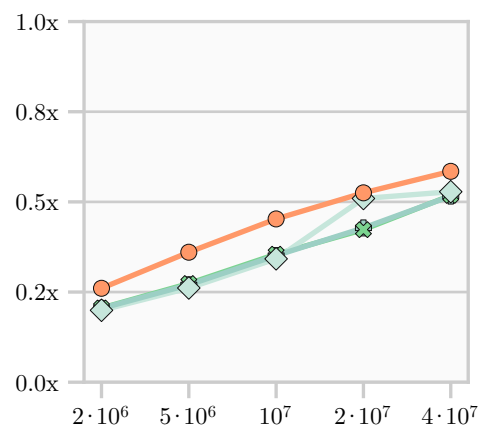


Images

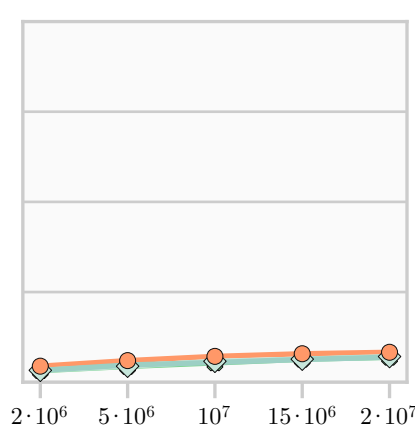


Serial Scheduling

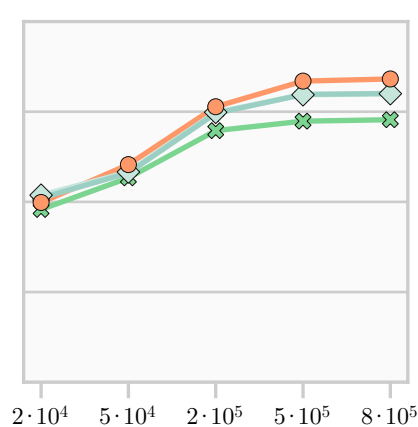
Vector Squares



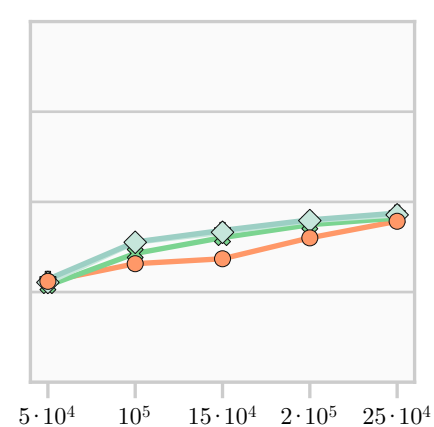
B&S



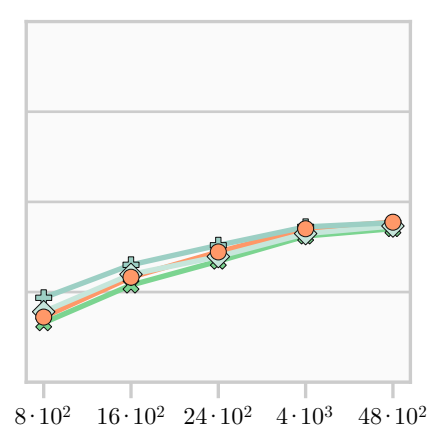
ML Ensemble



HITS



Images



Input number of elements