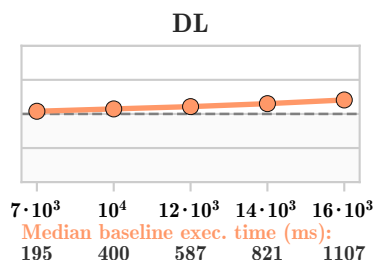
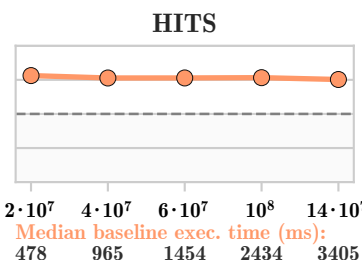
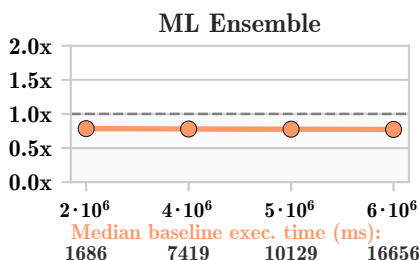
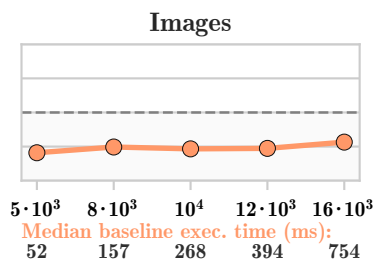
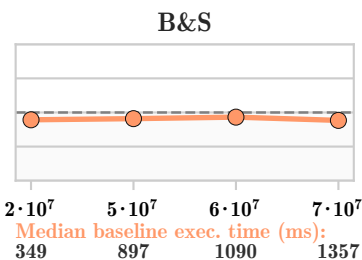
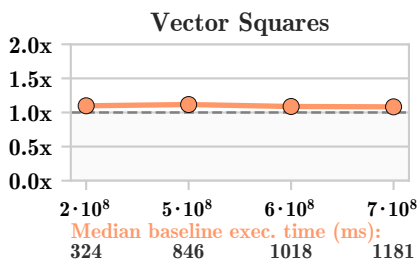


Speedup of GrCUDA scheduling w.r.t. hand-optimized C++ CUDA scheduling

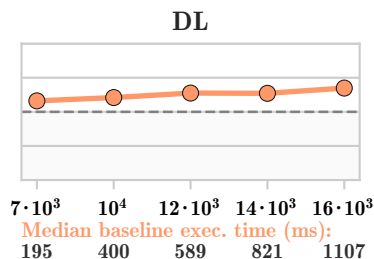
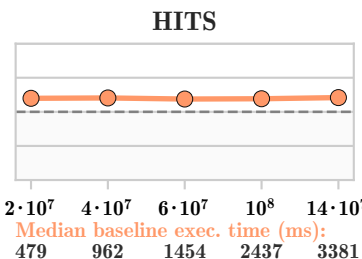
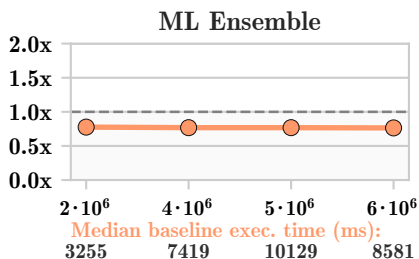
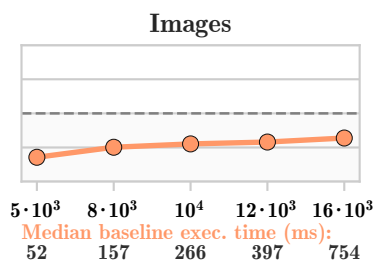
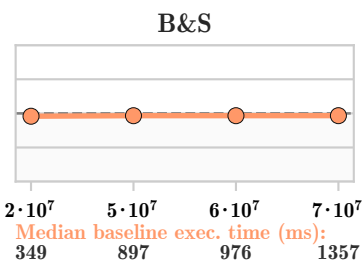
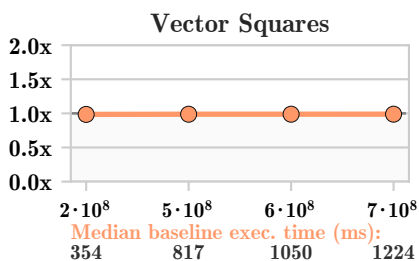
CUDA baseline type

● CUDA events

Parallel Scheduler



Serial Scheduler



Input number of elements