

Speedup of GrCUDA w.r.t. CUDA

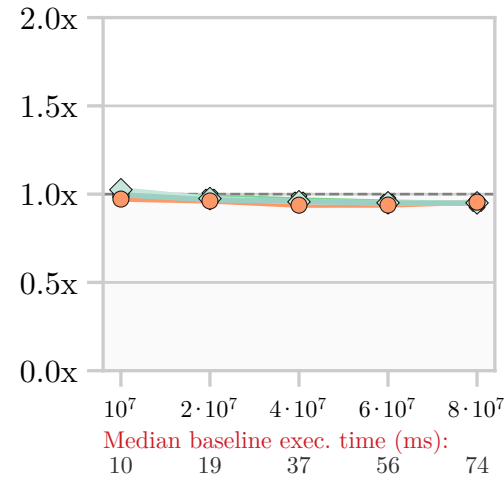
Block size:

- 1D=32, 2D=8
- 1D=128, 2D=8
- 1D=256, 2D=8
- 1D=1024, 2D=8

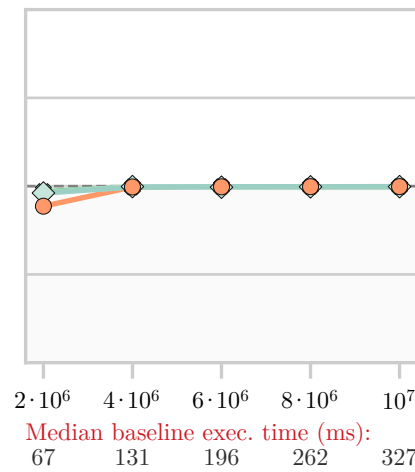
Speedup

DAG Scheduling

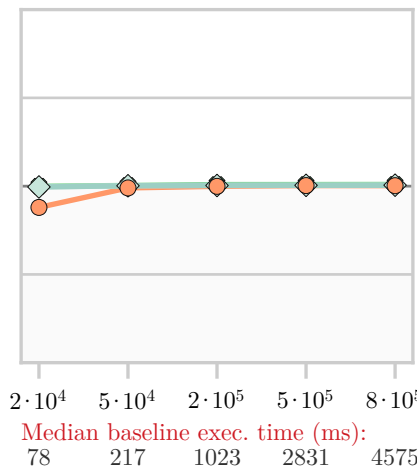
Vector Squares



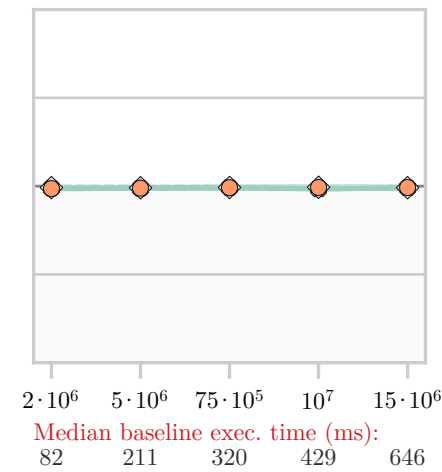
B&S



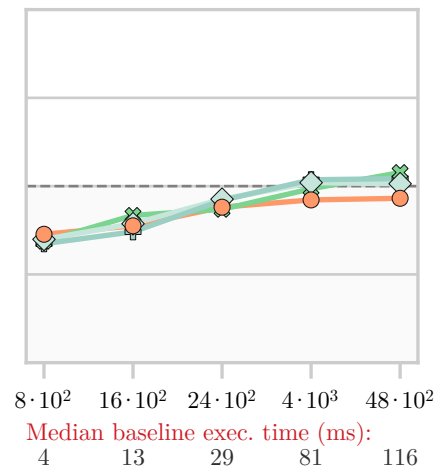
ML Ensemble



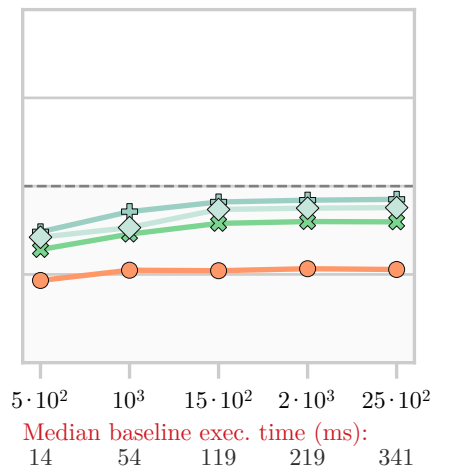
HITS



Images

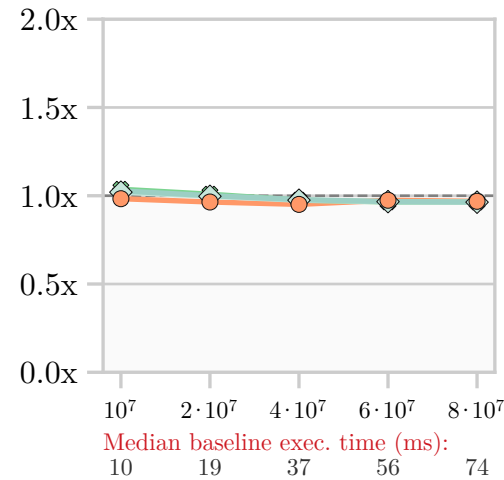


DL

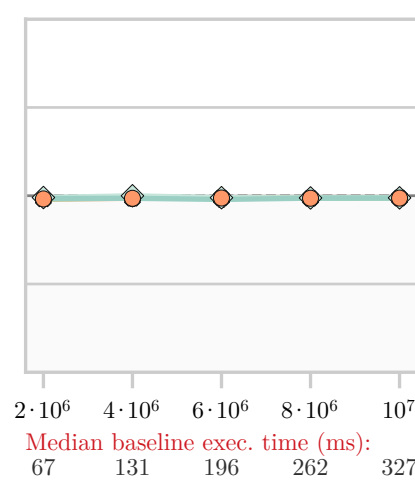


Serial Scheduling

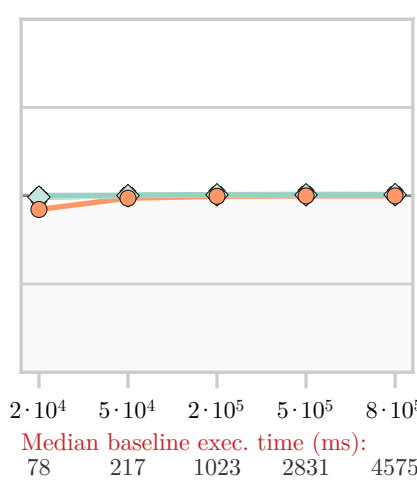
Vector Squares



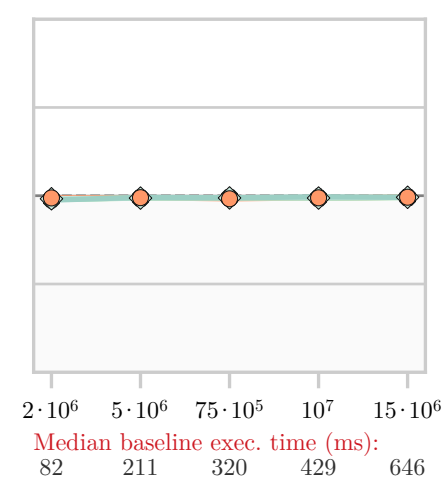
B&S



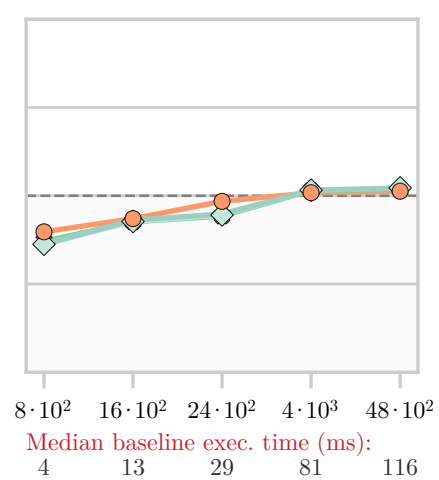
ML Ensemble



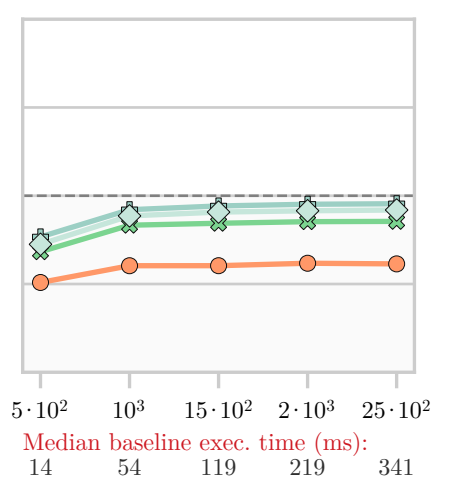
HITS



Images



DL



Input number of elements