Speedup w.r.t minimum theoretical time

Block size:
- 1D=32, 2D=8
- 1D=128, 2D=8
- 1D=256, 2D=8
- 1D=1024, 2D=8
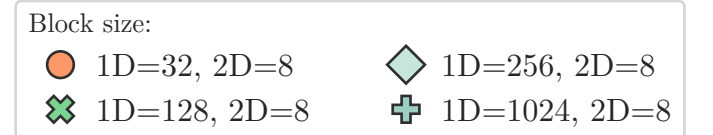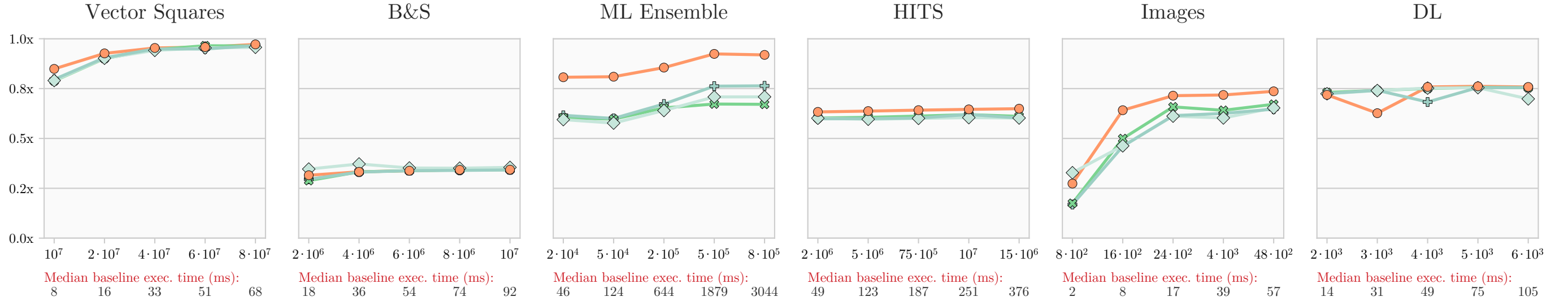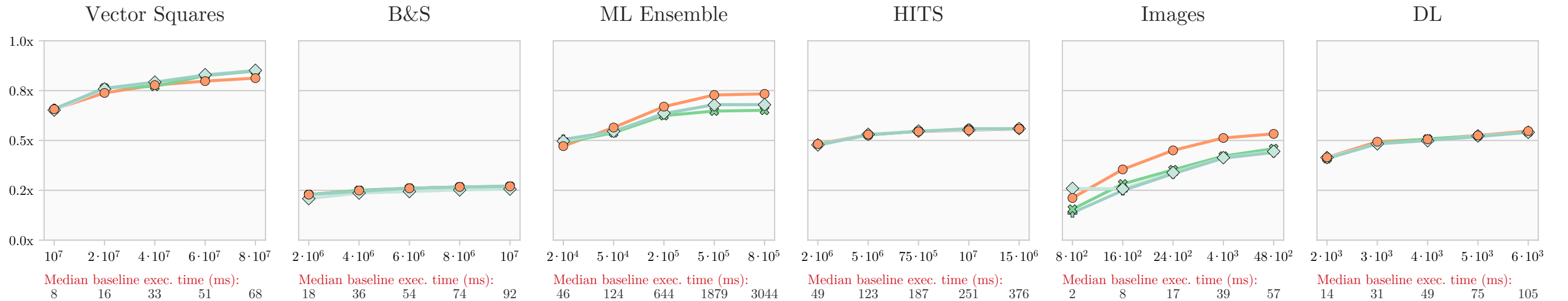
DAG Scheduling

Serial Scheduling

Input number of elements