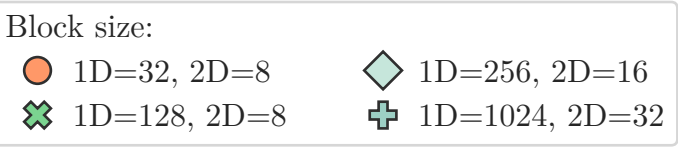
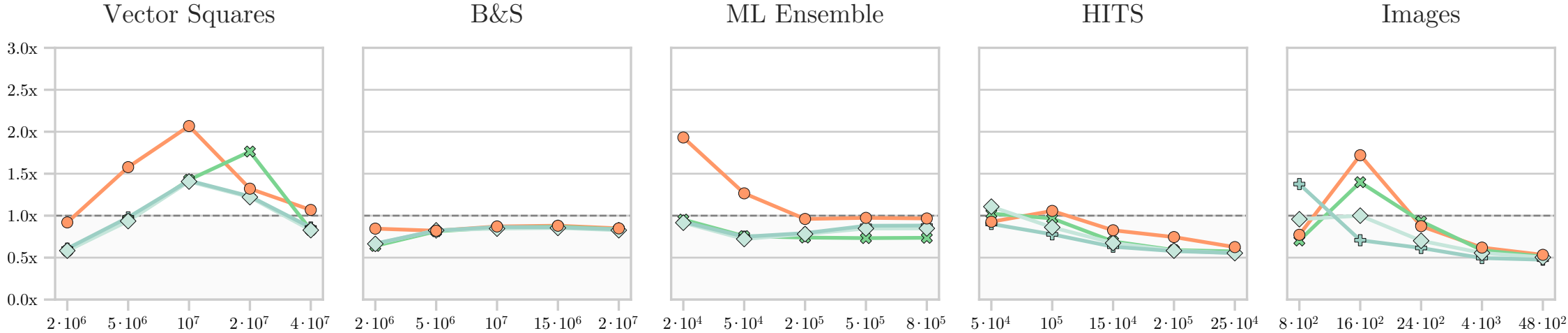


# Speedup w.r.t minimum theoretical time

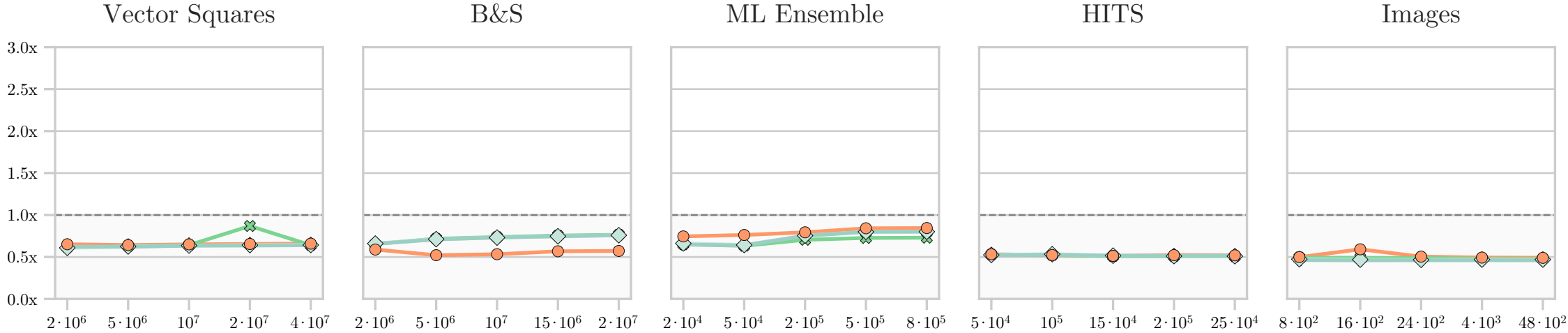


Speedup

DAG Scheduling



Serial Scheduling



Input number of elements