

Speedup of GrCUDA w.r.t. CUDA

Block size:

● 1D=32, 2D=8

◆ 1D=256, 2D=16

✕ 1D=128, 2D=8

⊕ 1D=1024, 2D=32

DAG Scheduling

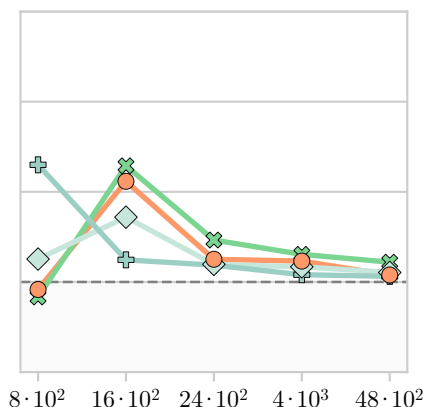
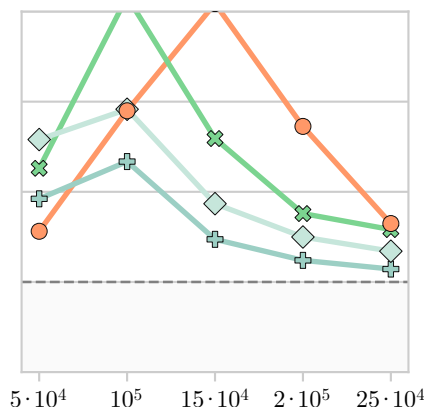
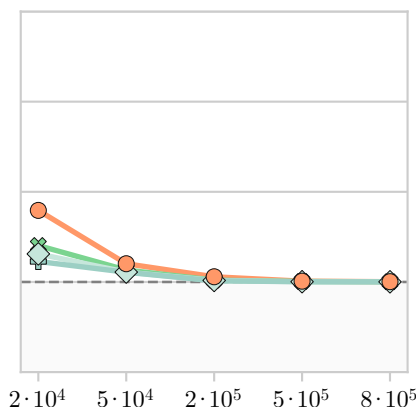
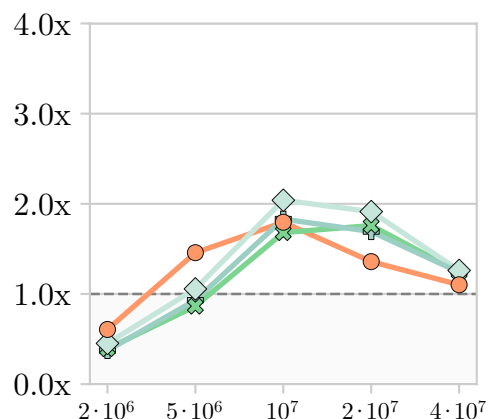
Vector Squares

ML Ensemble

HITS

Images

Speedup



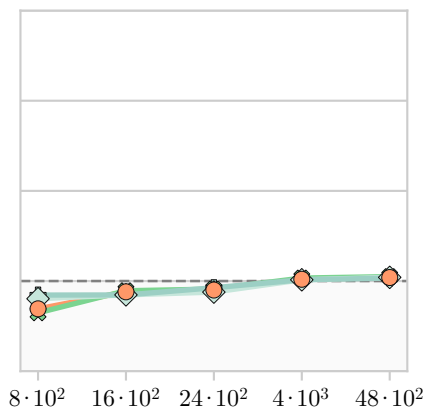
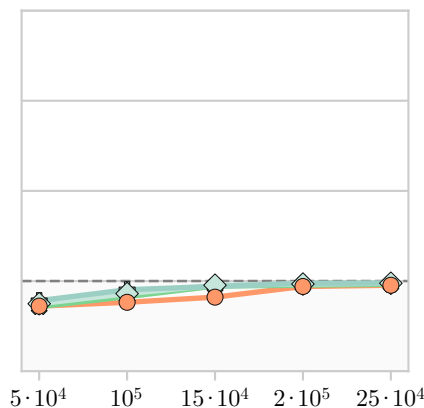
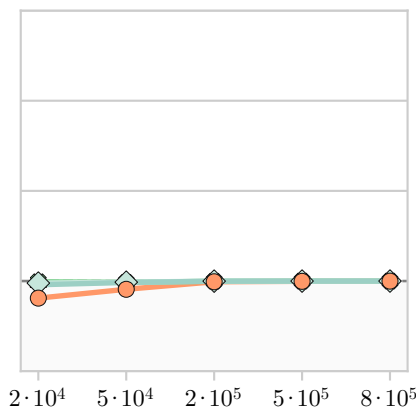
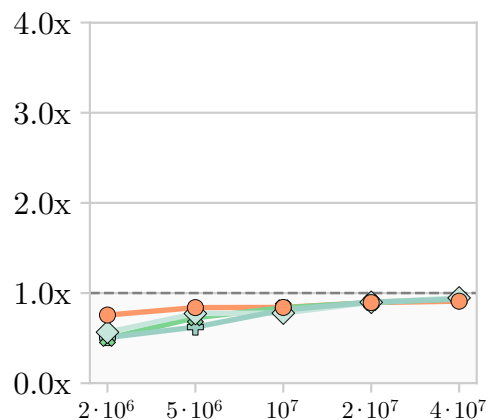
Serial Scheduling

Vector Squares

ML Ensemble

HITS

Images



Input number of elements