





University Of Texas At Austin

Schematic Name: Lab 3

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Semester: Spring 2016

Logic Analyzer

Setup...Load...Save...Min Time2.21074 sMax Time3.745114 sGrid0.1 sZoomInOutAllMin/MaxAutoUndoUpdate ScreenStopClearTransitionPrevNextJump toCodeTraceSignal InfoAmplitudeShow CyclesCursor

PE0

PE1

2.287458 s

2.5432.605458 s, d: 62 ms

DisassemblyLogic Analyzer

Startup.smain.smain.s

103

Delay

MOV

R7,

#20

104

Subt

MOV

R8,

#62000

105

wait

SUBS

R8,

#1

106

BNE

wait

107

SUBS

R7,

#1

108

BNE

Subt

109

BX

LR

110

111

112

ALIGN

; make sure the end of

113

END

; end of file

114

Texas Lab 3

Port E Hardware

TM4C123

SW

PE1

PE0

LED

Port E Registers

DATA: 0x01PUR: 0x00LOCK: 0x01

DIR: 0x01PDR: 0x00CR: 0xFF

DEN: 0x03RCGCGPIO: 0x00000019Clock enabled

Grading Controls

Grade

Score: 0

Number from EdX:

Copy this to EdX:

Type

Parameter	Value	Units	Conditions
Resistance of the 10k Ω resistor, R1	9.95k Ω	Ohms	With power off and disconnected from circuit (measured with ohmmeter)
Supply Voltage $V_{+3.3}$	2.975 V	Volts	Powered (measured with voltmeter)
Input Voltage V_{PE1}	37 mV	Volts	Powered, but with switch not pressed (measured with voltmeter)
Resistor Current	0mA AND 0mA	mA	Powered, but switch not pressed $I = V_{PE1}/R1$ (calculated and measured with an ammeter)
Input Voltage V_{PE1}	2.93 V	Volts	Powered and with switch pressed (measured with voltmeter)
Resistor Current	Calculated: .37 mA Measured: 0.29 mA	mA	Powered and switch pressed $I = V_{PE1}/R1$ (calculated and measured with an ammeter)

Row	Parameter	Value	Units	Conditions
1	Resistance of the 220Ω resistor R19	219.5Ω	Ohms	With power off and disconnected from circuit (measured with ohmmeter)
2	+5 V power supply V_{+5}	4.95 V	Volts	(measured with voltmeter relative to ground, notice that the +5V power is not exactly +5 volts)
3	TM4C123 Output V_{PE0} Input to 7406	3.266 V	Volts	with PE0 = 0 (measured with voltmeter relative to ground)
4	7406 Output V_{k-} LED k-	.59 V	Volts	with PE0 = 0 (measured with voltmeter relative to ground)
5	LED a+, V_{a+} Bottom side of R19	2.43 V	Volts	with PE0 = 0 (measured with voltmeter relative to ground)
6	LED Voltage	1.86 V	Volts	calculated as $V_{a+} - V_{k-}$
7	LED Current	Calculated: 11.48 mA Measured: 9.7 mA	mA	calculated as $(V_{+5} - V_{a+})/R19$ and measured with an ammeter

8	TM4C123 Output, V_{PEO} input to 7406	1.598 V	Volts	with PE0 = 1 (measured with voltmeter relative to ground)
9	7406 Output, V_{k-} LED k-	2.378 V	Volts	with PE0 = 1 (measured with voltmeter relative to ground)
10	LED a+, V_{a+} Bottom side of R19	3.695 V	Volts	with PE0 = 1 (measured with voltmeter relative to ground)
11	LED voltage	1.32 V	Volts	calculated as $V_{a+} - V_{k-}$
12	LED current	Calculated: 11.45 mA Measured: 10.38 mA	mA	calculated as $(V_{+5} - V_{a+})/R19$ and measured with an ammeter

```

1  ;***** main.s *****
2  ; Program written by: ***Your Names**update this***
3  ; Date Created: 1/22/2016
4  ; Last Modified: 1/22/2016
5  ; Section ***Tuesday 1-2***update this***
6  ; Instructor: ***Ramesh Yerraballi**update this***
7  ; Lab number: 3
8  ; Brief description of the program
9  ;   If the switch is presses, the LED toggles at 8 Hz
10 ; Hardware connections
11 ;   PE1 is switch input (1 means pressed, 0 means not pressed)
12 ;   PE0 is LED output (1 activates external LED on protoboard)
13 ;Overall functionality of this system is the similar to Lab 2, with six changes:
14 ;1- the pin to which we connect the switch is moved to PE1,
15 ;2- you will have to remove the PUR initialization because pull up is no longer needed.
16 ;3- the pin to which we connect the LED is moved to PE0,
17 ;4- the switch is changed from negative to positive logic, and
18 ;5- you should increase the delay so it flashes about 8 Hz.
19 ;6- the LED should be on when the switch is not pressed
20 ; Operation
21 ;   1) Make PE0 an output and make PE1 an input.
22 ;   2) The system starts with the LED on (make PE0 =1).
23 ;   3) Wait about 62 ms
24 ;   4) If the switch is pressed (PE1 is 1), then toggle the LED once, else turn the LED on.
25 ;   5) Steps 3 and 4 are repeated over and over
26
27
28 GPIO_PORTE_DATA_R      EQU    0x400243FC
29 GPIO_PORTE_DIR_R       EQU    0x40024400
30 GPIO_PORTE_AFSEL_R     EQU    0x40024420
31 GPIO_PORTE_DEN_R       EQU    0x4002451C
32 GPIO_PORTE_AMSEL_R     EQU    0x40024528
33 GPIO_PORTE_PCTL_R      EQU    0x4002452C
34 SYSCCTL_RCGCGPIO_R     EQU    0x400FE608
35 PE1                    EQU    0x40024008
36 PE0                    EQU    0x40024004
37
38     IMPORT  TExaS_Init
39     AREA   |.text|, CODE, READONLY, ALIGN=2
40     THUMB
41     EXPORT Start
42
43 Start
44     ; TExaS_Init sets bus clock at 80 MHz
45     BL TExaS_Init ; voltmeter, scope on PD3
46 ; you initialize PE1 PE0
47     LDR R0,= SYSCCTL_RCGCGPIO_R
48     LDR R1,[R0]
49     ORR R1,#0x10
50     STR R1,[R0]
51
52     NOP
53
54     LDR R0,= GPIO_PORTE_DIR_R
55     LDR R1,[R0]
56     BIC R1,#0x02
57     ORR R1,#0x01
58     STR R1,[R0]
59
60     LDR R0,= GPIO_PORTE_AFSEL_R
61     LDR R1,[R0]
62     BIC R1,#0x03
63     STR R1,[R0]
64
65     LDR R0,= GPIO_PORTE_DEN_R
66     LDR R1,[R0]
67     ORR R1,#0x03
68     STR R1,[R0]
69
70
71     CPSIE I      ; TExaS voltmeter, scope runs on interrupts
72

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```
73      LDR R0,= PE0
74      LDR R1,[R0]
75      ORR R1,#0xFF
76      STR R1,[R0]
77
78      loop
79      ; you input output delay
80      BL Delay
81
82
83      LDR R0,= PE1
84      LDR R2,= PE0
85
86      LDR R1,[R0]
87      CMP R1,#0
88      BNE Toggle
89      BEQ StayOn
90
91
92      Toggle LDR R1,[R2]
93              EOR R1, R1, #0xFF
94              STR R1,[R2]
95              B    loop
96
97      StayOn LDR R1,[R2]
98              ORR R1,#0xFF
99              STR R1,[R2]
100
101
102              B    loop
103
104      Delay MOV R7, #20
105      Subt  MOV R8, #62000
106      wait SUBS R8, #1
107              BNE wait
108              SUBS R7, #1
109              BNE Subt
110              BX  LR
111
112      ALIGN      ; make sure the end of this section is aligned
113      END        ; end of file
114
```