flea bit(e)s and pieces

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- 2 Procedure
- 3 Equality
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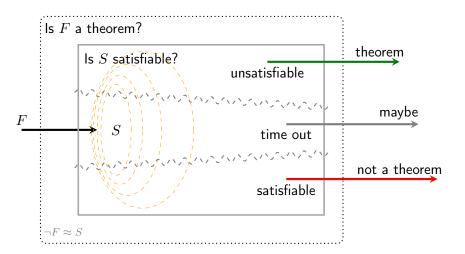
References



Bruno Dutertre, *Yices 2.2*, Computer-Aided Verification (CAV'2014) (Armin Biere and Roderick Bloem, eds.), Lecture Notes in Computer Science, vol. 8559, Springer, July 2014, pp. 737–744.



G. Sutcliffe, *The TPTP Problem Library and Associated Infrastructure: The FOF and CNF Parts, v3.5.0*, Journal of Automated Reasoning **43** (2009), no. 4, 337–362.



Definition (Ordered Resolution)

$$\frac{L \vee C \quad \neg L' \vee D}{(C \vee D)\sigma}$$

where

 $L\sigma$ strictly maximal in $C\sigma$, $\neg L'\sigma$ maximal in $D\sigma$, $\sigma = \text{mgu}(L, L')$.

Definition (Inst-Gen)

$$\frac{L \vee C \quad \neg L' \vee D}{(L \vee C)\sigma \quad (\neg L' \vee D)\sigma}$$

where

$$\operatorname{sel}(L \vee C) = L$$
 $\operatorname{sel}(\neg L' \vee D) = \neg L'$ $\sigma = \operatorname{mgu}(L, L')$

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$$S = \{ \mathsf{P}(x) \lor \neg \mathsf{P}(y), \neg \mathsf{P}(\mathsf{a}), \mathsf{P}(\mathsf{b}) \}$$

$$\frac{\mathsf{P}(x) \quad \neg \mathsf{P}(y)}{\mathsf{P}(y)} \xrightarrow{x \mapsto \mathsf{a}} \qquad \mathsf{P}(\mathsf{b})}{\Box} y \mapsto \mathsf{b}$$

Example (Inst-Gen)

$$S_0 \perp = \{ \mathsf{P}(\perp) \vee \neg \mathsf{P}(\perp), \neg \mathsf{P}(\mathsf{a}), \mathsf{P}(\mathsf{b}) \}$$

$$\frac{\mathsf{P}(x) \vee \neg \mathsf{P}(y)) \quad \neg \mathsf{P}(\mathsf{a})}{\mathsf{P}(\mathsf{a}) \vee \neg \mathsf{P}(y)} \quad x \mapsto \mathsf{a}$$

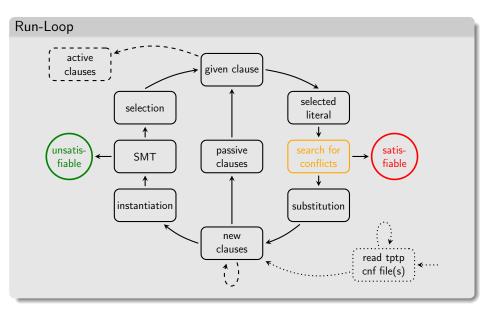
$$S_1 \perp \supseteq \{ \neg P(\mathsf{a}), P(\mathsf{b}), \frac{P(\mathsf{a})}{P(\mathsf{a})} \vee \neg P(\perp) \}$$
$$\frac{P(\mathsf{b}) \quad P(\mathsf{a}) \vee \neg P(y)}{P(\mathsf{a}) \vee P(\mathsf{b})} \ y \mapsto \mathsf{b}$$

 $S_2 \perp \supset \{\neg P(a), P(b), P(a) \vee \neg P(b)\}$

satisfiable

satisfiable

unsatisfiable



$$S = \{C, D, \ldots\} \qquad \exists \theta \ C\theta \subseteq D$$

C subsumes D

$$S$$
 satisfiable \iff $(S \setminus D)$ satisfiable

 θ is proper, $S \perp$ satisfiable $\stackrel{\mathsf{X}}{\Longleftrightarrow} (S \setminus D) \perp$ satisfiable

 θ is renaming, $S\perp$ satisfiable \iff $(S\setminus D)\perp$ satisfiable

Example

$$\begin{aligned} \{\mathsf{P}(x,y),\neg\mathsf{P}(\mathsf{a},z)\} & \quad \{\mathsf{P}(x,y),\neg\mathsf{P}(\mathsf{a},z),\mathsf{P}(\mathsf{a},z)\} \\ \{\mathsf{P}(\bot,\bot),\neg\mathsf{P}(\mathsf{a},\bot)\} & \quad \{\mathsf{P}(\bot,\bot),\neg\mathsf{P}(\mathsf{a},\bot), \textcolor{red}{\mathsf{P}(\mathsf{a},\bot)}\} \end{aligned}$$

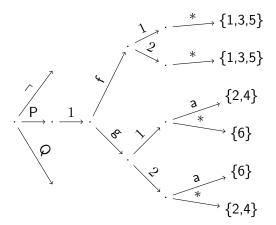
data structures

- 1 trees to represent clauses, literals and terms
- 2 path indexing for fast retrieval of clashing selected literals
- 3 discrimination trees for fast retrieval of clause variants

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$$\{{}^{^{1:}}\!\mathsf{P}(\mathsf{f}(x,x)),{}^{^{2:}}\!\mathsf{P}(\mathsf{g}(\mathsf{a},x)),{}^{^{3:}}\!\mathsf{P}(\mathsf{f}(y,z)),{}^{^{4:}}\!\mathsf{P}(\mathsf{g}(\mathsf{a},y)),{}^{^{5:}}\!\mathsf{P}(\mathsf{f}(y,x)),{}^{^{6:}}\!\mathsf{P}(\mathsf{g}(y,a))\}$$



$$\neg P(g(b, z)) \mapsto \{P.1.g.1.b, P.1.g.2.*\} \mapsto \{6\} \cap \{2, 4, 6\}$$

$$\ell_1 \mapsto \mathsf{P}(\mathsf{f}(x,y)), \ell_2 \mapsto \mathsf{P}(\mathsf{f}(x,\mathsf{h}(\mathsf{a}))), \ell_3 \mapsto \mathsf{P}(\mathsf{f}(\mathsf{h}(\mathsf{a}),\mathsf{a}))$$

$$\ell_1 \mapsto \mathsf{P}.\mathsf{f}.*.*$$

$$\ell_2 \mapsto \mathsf{P}.\mathsf{f}.*.\mathsf{h}.\mathsf{a}$$

$$\ell_3 \mapsto \mathsf{P}.\mathsf{f}.\mathsf{h}.\mathsf{a}.\mathsf{a}$$

$$\ell_3 \mapsto \mathsf{P}.\mathsf{f}.\mathsf{h}.\mathsf{a}.\mathsf{a}$$

$$\ell_3 \mapsto \mathsf{P}.\mathsf{f}.\mathsf{h}.\mathsf{a}.\mathsf{a}$$

Implementation

$$Clause \mapsto (Int, [term_t])$$
 $term_t \mapsto [Int]$
 $Int \mapsto Clause$

```
int32_t yices_assert_formula(context_t *ctx, term_t t);
term_t yices_or(uint32_t n, term_t arg[]);
term_t yices_not(term_t arg);
term_t vices_eq(term_t left, term_t right);
term_t yices_application(term_t fun, uint32_t n, const term_t arg[]);
term_t yices_new_uninterpreted_term(type_t tau);
type_t yices_function_type(uint32_t n, const type_t dom[], type_t range);
type_t vices_bool_type(void);
type_t vices_new_uninterpreted_type(void);
smt_status_t yices_check_context(context_t *ctx, const param_t *params);
model_t *yices_get_model(context_t *ctx, int32_t keep_subst);
int32_t yices_get_bool_value(model_t *mdl, term_t t, int32_t *val);
```

$$\begin{split} S = & \{ \mathsf{P}(\mathsf{a}), \neg \mathsf{P}(\mathsf{f}(\mathsf{a},\mathsf{b})), \mathsf{f}(x,\mathsf{b}) = x \} \\ S \bot = & \{ \mathsf{P}(\mathsf{a}), \neg \mathsf{P}(\mathsf{f}(\mathsf{a},\mathsf{b})), \mathsf{f}(\bot,\mathsf{b}) = \bot \} \\ & \mathsf{a} \neq y \vee \neg \mathsf{P}(\mathsf{a}) \vee \mathsf{P}(y) \end{split}$$

unsatisfiable satisfiable P(a), congruence

Schemata

$$\begin{array}{cccc} x=x & s\neq s & \text{reflexivity} \\ x\neq y\vee y=x & s\neq t & \text{symmetry} \\ x\neq y\vee y\neq z\vee x=z & s\neq t & \text{transitivity} \\ x_1\neq y_1\vee x_2\neq y_2\vee \mathsf{f}(x_1,x_2)=\mathsf{f}(y_1,y_2) & \mathsf{f}(s_1,s_2)\neq \mathsf{f}(t_1,t_2) \\ x\neq y & \vee \neg \mathsf{P}(x)\vee \mathsf{P}(y) & \mathsf{P}(s) \\ x\neq y & \vee \neg \mathsf{P}(x)\vee \mathsf{P}(y) & \neg \mathsf{P}(s) & \text{congruence} \end{array}$$

Lemma

Symmetry and transitivity are consequences of reflexivity and congruence.

Symmetry.

$$\frac{\overline{x_1 = y_1 \land x_2 = y_2 \land x_1 = x_2 \rightarrow y_1 = y_2}}{x = y \land x = x \land x = x \rightarrow y = x} \xrightarrow{\text{reflexivity}} x_1 \mapsto x, x_2 \mapsto x, y_1 \mapsto y, y_2 \mapsto x$$

Transitivity.



The Bernays-Schönfinkel-Ramsey class of first-order formulae is a decidable fragment of first-order logic. Each formula in this class is equivalent to a satisfiable formula

$$\exists a_1 \dots a_m \forall y_1 \dots y_n F$$

where F is quantifier free and does not contain function symbols.

Example

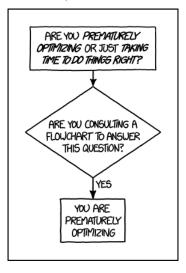
$$\mathsf{a} \neq x \vee \mathsf{Q}(\mathsf{a},x), \mathsf{Q}(y,\mathsf{b})$$

Remark

In practice the addition of equality axioms reduces the success rates of (instantiation-based) automated theorem provers drastically.

- Done:
 - Scanning and parsing with flex and bison
 - Application logic with two simple strategies
 - SMT encoding to Yices 2
- Ongoing:
 - Integration of Unit Superposition calculus
 - Integration of Z3 for unsatisfiable core and maximal completion
 - Tests and experiments
- Missing:
 - Additional strategies
 - Combinations of strategies
 - Migration to Linux

Optimization



xkcd.com/1691/