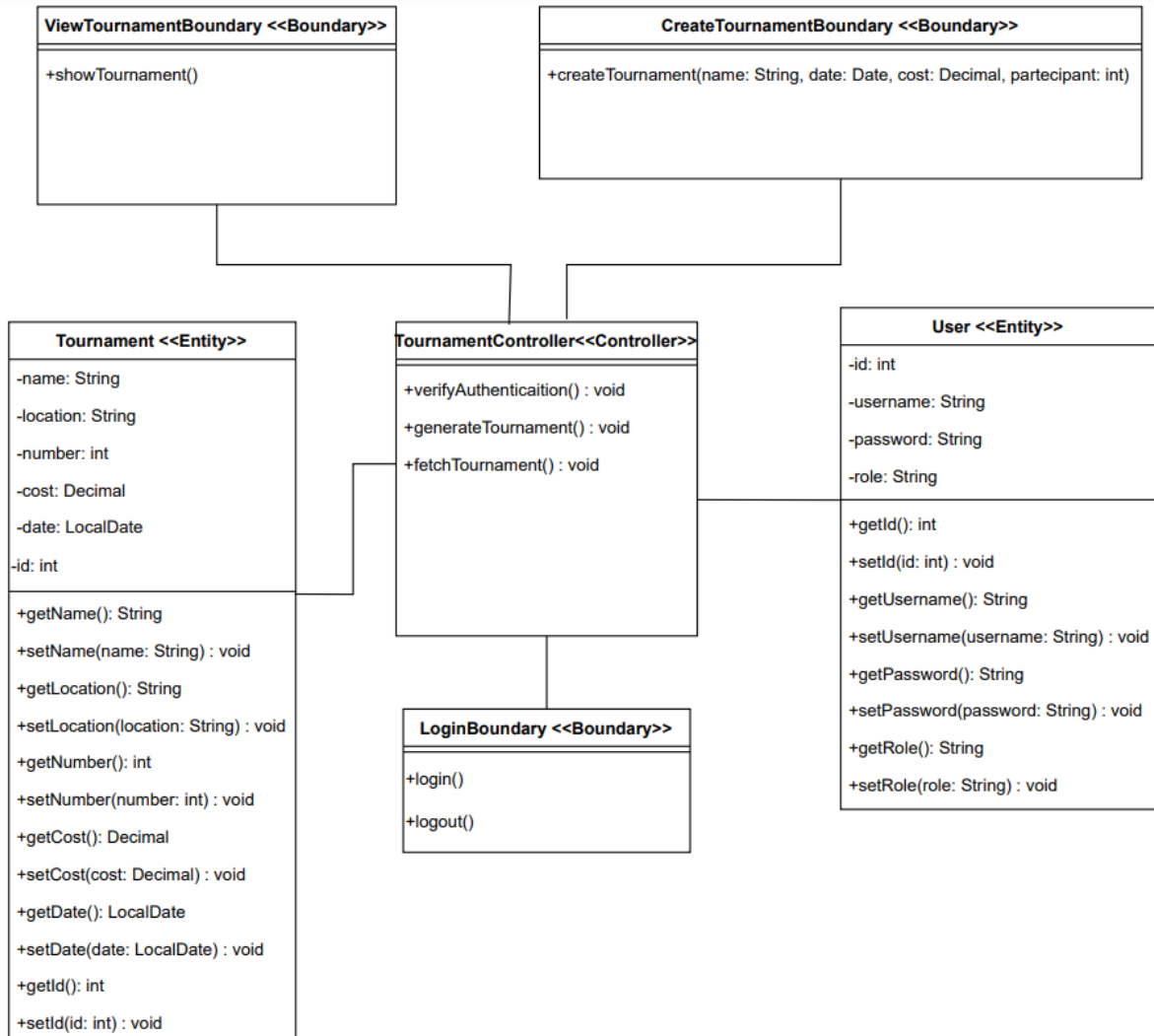
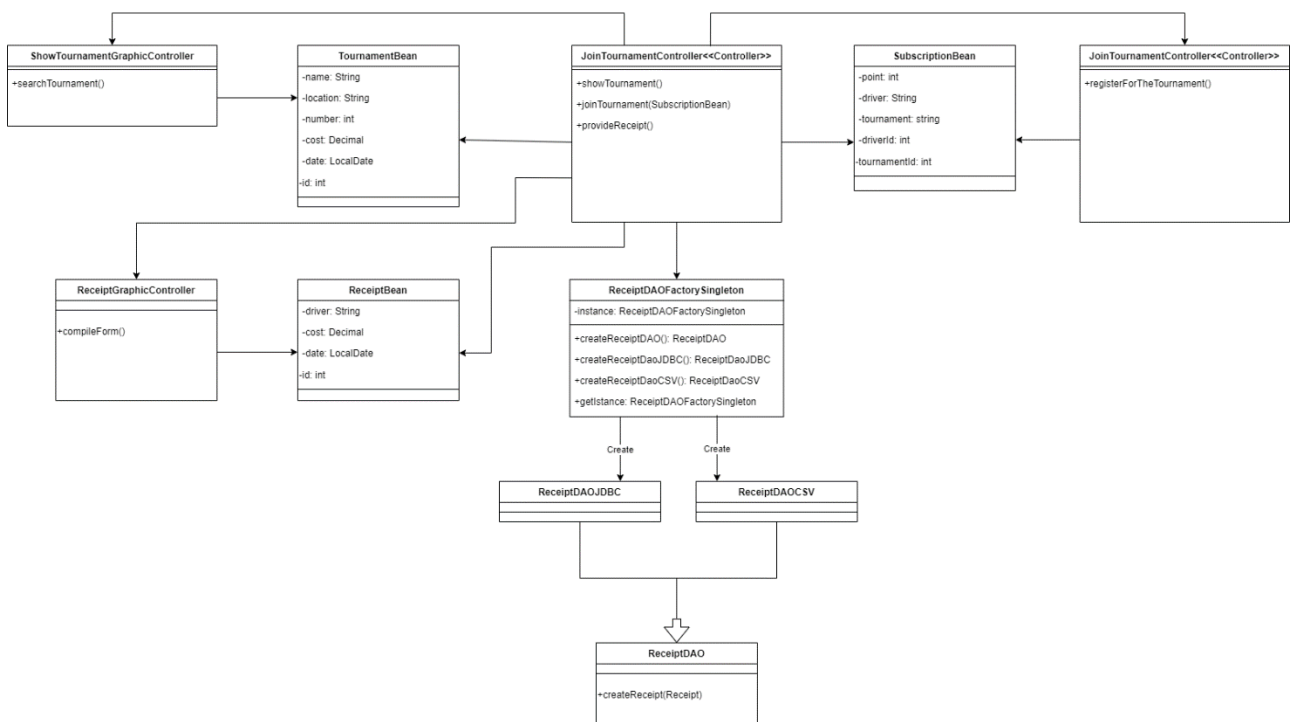


# DESIGN

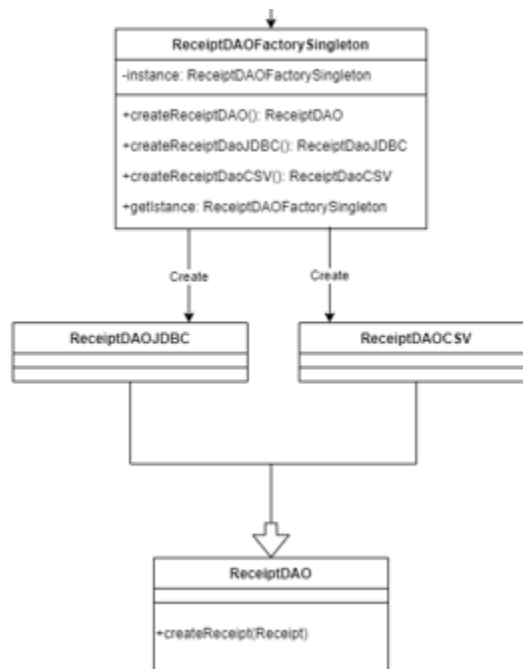
## 1. VOPC



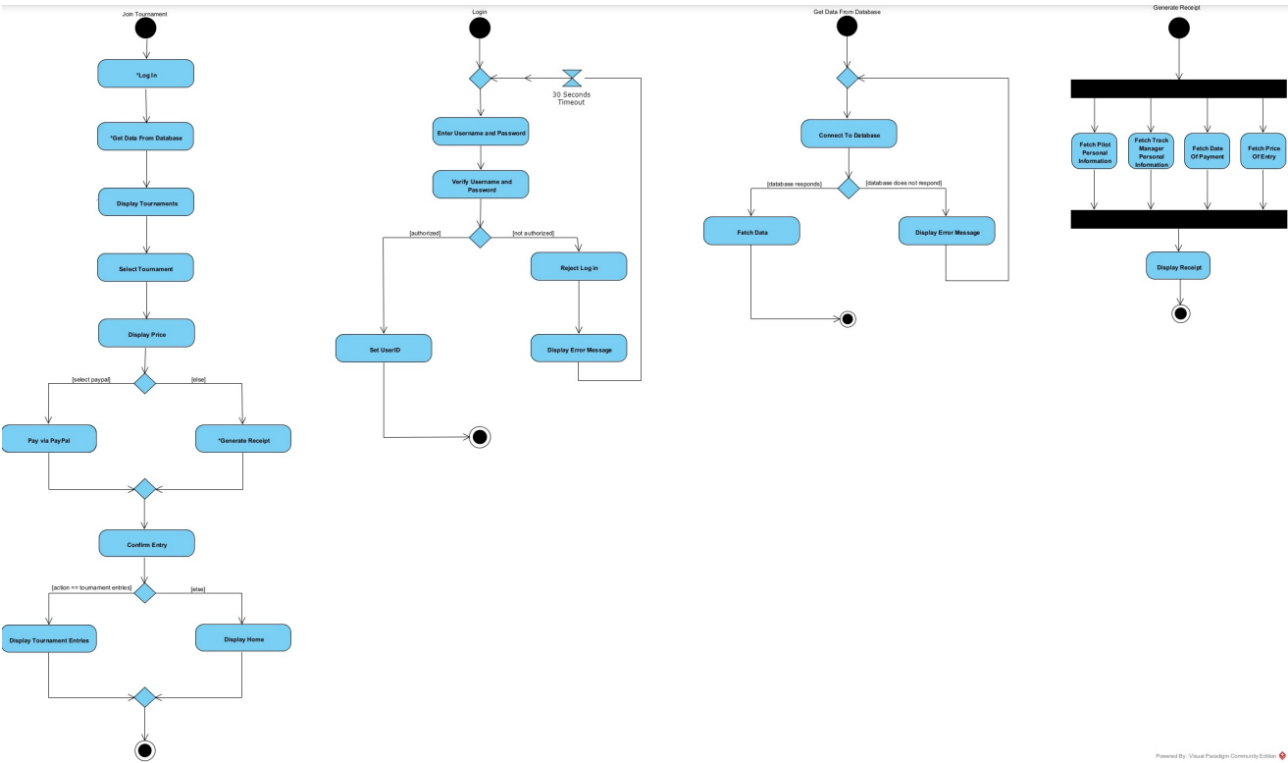
## 2. DLD



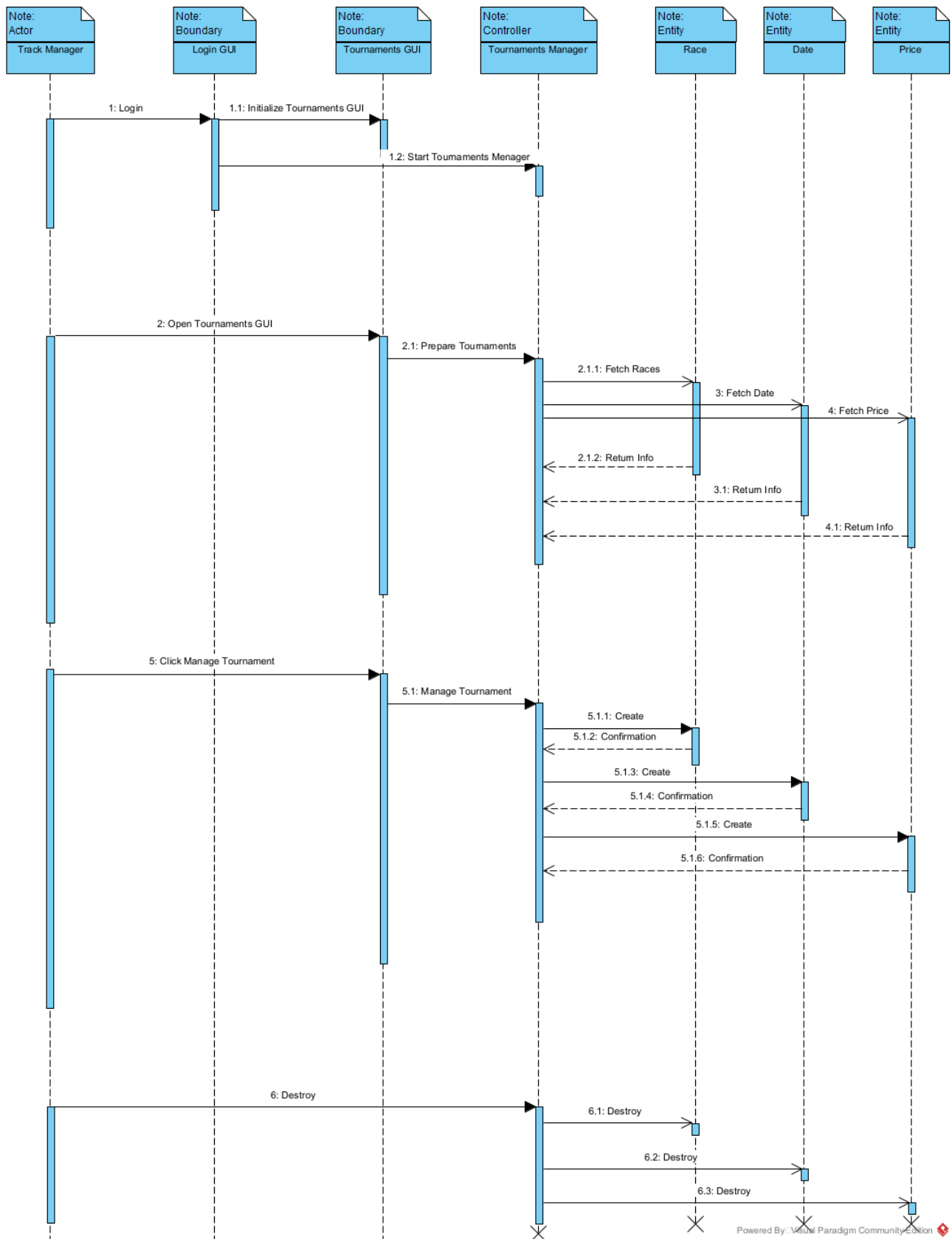
## 3. DESIGN PATTERN



# 4. ACTIVITY DIAGRAM



## 5. SEQUENCE DIAGRAM



## 6. STATE DIAGRAM

