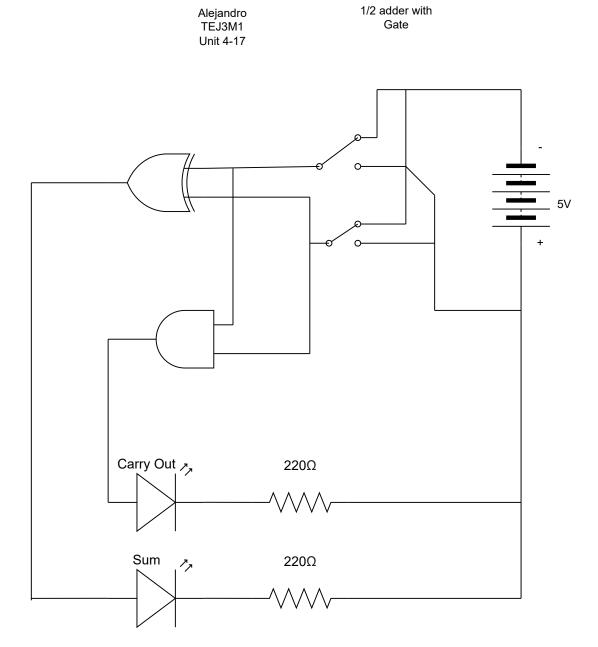
1/2 Add		
Input A		ln
Α	В	
0	0	! !
0	1	
1	0	
1	1	



er with Gate

Output Carry Out

put B Sum