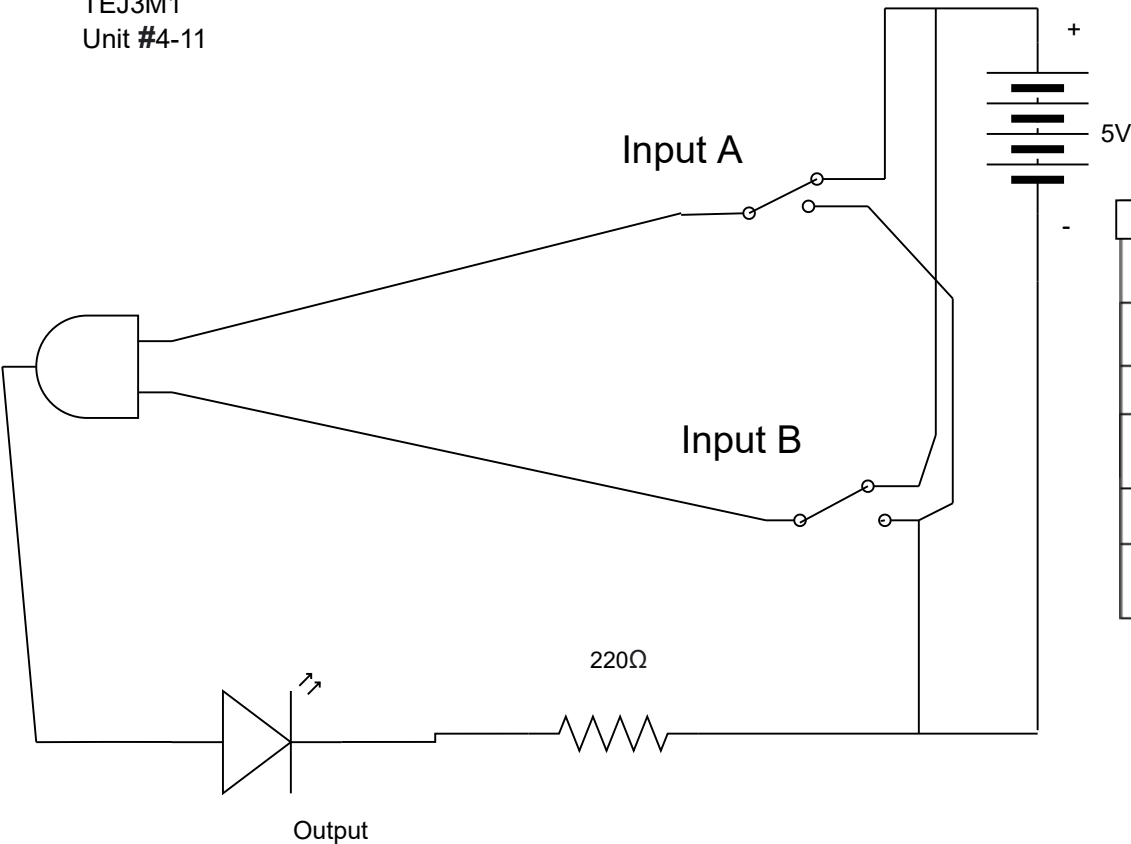


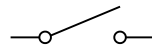
Alejandro Castaneda  
TEJ3M1  
Unit #4-11

AND Gate with Transistor

Alejandro  
TEJ3M1  
Unit #4-11



And Gate with Transistor		
Input A	Input B	Output
0	0	0
0	1	0
1	0	0
1	1	1



Castaneda

### AND Gate with Transistor

