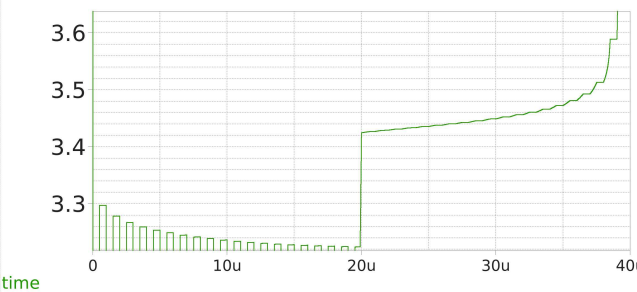
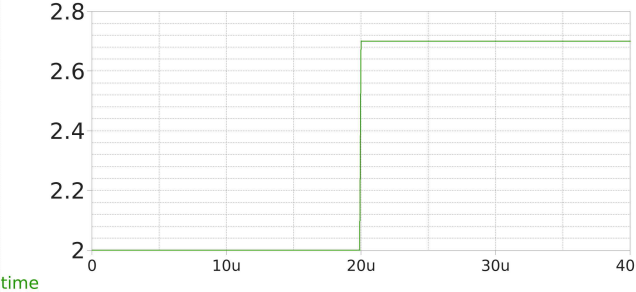


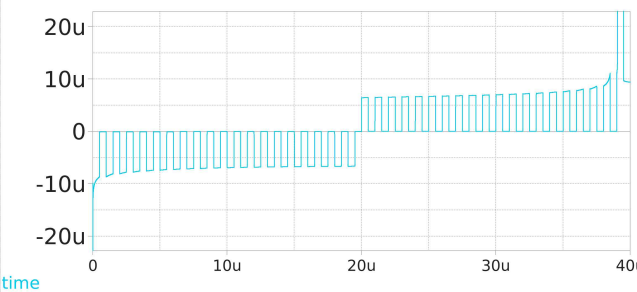
ThicknessFill [nm]



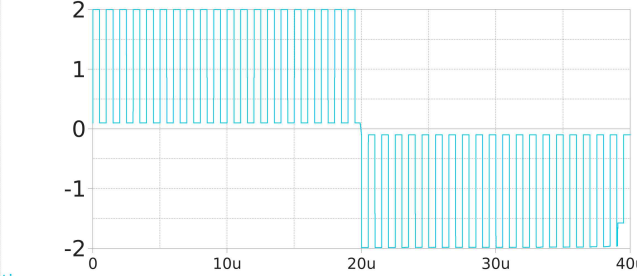
Gate Voltage WL [V]



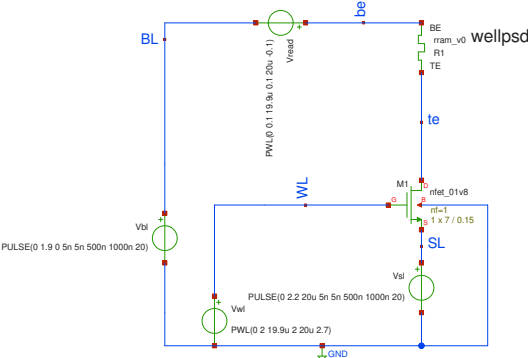
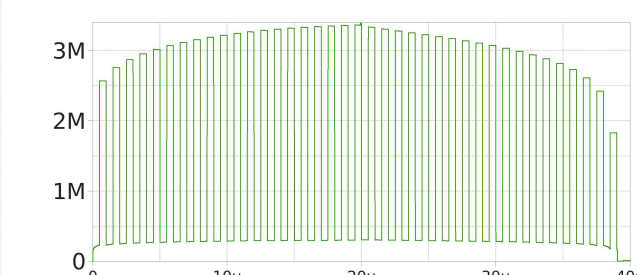
i(vread)



Voltage Difference[Volts]



mem1



NGSPICE

```
.save @m.xml.msky130_fd_pr_nfet_01v8[id]
.control
* Modify according to your specific location
save all
tran 1n 40u
write tb_1T1R_pulses_rram_v0.raw
.endc
```

load waves

