



```
NGSPICE
.options num_threads=8
.save @m.xml.msky130_fd_pr_nfet_01v8[id]
.control
save all
tran 100n 50u uic
write tb_1T1R_rram_v0.raw
.endc

MODELS2
** opencircuitdesign pdks install
.inc /foss/designs/tutoMemristor/rram_v0.spice
```

Corner: tt

load waves

