



load waves

Corner: tt

```
NGSPICE
*.save @m.xml.msky130_fd_pr_nfet_01v8[id]
.control
* Modify according to your specific location
save all
tran 100n 20u
write tb_1T1R_pulses_rram_v0.raw
.endc

MODELS2
*MADE BY JORGE ALEJANDRO JUAREZ LORA IPN

.subckt rram_v0 TE BE
*N1 TE BE rram_v0_model gap_initial=unif(0.9,0.8)
N1 TE BE rram_v0_model
.ends rram_v0

.model rram_v0_model rram_v0_va

.control
pre_osdi /foss/designs/tutoMemristor/rram_v0.osdi
.endc
```

