

# TraceX<sup>®</sup>

User's Manual: Software

Renesas Synergy<sup>TM</sup> Platform

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ThreadX system analysis tool  
V5 for Windows

## User Guide

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## **About This Guide**

This guide contains comprehensive information about TraceX™, the Microsoft Windows-based system analysis tool from Express Logic, Inc.

It is intended for the embedded real-time software developer using ThreadX Real-Time Operating System (RTOS) and add-on components. The developer should be familiar with standard ThreadX FileX, and NetX concepts .

## **Organization**

- |                  |  |
|------------------|--|
| <b>Chapter 1</b> | Contains an basic overview of TraceX and describes its relationship to real-time development.                    |
| <b>Chapter 2</b> | Gives the basic steps to install and use TraceX to analyze your application right out of the box.                |
| <b>Chapter 3</b> | Describes the main features of TraceX.   |
| <b>Chapter 4</b> | Details performance analysis features of TraceX.   |
| <b>Chapter 5</b> | Describes how to set up ThreadX, FileX, and NetX in order to generate a trace buffer that is viewable by TraceX. |

<b>Chapter 6</b>	Describes TraceX events in detail.
<b>Chapter 7</b>	Describes FileX events in detail.
<b>Chapter 8</b>	Describes NetX events in detail.
<b>Chapter 9</b>	Describes USBX events in detail.
<b>Chapter 10</b>	Describes creating custom user events in detail.
<b>Chapter 11</b>	Describes the internal trace buffer in detail.
<b>Appendix A</b>	ThreadX port-specific file with its time-stamp source for gathering trace events.
<b>Appendix B</b>	ThreadX <code>tx_trace.h</code> file that shows implementation details regarding the event trace buffer.
<b>Appendix C</b>	Summarizes command line utilities for converting various file formats into proper TraceX binary files.
<b>Appendix D</b>	Examples of dumping trace files from various development tools.
<b>Index</b>	Topic cross reference.

## Guide Conventions

*Italics*

Typeface denotes book titles, emphasizes important words, and indicates variables.

**Boldface**

Typeface denotes file names, key words, and further emphasizes important words and variables.



Indicates a specific area of interest in screenshots of the TraceX graphic user interface (GUI).



Indicate information of note.

## Customer Support Center

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### Latest Product Information

Visit the Express Logic web site and select the “Support” menu option to find the latest support information, including information about the latest TraceX product releases.

## What We Need From You

To more efficiently resolve your support request, provide us with the following in your email request:

- A detailed description of the problem, including frequency of occurrence and whether it can be reliably reproduced.
- An attached copy of the trace file causing the problem.
- The version of TraceX you are using.
- The version of ThreadX you are using as well as the `_tx_version_id` and `_tx_build_options` variables in ThreadX.
- The version of FileX you are using including the `_fx_version_id` string.
- The version of NetX you are using including the `_nx_version_id` string.

## Where to Send Comments About This Guide

The staff at Express Logic is always striving to provide you with better products. To help us achieve this goal, email any comments and suggestions to the Customer Support Center at

[support@expresslogic.com](mailto:support@expresslogic.com)

Enter “**TraceX User Guide**” in the subject line.

# 1

## ***Introduction to TraceX***

TraceX is a Microsoft system analysis tool that displays system event information gathered by ThreadX running on an embedded target. The user is responsible for transferring the trace buffer stored in RAM in the embedded target to a binary file on the host computer. The user can then open this file with TraceX and graphically analyze the target events, diagnosing system problems and tuning a working application to improve performance and resource management.

This chapter describes the following:

- TraceX Requirements 12
- TraceX Constraints 12

## TraceX Requirements

TraceX requires Windows XP (or above). The system should have a minimum of 192MB of RAM, 2 GB of available hard-disk space, and a minimum display of 1024x768 with 256 colors. In addition, the application must be running on ThreadX V5.0 or later.

TraceX also requires the Microsoft .NET framework be installed, which the TraceX installer does automatically.

## TraceX Constraints

TraceX has the following constraints:

- TraceX files are limited to a maximum of 32,768 events (roughly 1MB ).
- The time-stamp source must have reasonable resolution. If the resolution is too low, the events will overlap. If the resolution is too high, there is potential for long gaps between events.
- TraceX cannot accurately measure intervals between events greater than the timer period.

# 2

## *Installation and Use of TraceX*

This chapter contains a description of various issues related to installation, setup, and usage of the TraceX system analysis tool, including the following:

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## Product Distribution

TraceX is shipped on a single CD-ROM compatible disk. The package includes an installation program **Setup.exe** that automatically runs from the CD. If the TraceX installer does not automatically run, click on the **Setup.exe** program manually to install TraceX. The TraceX package also contains an example directory of pre-built traces that should serve as a good starting point for new TraceX users.

The release notes associated with each new TraceX release can be found in the file **readme\_tracex.txt**. Review this file to see what has changed between successive TraceX releases.

## TraceX Installation Directory

TraceX, by default, is installed in the directory **c:\ExpressLogic\TraceX\_v**, where *v* is the version of TraceX being installed. The default location for TraceX installation may be changed via the installation dialog as shown in the next section.



*TraceX requires the Microsoft .NET framework to operate. The installation of this is done automatically by the TraceX installer via the dialogs shown later in this chapter.*

## TraceX Installation

TraceX is easily installed, as shown in **Figure 2.1** through **Figure 2.7**. The installation dialogs are fairly straightforward, but it is worth noting that **Figure 2.4** shows the dialog for changing the default installation directory for TraceX.



**FIGURE 2.1**

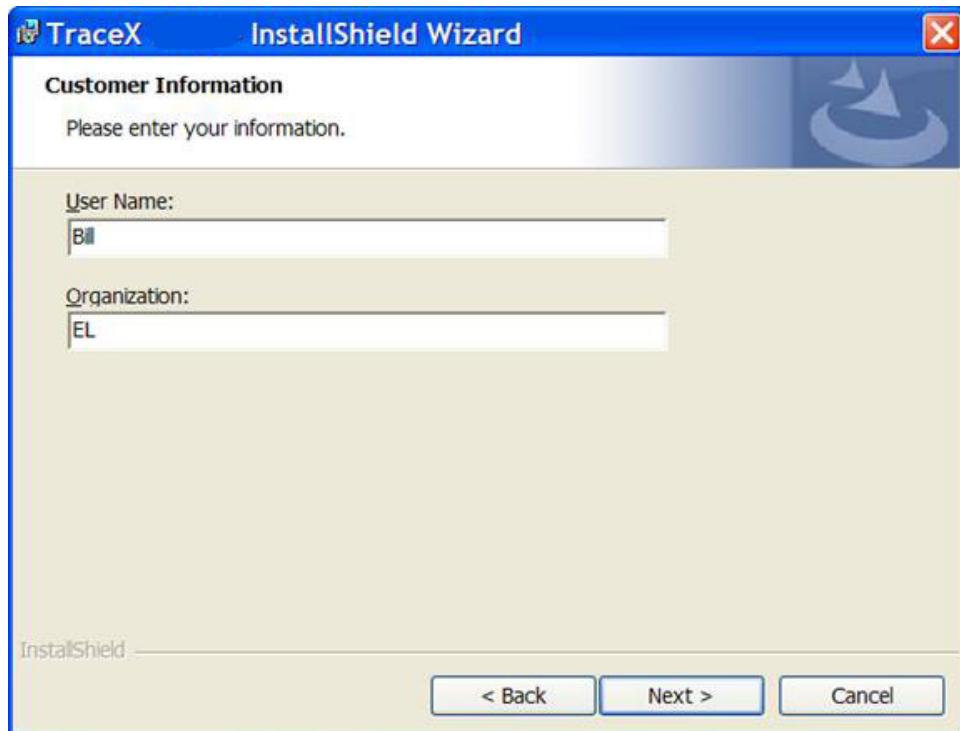
Selecting **Next** button launches the TraceX installation, as shown in **Figure 2.2**.



**FIGURE 2.2**

Selecting **Next** button in **Figure 2.2** indicates the terms of the license agreement are agreed and

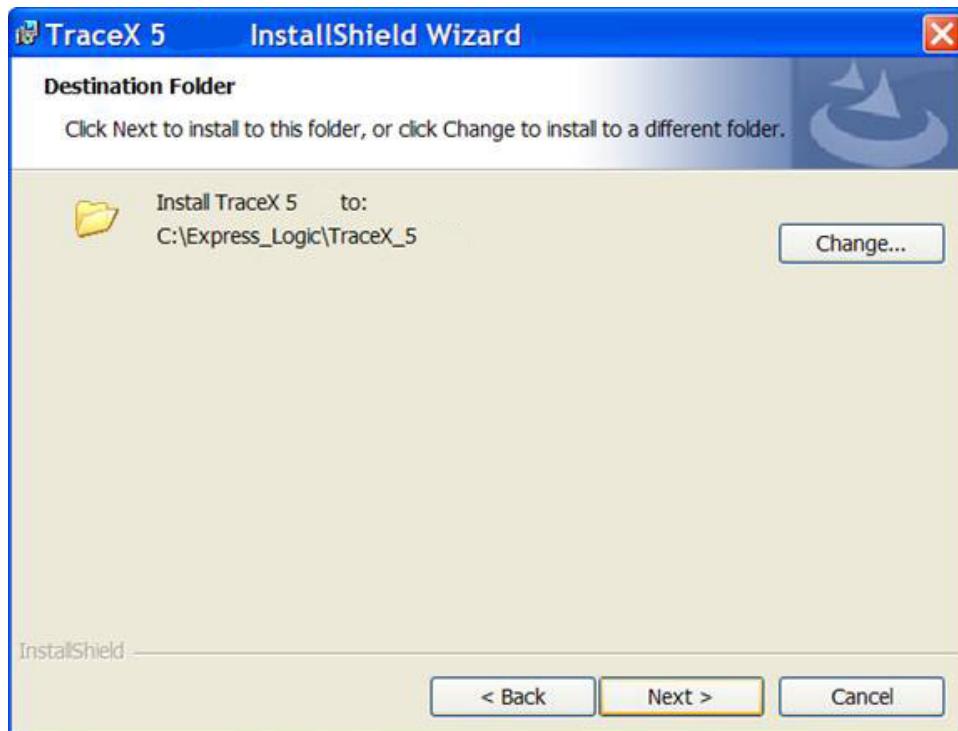
TraceX installation continues, as shown in the **Figure 2.3**.



**FIGURE 2.3**

At this point, the **User Name** and **Organization** should be entered, followed by selection of the **Next**

button, which continues the installation, as shown in the **Figure 2.4**.



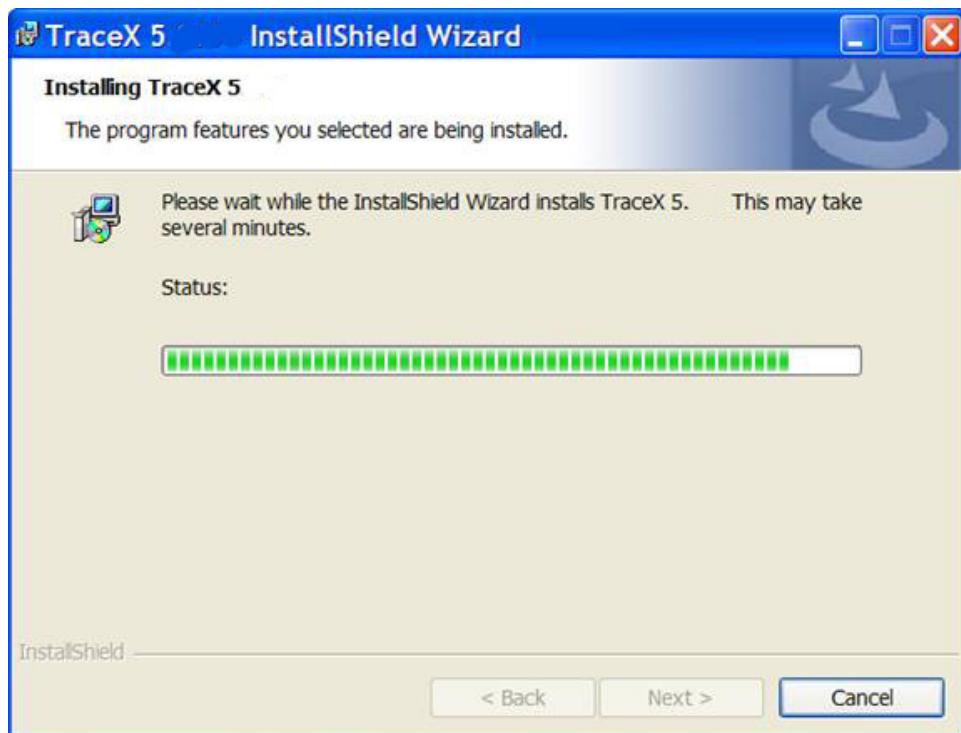
**FIGURE 2.4**

If the default installation path is okay, simply select the **Next** button to continue the installation, as shown in **Figure 2.5**.



**FIGURE 2.5**

If everything is acceptable, select the **Next** button to continue the installation, as shown in **Figure 2.6**.



**FIGURE 2.6**

You should now observe the installation of TraceX on your Windows computer.



**FIGURE 2.7**

Selecting **Finish** button completes the installation and by default launches TraceX. At this point, TraceX is installed and ready to use!

As mentioned previously, TraceX requires .NET v3.5 or higher. If this is not currently installed, the TraceX installation process will automatically install it. **Figure**

2.8 through **Figure 2.10** show the general flow of .NET installation.



**FIGURE 2.8**

Selecting **Install** button launches the Microsoft .NET installation, as shown in **Figure 2.9**.



**FIGURE 2.9**

Further note that the Microsoft .NET framework installation may take as much as eight minutes to complete. After complete, the Microsoft .NET installation requires a reboot. After the reboot, the installation will automatically continue where it left off. If it does not, launch the installation again.

## Using TraceX

Using TraceX is as easy as opening a trace file inside TraceX! Run TraceX via the **Start** button. At this point you will observe the TraceX graphic user interface (GUI). You are now ready to use TraceX to graphically view an existing target trace buffer. This is easily done by clicking **File -> Open**, then entering the binary trace file.



*You can also double-click on any trace file with an extension of **trx**, which will automatically launch TraceX.*

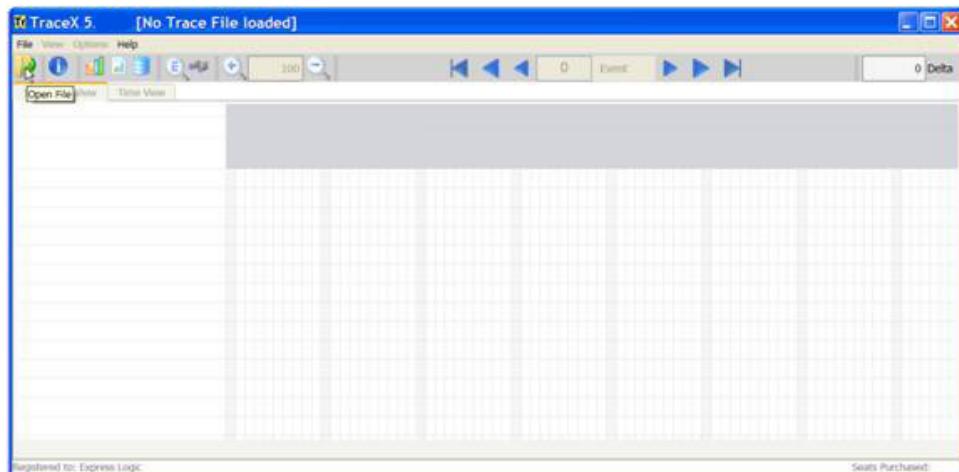


FIGURE 2.10



*Refer to **Chapter 5** for instructions on how to generate trace buffers on the target using ThreadX.*

## TraceX Examples

A series of example trace files with the extension **trx** are found in the **TraceFiles** subdirectory of your installation. These pre-built examples will help you get comfortable with using TraceX on the trace buffers generated by ThreadX running with your application.

One example trace file always present is the file **demo\_threadx.trx**. This example trace file shows the execution of the standard ThreadX demo, as described in Chapter 6 of the *ThreadX User Guide*.



FIGURE 2.11



# 3

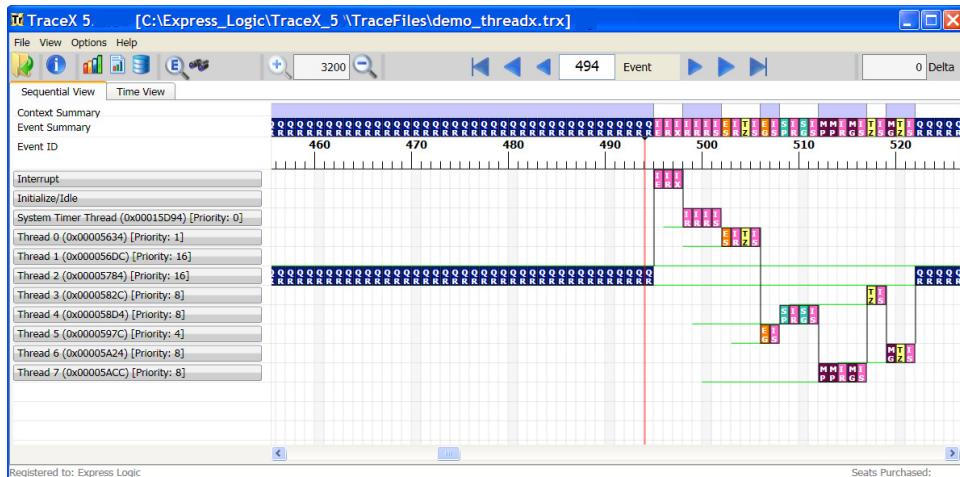
## *Description of TraceX*

This chapter describes the overall functionality of the TraceX system analysis tool, including the overall functionality of its GUI, including the following:

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# Display Overview

**Figure 3.1** shows the main display window of the TraceX system analysis tool. The layout is straightforward—the execution contexts are represented by the vertical elements on the left side; e.g., initialization, interrupt, idle, and the various thread entries. The events that take place in each context are displayed horizontally on the same context line. For example, the **QR** events shown below show that **thread 2** is making successive calls to ***tx\_queue\_receive***.

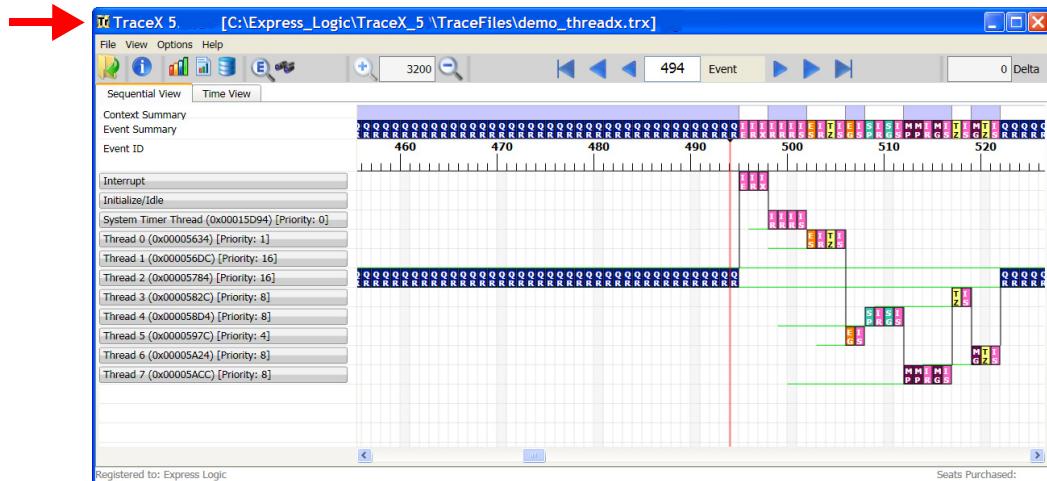


**FIGURE 3.1**

Context changes are represented by the vertical black lines that connect the context lines. The currently selected event is represented by a solid red vertical line. In this example, event 494 is selected.

## Title Bar

The TraceX title bar provides several pieces of useful information. First is the current version of TraceX. Second is the full path of the currently opened trace file. The example in **Figure 3.2** shows **TraceX** version **5.0.1** is displaying the **demo\_threadx.trx** trace file.

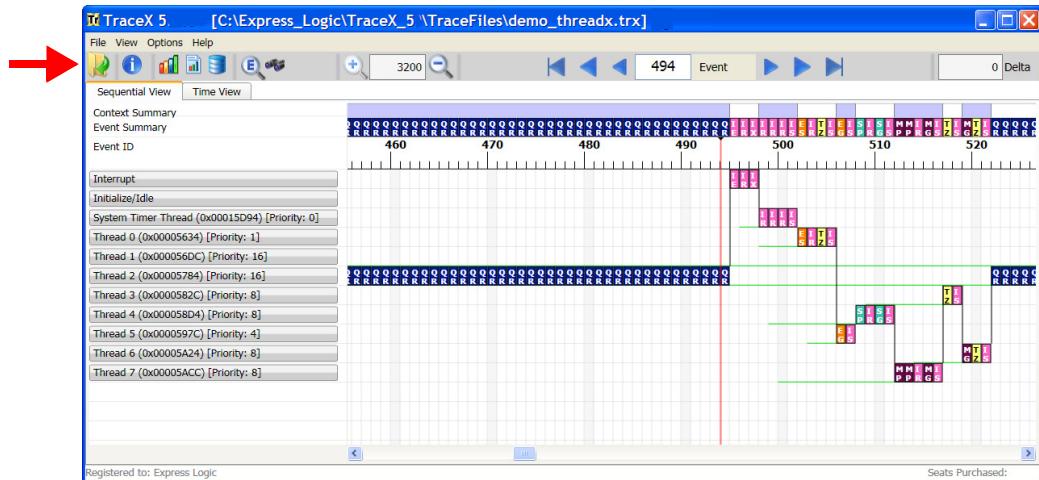


**FIGURE 3.2**

## Tool Bar

The TraceX tool bar provides several buttons to open trace files and control elements of their display.

**Figure 3.3** identifies the TraceX tool bar.



**FIGURE 3.3**

The TraceX tool bar buttons—from left to right—are defined as follows:

Button	Function
	Open a trace file
	Generate multi-core CPU utilization analysis [Multi-Core Only]
	Generate execution profile
	Generate performance statistics
	Generate Thread Stack usage
	Display currently selected event
	Search for events
	Open the TraceX User Guide.
	Zoom in.
 3200	Select percentage of display zoom, where 100% means the entire trace file is displayed within the current view.
	Zoom out.



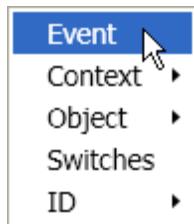
Select first event.



Display previous event page.



Display previous event.



Determine how the next/previous navigation buttons operate. If **Event** is selected, navigation is done on the next/previous event. If **Context** is selected, navigation is done on the next/previous event on the specified context. If **Object** is selected, navigation is done on the next/previous event of the specified object; e.g., events associated with a specific queue. If **Switches** is selected, navigation is done on the next/previous change in context. If **ID** is selected, navigation is done on the next/previous event of the specified event ID.



Display next event.



Display next event page.

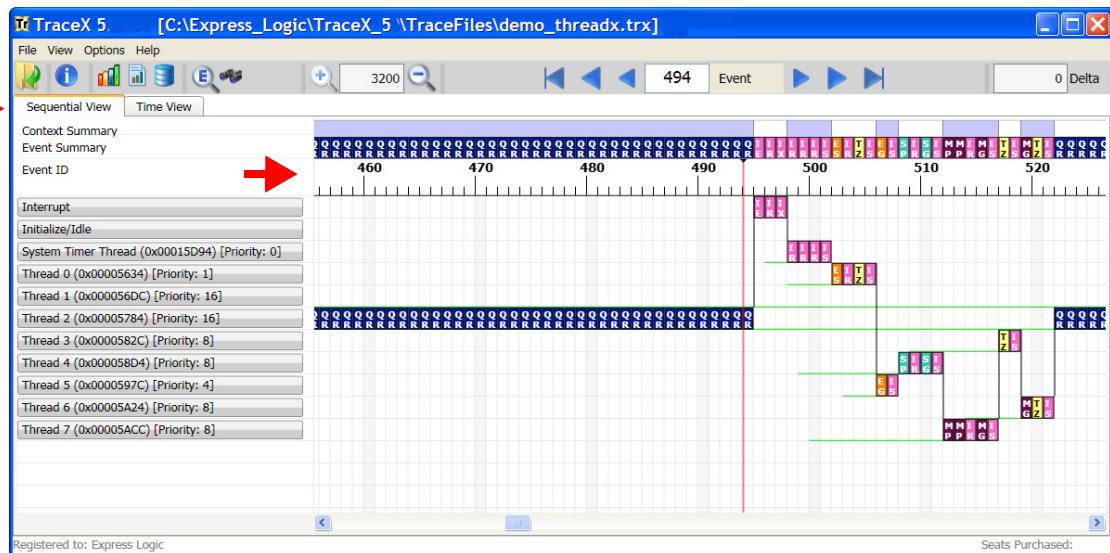


Select last event.

## Display Mode Tabs

TraceX displays system events in two different ways: *sequential* and *time relative*. The default mode is sequential and that is the mode shown in **Figure 3.4**.

Changing the mode is as simple as selecting the **Sequential View** or **Time View** tabs in the TraceX window. **Figure 3.4** shows the **Sequential View** and **Time View** tabs.



**FIGURE 3.4**

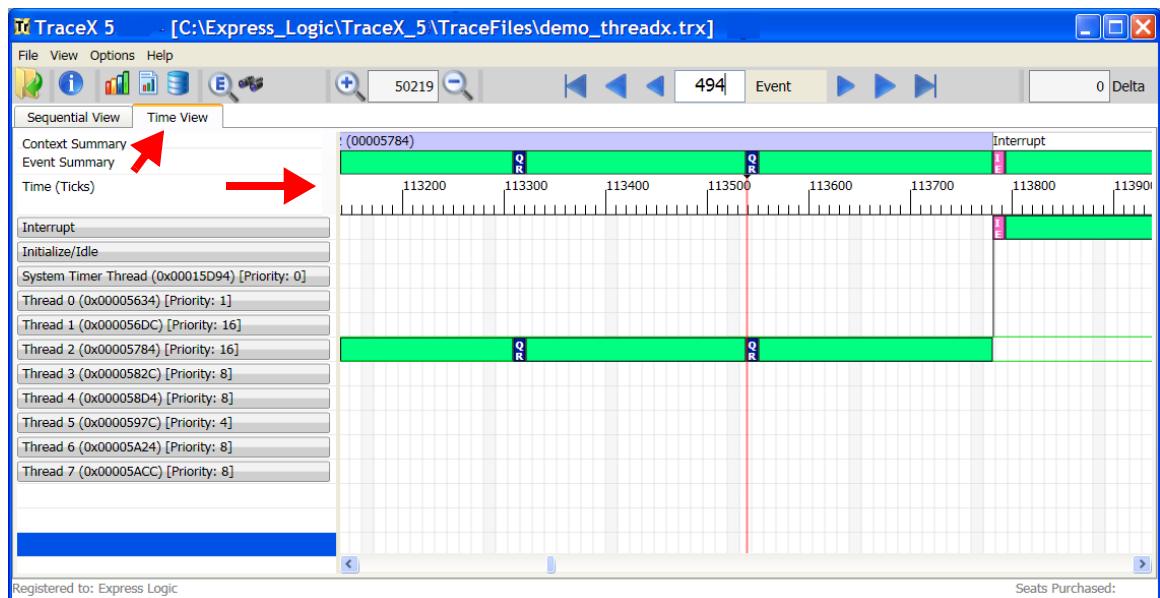
## Sequential View Mode

The sequential view mode is selected by the **Sequential View** tab shown in **Figure 3.4**. This is the default mode. In this mode, events are shown immediately following each other, regardless of the elapsed time between them. Note also the ruler above the display area in **Figure 3.4**. It shows the relative event number from the beginning of the trace.

This mode is the default mode and is useful in getting a good overview of what is going on in the system.

## Time View Mode

The time view mode is selected by the **Time View** button. **Figure 3.5** shows the same event trace as **Figure 3.4** except in time view mode. In this mode, events are shown in a time relative manner, with the solid green bar being used to show execution between events. This mode is useful to see where the bulk of processing is taking place in the system, which can help developers tune their system for greater performance and/or responsiveness.



**FIGURE 3.5**

Note also the ruler above the event display in **Figure 3.5**. This ruler shows relative ticks from the beginning of the trace, as derived from the time stamp instrumented in the event trace logging inside of ThreadX. If the time stamps are too close (low frequency timer), the events will run together. Conversely, if the time stamps are too far apart (high frequency timer), then the events will be too far apart. Choosing the right frequency time stamp is an important consideration in making the time relative view meaningful.

## System Summary Line

TraceX also provides a single summary line (the top context in **Figure 3.6**) that includes all events on the same line. This makes it easy to see an overview of a complex system. The summary bar is especially beneficial in systems that have many threads. Without such a summary line, you would have to

follow complex system interactions using the vertical scroll bar to follow the context of execution.

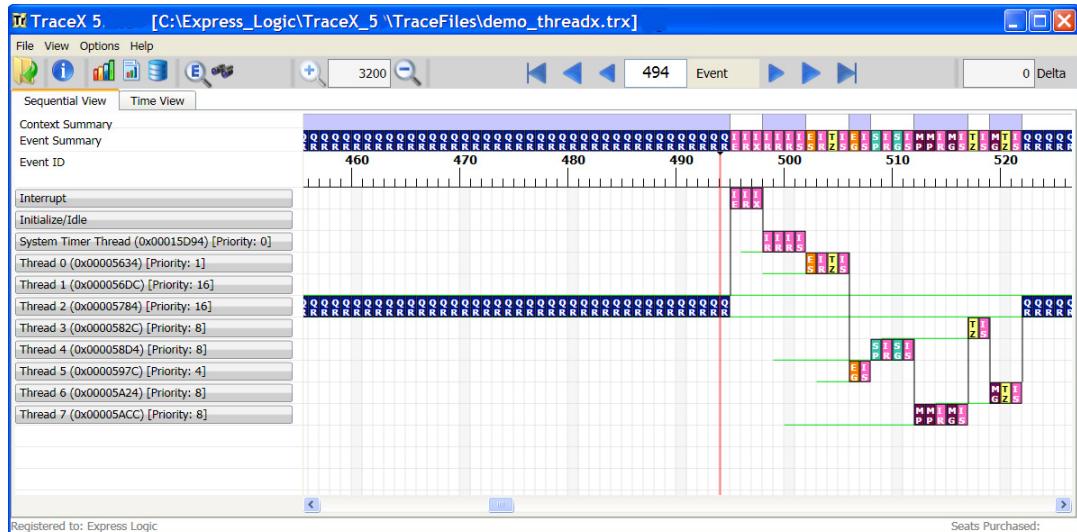


FIGURE 3.6

The summary line contains a summary of the context as well as the corresponding event summary underneath. In the example shown in **Figure 3.6**, it is easy to see that **thread 2** is executing and interrupted. The interrupt results in preemption by **thread 3**, **thread 6**, **thread 4**, and **thread 7**, after which **thread 2** resumes execution.

## System Contexts

TraceX lists the system contexts on the left-hand side of the display, as shown in **Figure 3.7**. Events that occur in a particular context are displayed on the horizontal line to the right of that context. In this way, you can easily ascertain which context the event

occurred as well as follow that context line to see all the events that occurred in a particular context.

The first two context entries are always the ***Interrupt*** and ***Initialize/Idle*** contexts. ***Interrupt*** context represents all system events made from Interrupt Service Routines (ISRs). ***Initialize/Idle*** context represents two contexts in ThreadX. Events that occur during ***tx\_application\_define***, are ***Initialize*** events and are displayed on the ***Initialize/Idle*** context. If the system is idle and thus no events are occurring, the green bar representing ***Running*** in the time view is drawn on the ***Initialize/Idle*** context.

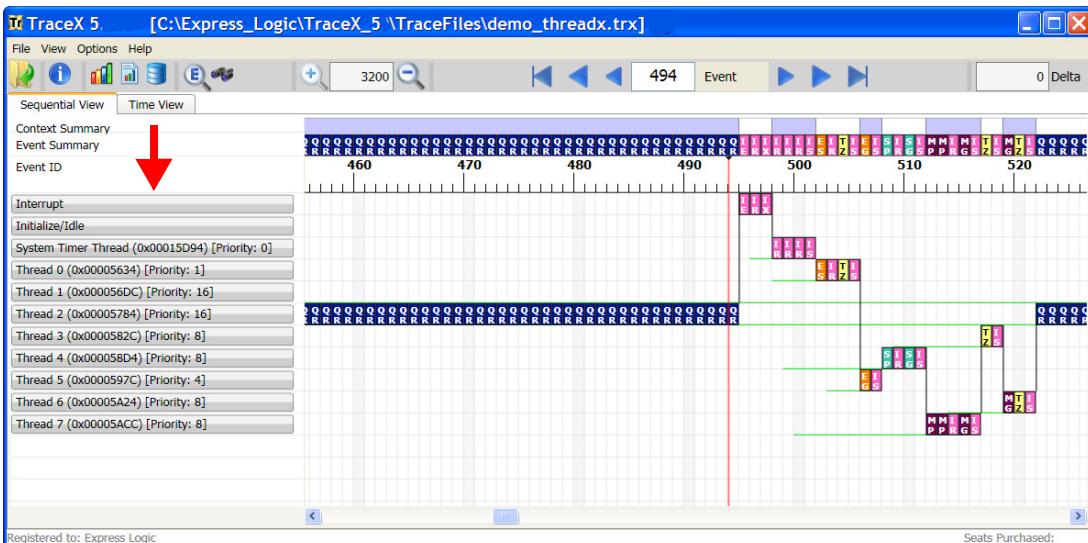
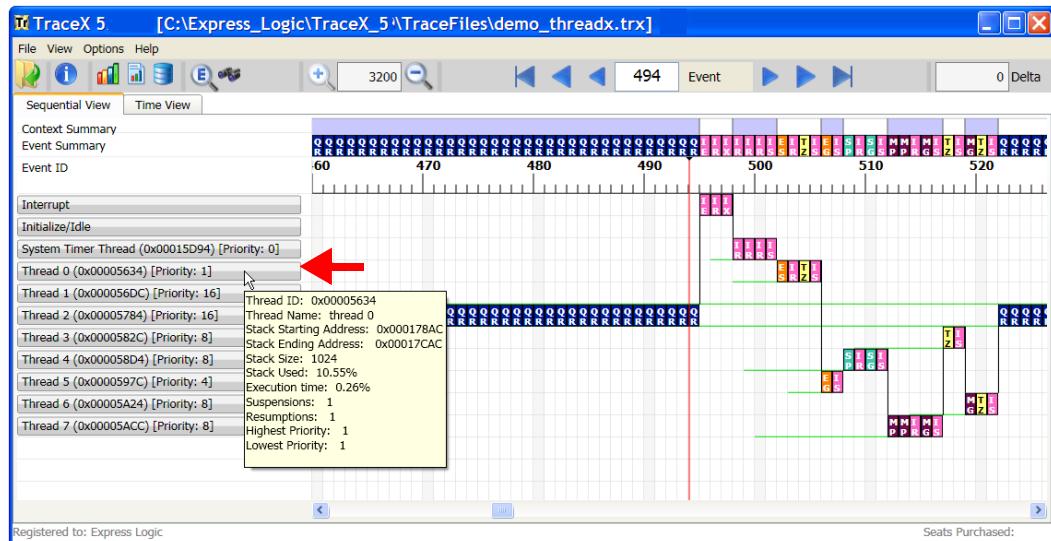


FIGURE 3.7

In the example in **Figure 3.7**, there are nine thread contexts, starting from the ***System Timer Thread*** context. Additional information about an individual context is available by placing the mouse on that context. The additional information includes the thread's starting stack address, ending stack

address, total size, percent used, relative execution percentage, number of suspension, resumptions, and its highest and lowest priority during the trace.

**Figure 3.8** shows information for **thread 0**.

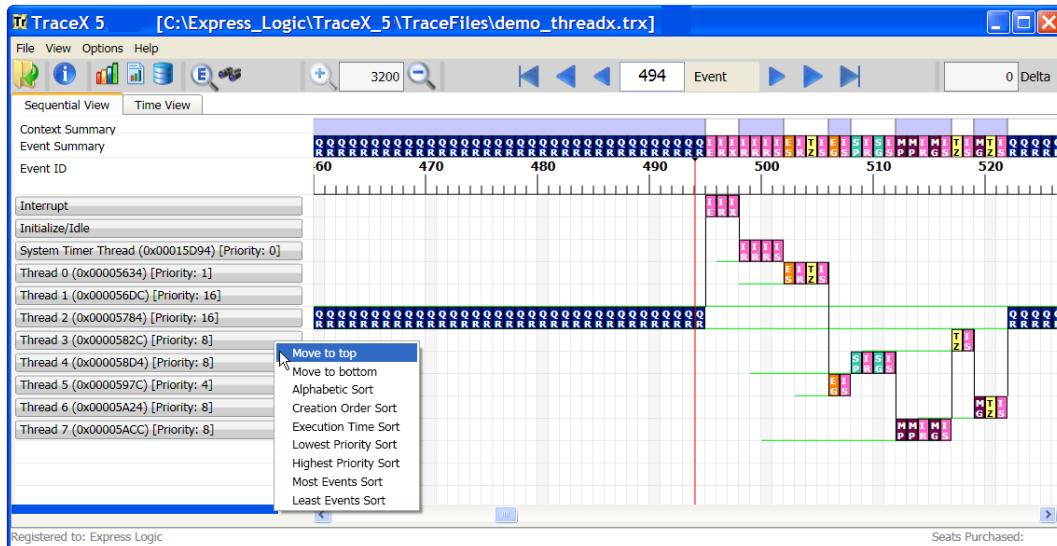


**FIGURE 3.8**

Contexts may also be moved to group those of greater interest. This is accomplished by dragging and dropping the context or right-clicking on the context. Right-clicking on the context yields a dialog

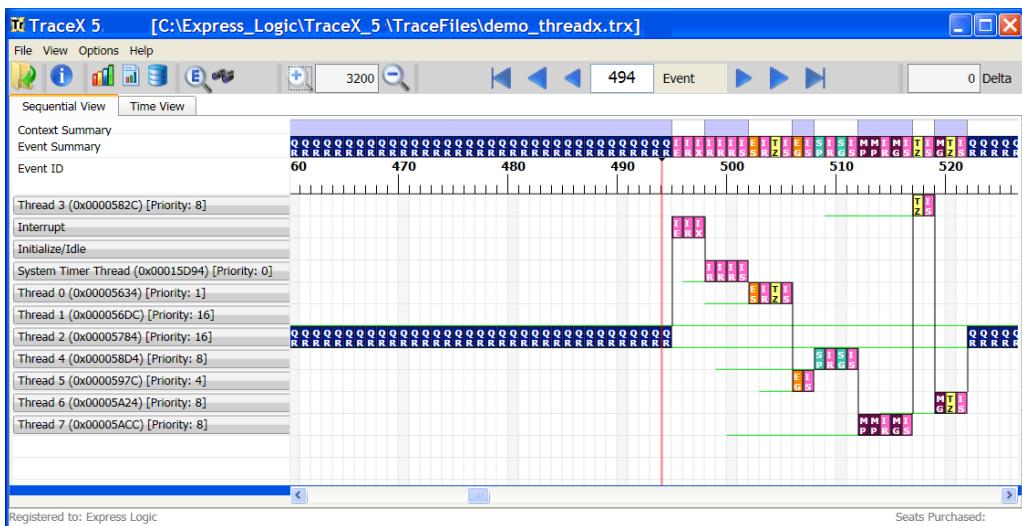
for moving the context to the top or the bottom.

**Figure 3.9** shows the reorder dialog for **thread 3**.



**FIGURE 3.9**

Selecting **Move to top** results in the **thread 3** context being moved to the top of the context list, as shown in **Figure 3.10**.

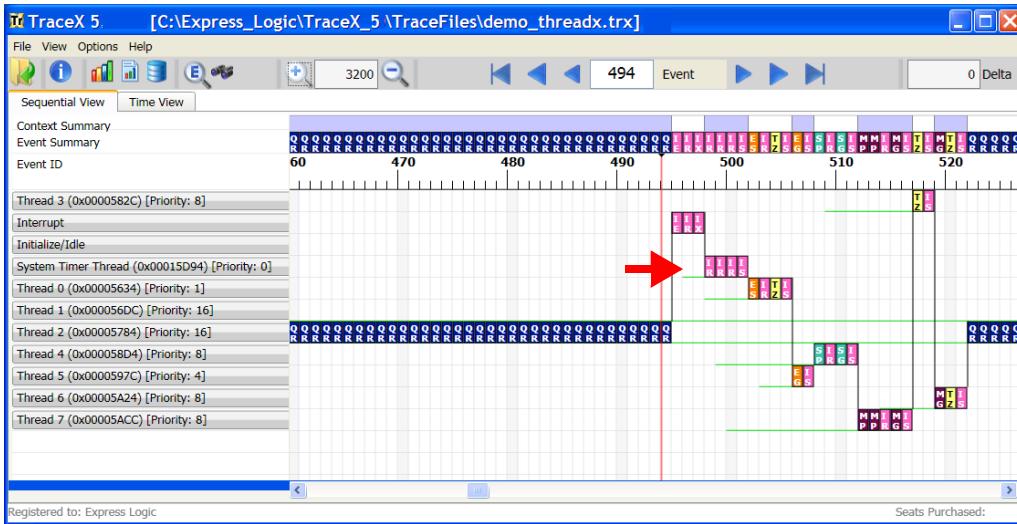


**FIGURE 3.10**

## Thread Status Information

When enabled, TraceX displays the status of each thread via a colored line on the thread's context. A green line indicates that the thread is in a “ready” state, while a line of any other color indicates the thread is suspended. For suspended threads, the color of the line indicates the type of ThreadX object that the thread is suspended on. For example, in **Figure 3.11** the green line on the **System Timer Thread**'s context starting at event 496 shows that the **System Timer Thread** is ready. Prior to event 496 and after event 502, the absence of the green line

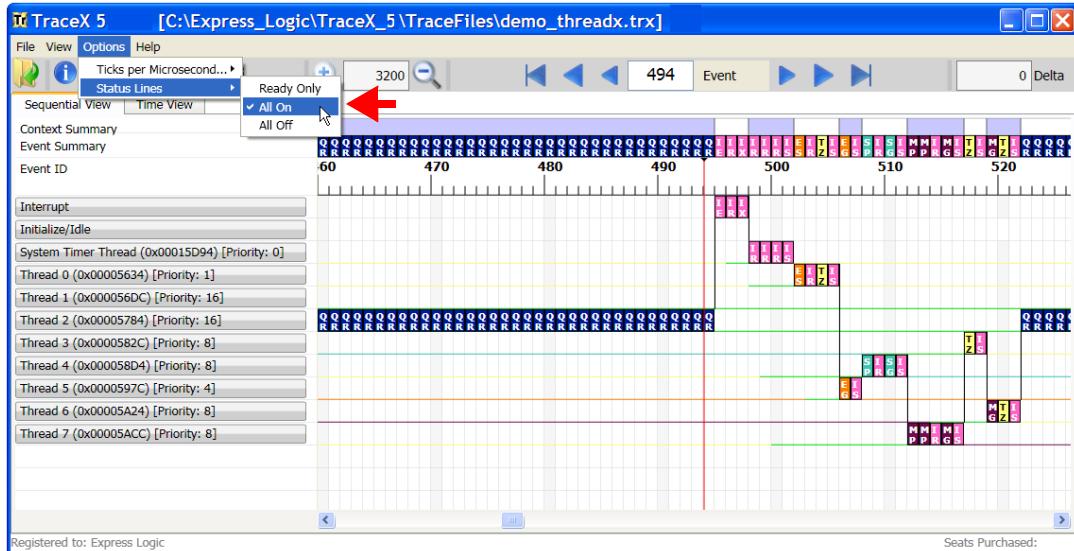
indicates that the **System Timer Thread** is suspended.



**FIGURE 3.11**

There are three modes of thread status display, available via the **Options -> Status Lines** menu as shown in **Figure 3.12**. The **Ready Only** option only shows the ready (green) status lines, but does not display any suspension status lines. This is the default option for TraceX. The **All On** option enables the display of all status lines (ready and suspension).

Finally, the **All Off** option disables the display of all status lines.



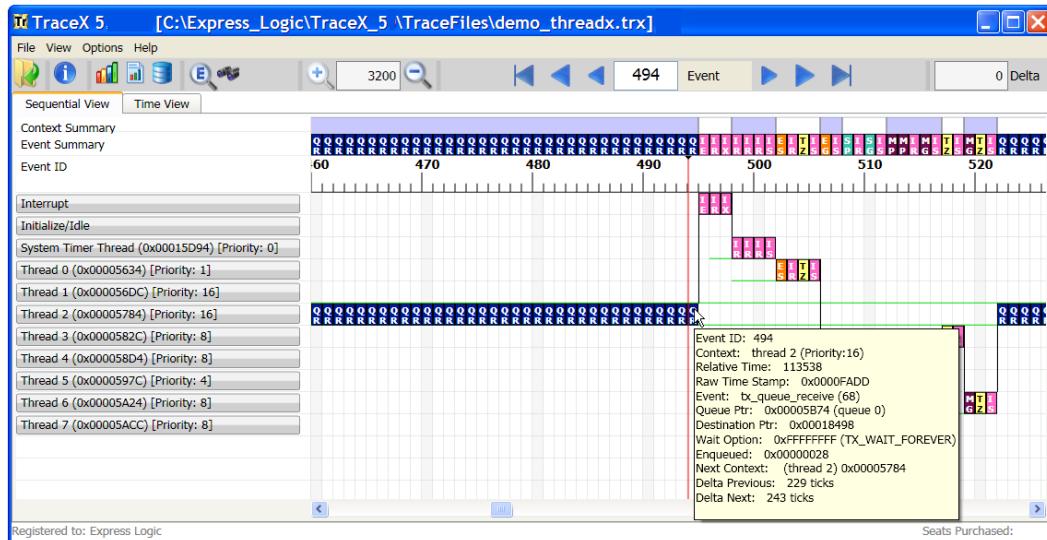
**FIGURE 3.12**

## Event Information Display

TraceX provides detailed information on some 600 run-time events, including ThreadX, FileX, NetX, NetX Duo, and USBX API calls and internal events. TraceX also supports up to an additional 61,439 unique user-defined events.

Regardless of whether sequential or time display mode is selected, a mouse-over on any event in the display area results in detailed event information displayed near the event. The mouse-over event

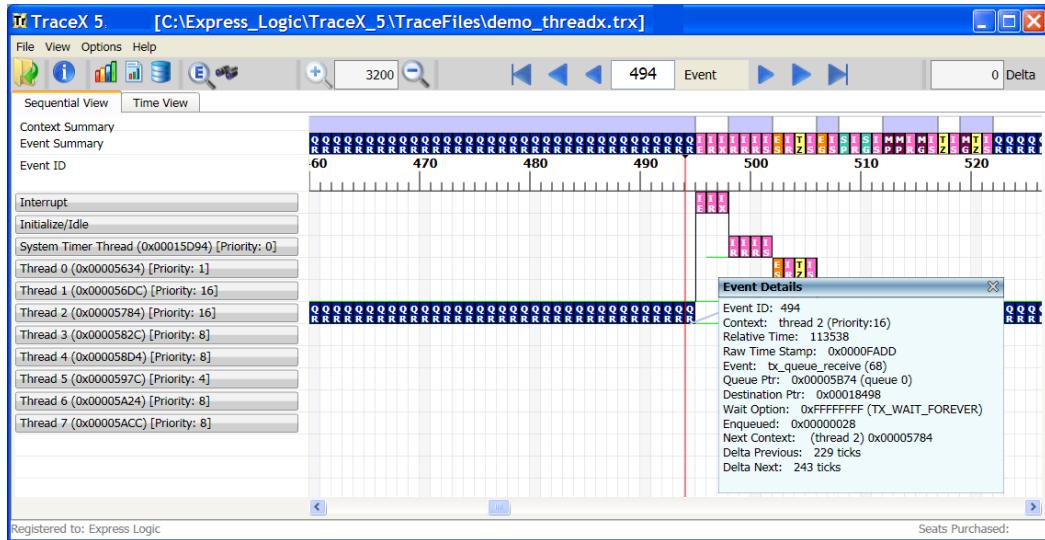
494 in the demonstration ***demo\_threadx.trx*** trace file is shown in **Figure 3.13**:



**FIGURE 3.13**

Each event displayed contains standard information about **Context** and both the **Relative Time** and **Time Stamp**. The Context field shows what context the event took place in. There are exactly four contexts: thread, idle, ISR, and initialization. When an event takes place in a thread context, the thread name and its priority at that time is gathered and displayed as shown above. The **Relative Time** shows the relative number of timer ticks from the beginning of the trace. The **Raw Time Stamp** displays the raw time source of the event. Finally, all event-specific information is displayed. This information is detailed throughout the remainder of this chapter.

Detailed event information is also available by double clicking on any event. Double clicking on event 494 is shown in **Figure 3.14**:



**FIGURE 3.14**

Being able to view multiple events at once gives the user a much richer view of what happened. Seeing them side by side is quite useful since many events are interrelated. This is accomplished by double-clicking on multiple events.

## Current Event Display

TraceX displays the current event—in a separate window—when selected by the user via **View -> Current Event** or clicking on the current event button on the toolbar, as shown in **Figure 3.15**. After selected, TraceX displays the currently selected

event in a stand-alone window and refreshes this window whenever another event is selected. For example, **Figure 3.16** shows the contents for currently selected event 494.

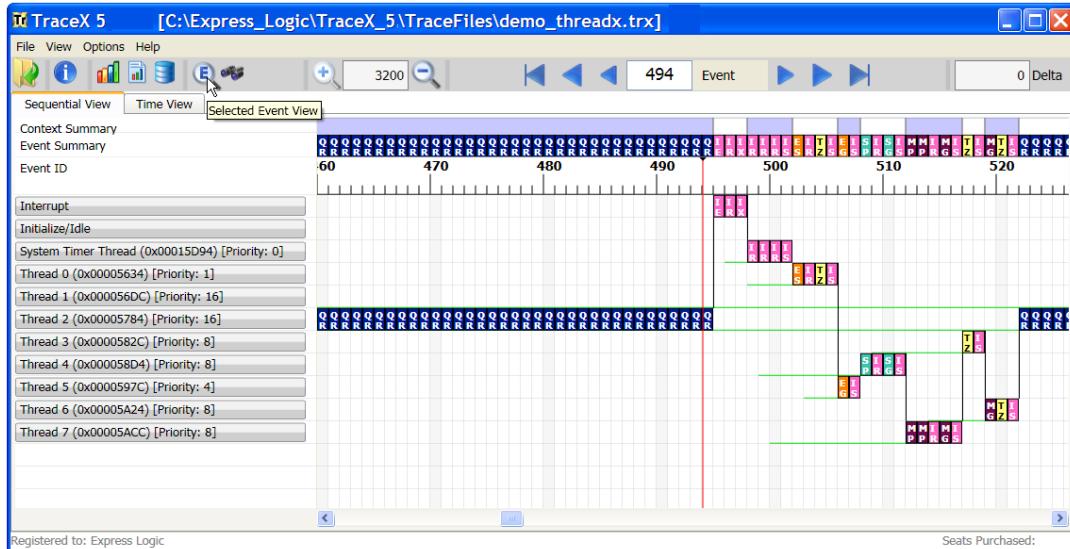


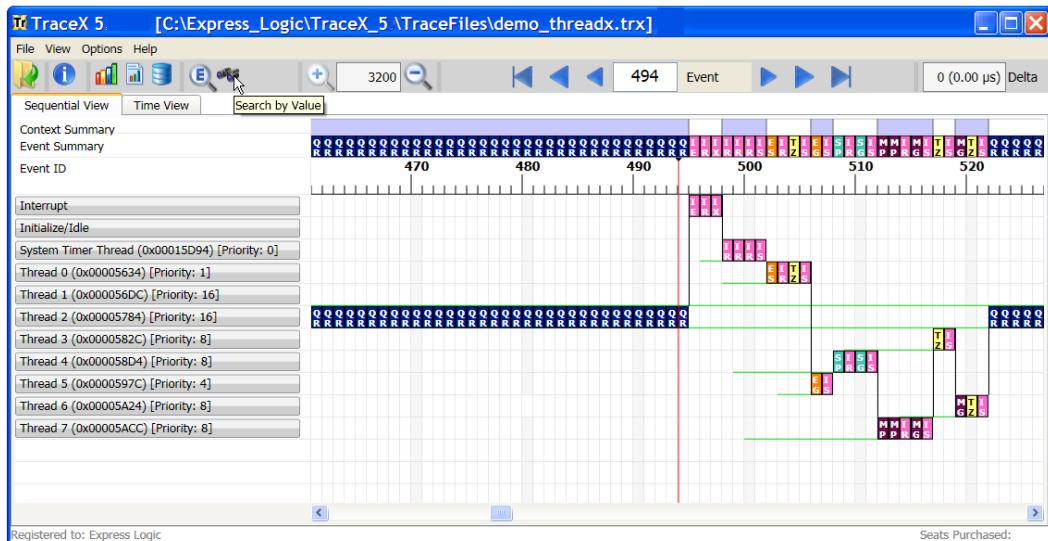
FIGURE 3.15



FIGURE 3.16

# Event Searching

TraceX provides an extensive event search capability. The event ID and information fields of each event are the primary search parameters. Not specifying a value for a search parameter indicates that parameter effectively removes that parameter from the search. In addition, the search can be done such that any parameter found will satisfy the search or all parameters must be found to satisfy the search. The search may also be restricted to a particular context or cover all contexts in the trace. Invoking the event search is done by selecting the **Search by Value** button on the toolbar, as shown in **Figure 3.17**. When selected the search dialog is displayed, which specifies all the parameters for the search. The **Next** and **Previous** buttons in the search dialog can then be used to find the next and previous events that match the specified search criteria. **Figure 3.18** shows the search dialog.



**FIGURE 3.17**

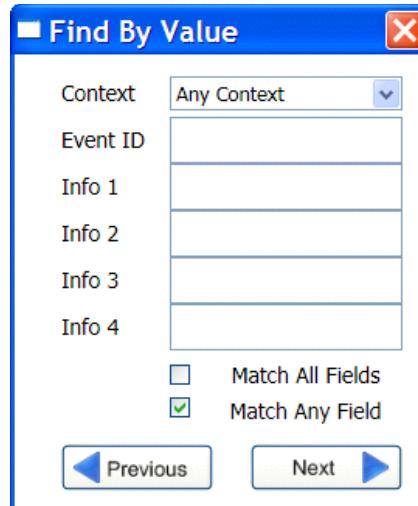
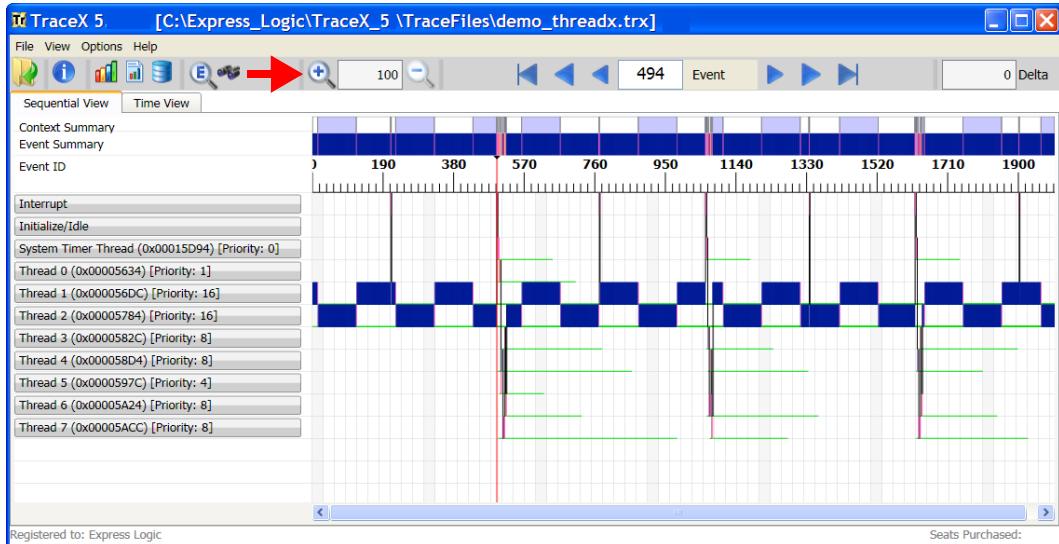


FIGURE 3.18

## Zooming In and Out

By default, TraceX displays the events at their full size. You may zoom in or zoom out as desired. Zooming out is useful to see the overall events captured in the trace, while zooming in is useful in conditions where the events overlap because of the resolution of the time stamp source. **Figure 3.19**

shows the **demo\_threadx.trx** file zoomed out so that 100% of the trace file is shown.



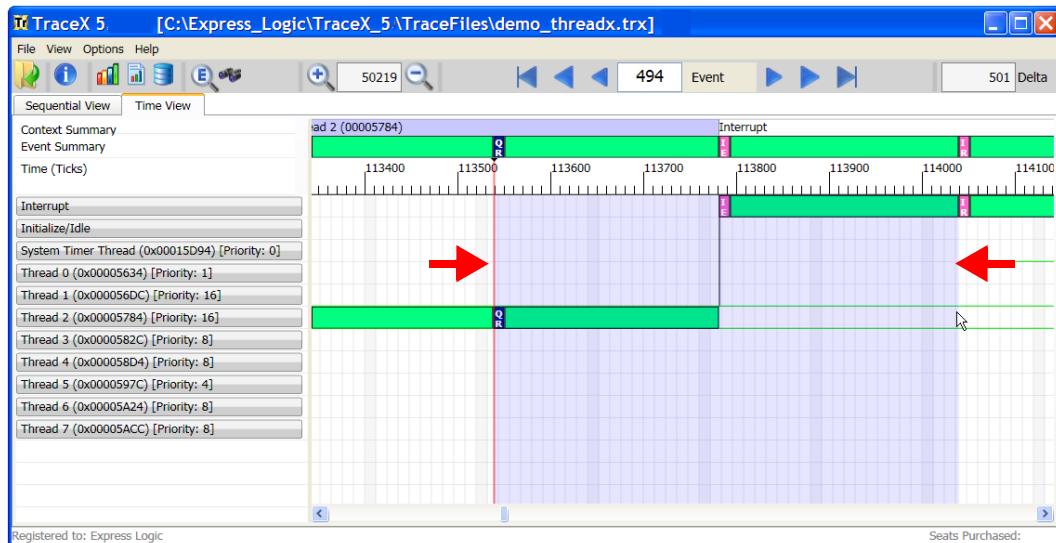
**FIGURE 3.19**

When zoomed out at 100% to show the entire trace within the current display page, it is easy to see all the context execution captured in the trace as well as the general events occurring within those contexts. Notice in **Figure 3.12** that **thread 1** and **thread 2** execute most often. The blue coloring for their events also suggests that these threads are making queue service calls (queue events are blue in color).

Restoring to a full icon view is equally easy; Either the zoom-in button may be selected repeatedly or some factor of 100 may be entered.

## Delta Ticks Between Events

Determining the number of ticks between various events in TraceX is easy—click on the starting event and drag the mouse to the ending event. The delta number of ticks between the events shows up in the upper right-hand corner of the display, as shown in **Figure 3.20**.

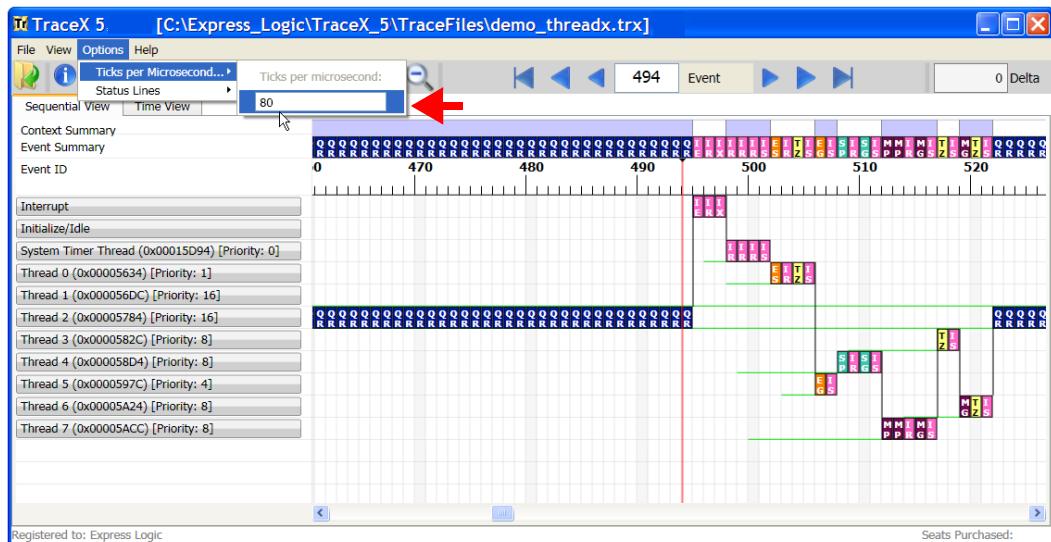


**FIGURE 3.20**

The delta ticks shown in **Figure 3.20** show that 501 ticks have elapsed between event 494 and event 496. This could also be calculated manually by looking at the relative time stamps in each event and subtracting, but using the GUI is easy and instantaneous.

## Actual Time Display

When enabled, TraceX displays the actual time in microseconds in **Time View** and for the various delta time information displayed by TraceX. By default, the actual time display is disabled. To enable the actual time display, the number of ticks per microsecond must be entered via the **Options -> Ticks per Microsecond** menu selection (the value to enter is determined by the hardware timer source used for the TraceX event logging on the target). **Figure 3.21** shows a selection of 80 ticks per microsecond. After this selection, the TraceX tick information is also translated into microseconds, as shown in **Figure 3.22**.



**FIGURE 3.21**

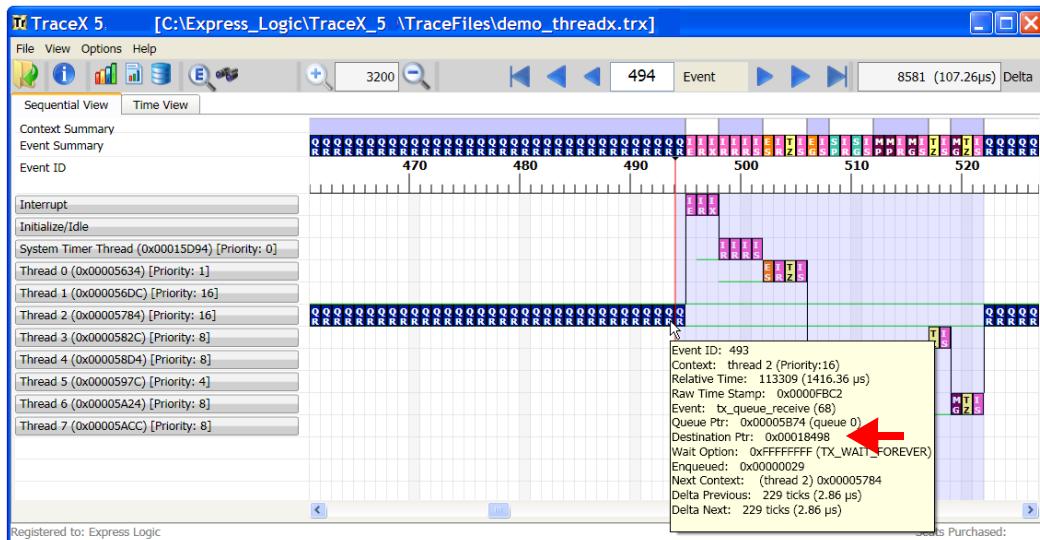


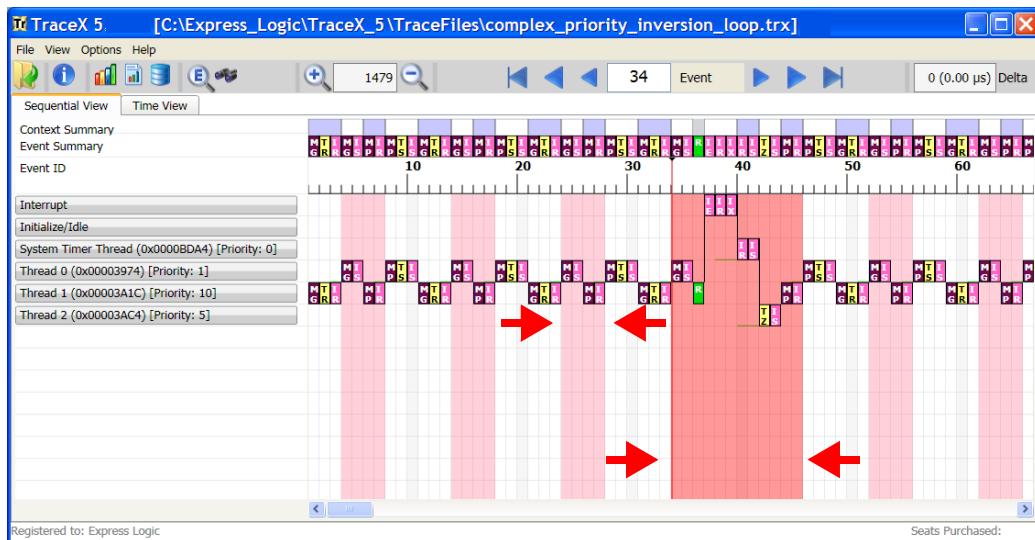
FIGURE 3.22

## Priority Inversions

TraceX automatically displays priority inversions detected in the trace file. Priority inversions are defined as conditions where a higher-priority thread is blocked trying to obtain a mutex that is currently owned by a lower-priority thread. This condition is termed *deterministic*, because the system was set up to operate in this manner. To inform the user, TraceX shows *deterministic* priority inversion ranges as a light salmon color.

TraceX also displays *non-deterministic* priority inversions. These priority inversions differ from the *deterministic* priority inversions in that another thread of a different priority level has executed in the middle of what was a *deterministic* priority inversion, thereby

making the time within the priority inversion somewhat *non-deterministic*. This condition is often unknown to the user and can be very serious. In order to alert the user of this condition, TraceX shows *non-deterministic* priority inversions as a brighter salmon color. **Figure 3.23** shows both *deterministic* and *non-deterministic* priority inversions.



**FIGURE 3.23**

**Figure 3.15** shows a *deterministic* priority inversion from event 24 through event 27. In this range, the higher-priority **thread 0** blocks on a mutex owned by a lower-priority **thread 1**. At event 27, **thread 1** releases the mutex and thus ends the priority inversion.

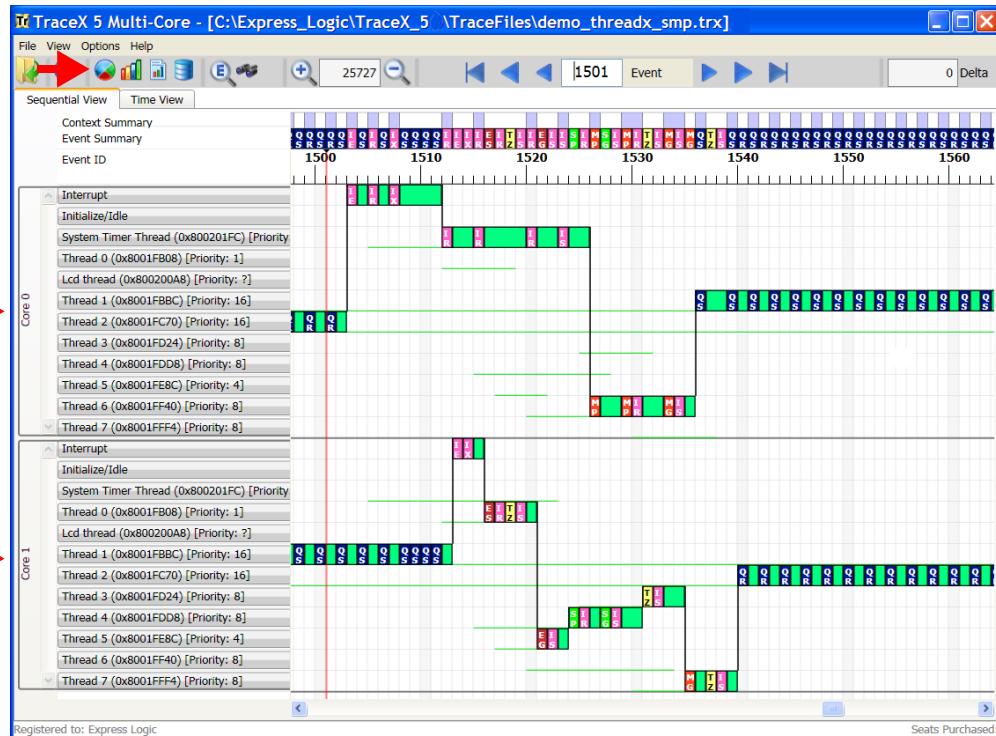
The bright red shaded area shows a *non-deterministic* priority inversion between event 34 through event 45. What makes this *non-deterministic* is that while **thread 1** holds the mutex that higher-priority **thread**

**0** is blocked on, an interrupt occurs that resumes **thread 2**, which then executes and lengthens the time the system is in priority inversion. This condition can be quite serious and difficult to identify; however, with TraceX it is easily identified.

## TraceX Multi-Core

TraceX Multi-Core is nearly identical to the standard TraceX, with two major additions. First, TraceX Multi-Core has the ability to display ThreadX SMP trace events on multiple cores. Second, TraceX Multi-Core introduces a new performance analysis capability. This new capability calculates the **CPU Utilization** of

each core. Figure 3.24 shows a trace the standard ThreadX demonstration running on a ThreadX SMP platform.



**FIGURE 3.24**

Especially take note of the core identification on the left side. Each core contains the complete list of contexts such that all activities for all cores may be viewed together. In this example, event number 1501 shows queue receive event in Core 0, the next event 1502 shows a queue send event on Core 1.

Also note the new button for generating the CPU Utilization, as pointed to in **Figure 3.24**. Selecting this button produces a report defined in greater detail in Chapter 4.

# 4

## *TraceX Performance Analysis*

This chapter describes the TraceX performance analysis tool:

- Performance Analysis 56
- Multi-Core CPU Utilization 57
- Execution Profile 57
- Popular Services 58
- Thread Stack Usage 60
- Performance Statistics 61
- FileX Statistics 63
- NetX Statistics 65
- Trace File Information 66
- Raw Trace Dump 67

# Performance Analysis

TraceX provides built-in performance analysis of trace files. Information such as the *execution profile*, *popular services*, *thread stack usage*, and various *performance statistics*, including FileX and NetX statistics, are readily available. This information is available via the **View** menu item as shown in **Figure 4.1**.

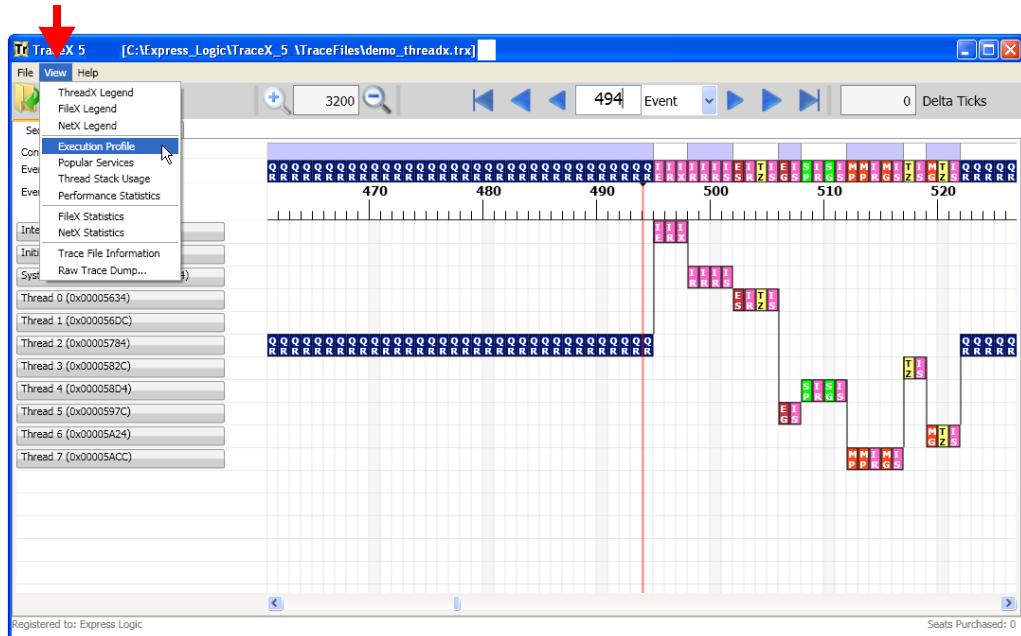
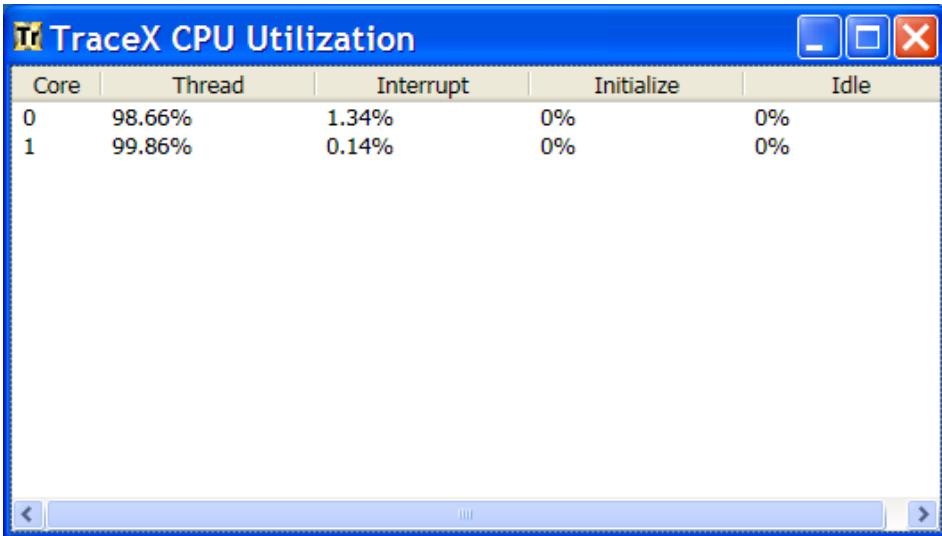


FIGURE 4.1

## Multi-Core CPU Utilization

Selecting the **CPU Utilization** button or **View -> CPU Utilization** presents the TraceX Multi-Core CPU utilization analysis for all cores in the currently loaded trace file. The CPU utilization associated with the sample ThreadX SMP demonstration running on two cores is shown in **Figure 4.2**.



Core	Thread	Interrupt	Initialize	Idle
0	98.66%	1.34%	0%	0%
1	99.86%	0.14%	0%	0%

FIGURE 4.2

## Execution Profile

Selecting the **Generate Execution Profile** button or **View -> Execution Profile** presents the TraceX execution profile for the currently loaded trace file. The

execution profile associated with the sample ThreadX demonstration trace is shown in **Figure 4.3**.

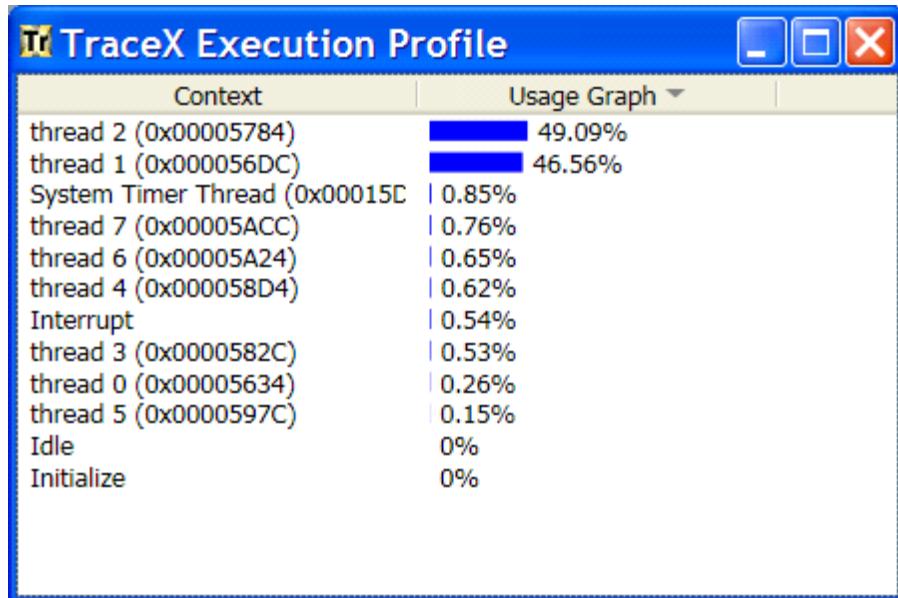


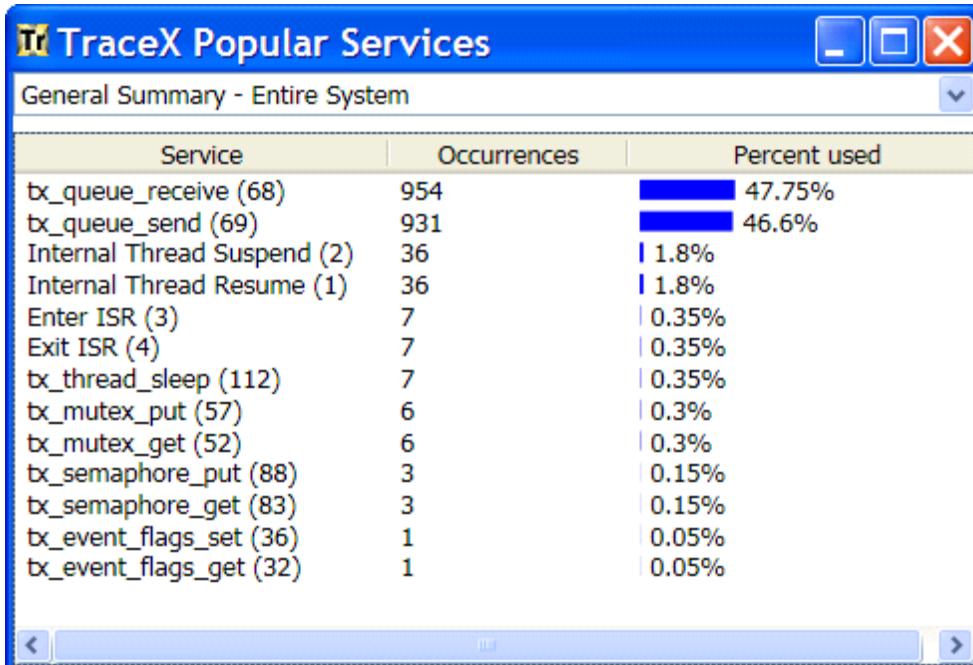
FIGURE 4.3

The example shown in **Figure 4.3** indicates that nearly 49% of the processing time is inside of **thread 2** and nearly 47% of the processing time is inside of **thread 1**. This is logical since the bulk of the trace shows these threads sending and receiving messages. The remaining execution contexts have only a small amount of execution time in this example.

## Popular Services

Selecting **View -> Popular Services** presents the popular services in the currently loaded trace file. By

default, this information is displayed for the entire system. However, the popular services for specific threads are also available. The popular services in the sample ThreadX demonstration trace are shown in **Figure 4.4**.



**FIGURE 4.4**

The example shown in **Figure 4.4** indicates that **tx\_queue\_send** and **tx\_queue\_receive** are the two most popular services in this trace. This is consistent with the behavior of the standard ThreadX demonstration from which this trace was captured.

As mentioned previously, specific threads can be selected for this analysis. **Figure 4.5** shows this analysis for **thread 3**.

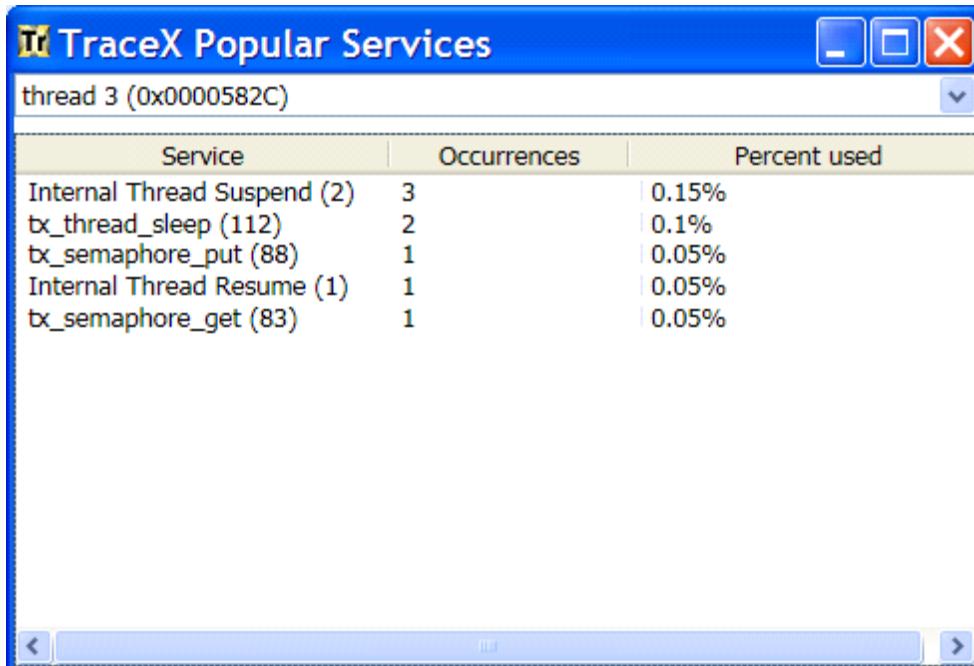
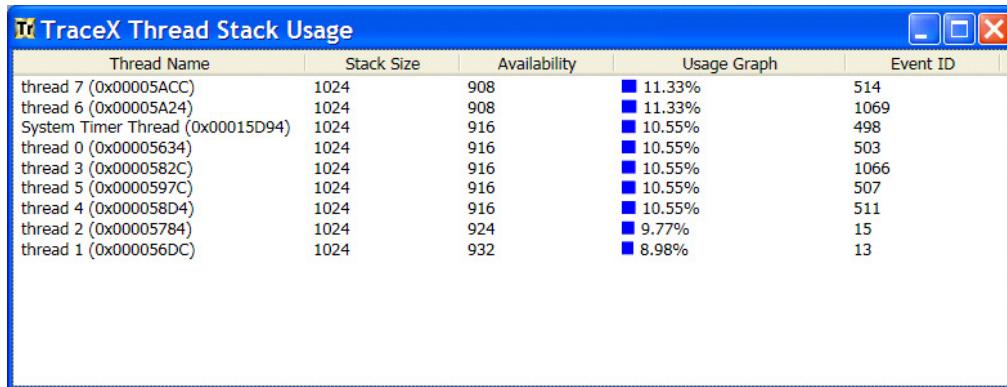


FIGURE 4.5

## Thread Stack Usage

Selecting the **Generate Thread Stack Usage** button or **View -> Thread Stack Usage** presents the stack usage for each thread in the trace file. This is accomplished by ThreadX including the current thread stack pointer in many of the trace entries in the file. A stack usage of 100% indicates the stack has overflowed and must be corrected in the

application. If there is no thread execution within this trace file, the stack usage for that thread is shown at 0%. The thread stack usage in the sample ThreadX demonstration trace is shown in **Figure 4.6**.



Thread Name	Stack Size	Availability	Usage Graph	Event ID
thread 7 (0x000005ACC)	1024	908	■ 11.33%	514
thread 6 (0x000005A24)	1024	908	■ 11.33%	1069
System Timer Thread (0x00015D94)	1024	916	■ 10.55%	498
thread 0 (0x000005634)	1024	916	■ 10.55%	503
thread 3 (0x00000582C)	1024	916	■ 10.55%	1066
thread 5 (0x00000597C)	1024	916	■ 10.55%	507
thread 4 (0x0000058D4)	1024	916	■ 10.55%	511
thread 2 (0x000005784)	1024	924	■ 9.77%	15
thread 1 (0x0000056DC)	1024	932	■ 8.98%	13

**FIGURE 4.6**

The example shown in **Figure 4.5** indicates that most threads in this trace have between 9% and 12% stack usage.

## Performance Statistics



Selecting the **Generate Performance Statistics** button or **View -> Performance Statistics** presents the performance statistics of the currently loaded trace file. By default, this information is displayed for the entire system. However, the performance statistics are also available for each specific thread.

The performance statistics of the sample ThreadX demonstration trace are shown in **Figure 4.7**.

The screenshot shows a software window titled "TraceX Performance Statistics". The main title bar has a blue background with the TraceX logo and the title. Below the title bar is a toolbar with three icons: a minus sign, a square, and an X. The main content area is a table with a light blue header. The header row contains two columns: "Statistic" and "Occurrences". The table lists various system events with their counts:

Statistic	Occurrences
Context Switches	39
Time Slices	0
Thread Preemptions	6
Thread Suspensions	36
Thread Resumptions	36
Interrupts	7
Priority Inversions	
Deterministic	0
Non-deterministic	0

**FIGURE 4.7**

The example shown in **Figure 4.8** indicates that there were 39 context switches in this trace file, as well as three thread preemptions, 36 thread suspensions, 36 thread resumptions, and seven interrupts. There were no priority inversions found in this trace file. Notice there are two categories of priority inversions, namely, *deterministic* and *non-deterministic*. Deterministic priority inversions are priority inversion in which a thread is blocked on a mutex owned by a lower priority thread. A non-deterministic priority inversion is where a different lower priority thread runs during a deterministic priority inversion. The later can cause unforeseen

timing behavior in the application and should be studied carefully.

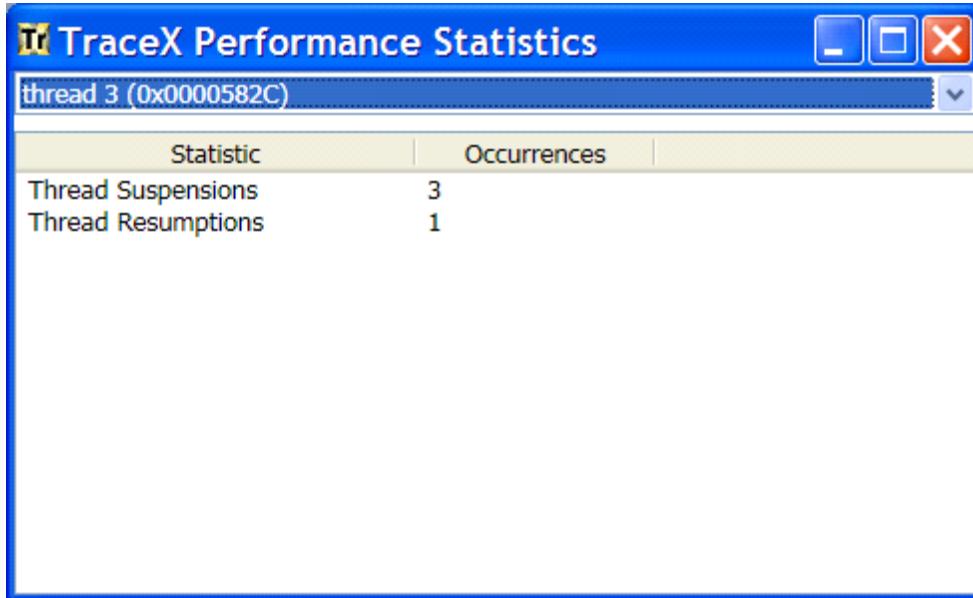


FIGURE 4.8

As mentioned previously, specific threads can be selected for this analysis. **Figure 4.8** shows this analysis specifically for **thread 3**.

## FileX Statistics

Selecting **View -> FileX Statistics** presents the FileX performance statistics of the currently loaded trace file. This information is displayed for the entire system, on all opened media objects. The

performance statistics of the sample FileX demonstration trace are shown in **Figure 4.9**.

Statistic	Occurrences
<b>Media Statistics:</b>	
Media Opens	39
MediaCloses	39
Media Aborts	0
Media Flushes	39
<b>Directory Statistics:</b>	
Directory Reads	39
Directory Writes	39
Directory Cache Misses	39
<b>File Statistics:</b>	
File Opens	39
File Closes	39
File Bytes Read	1092
File Bytes Written	1092
<b>Logical Sector Statistics</b>	
Logical Sector Reads	195
Logical Sector Writes	78
Logical Sector Cache Misses	195

**FIGURE 4.9**

The example shown in **Figure 4.9** indicates there were 39 media opens, 39 media closes, 39 media flushes, 39 directory reads, 39 directory writes, and 39 directory cache misses. There were also 39 file opens, 39 file closes, 1092 bytes read, 1092 bytes written, 195 logical sector reads, 78 logical sector writes, and 195 logical sector cache misses.

## NetX Statistics

Selecting **View -> NetX Statistics** presents the NetX performance statistics of the currently loaded trace file. This information is displayed for the entire system. The performance statistics of the sample NetX demonstration trace are shown in **Figure 4.10**.

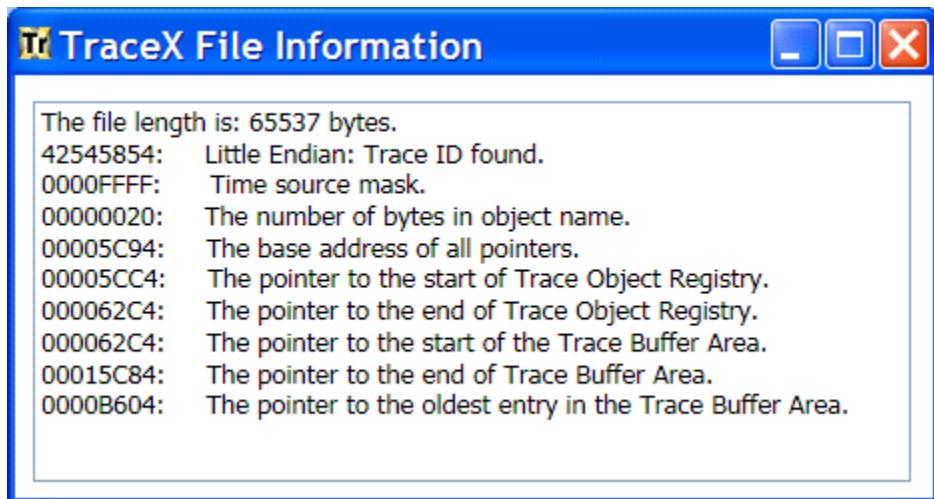
Statistic	Occurrences
<b>ARP Statistics:</b>	
ARP Requests Sent	0
ARP Responses Sent	0
ARP Requests Received	0
ARP Responses Received	0
<b>Packet Pool Statistics:</b>	
Packet Pool Allocations	141
Packet Pool Releases	139
Empty Allocation Requests	0
Packet Pool Invalid Releases	0
<b>Ping Statistics:</b>	
Pings Sent	0
Ping Responses	0
<b>IP Statistics:</b>	
IP Packets Sent	70
Total Bytes Sent	3188
IP Packets Received	69
Total Bytes Received	3156
<b>TCP Statistics:</b>	
TCP Client Connections	8
TCP Server Connections	9
TCP Packets Sent	9
TCP Bytes Sent	252
TCP Packets Received	9
TCP Bytes Received	252
<b>UDP Statistics:</b>	
UDP Packets Sent	0
UDP Bytes Sent	0
UDP Packets Received	0
UDP Bytes Received	0

FIGURE 4.10

The example shown in **Figure 4.10** indicates there were no ARP, Ping, or UDP events, but there were 70 IP packets sent, 3,188 IP bytes sent, 69 IP packets received, and 3,156 IP bytes received. There were also eight TCP client connections, nine TCP server connections, nine TCP packets sent, 252 TCP bytes sent, nine TCP packets received, and 252 TCP bytes received.

## Trace File Information

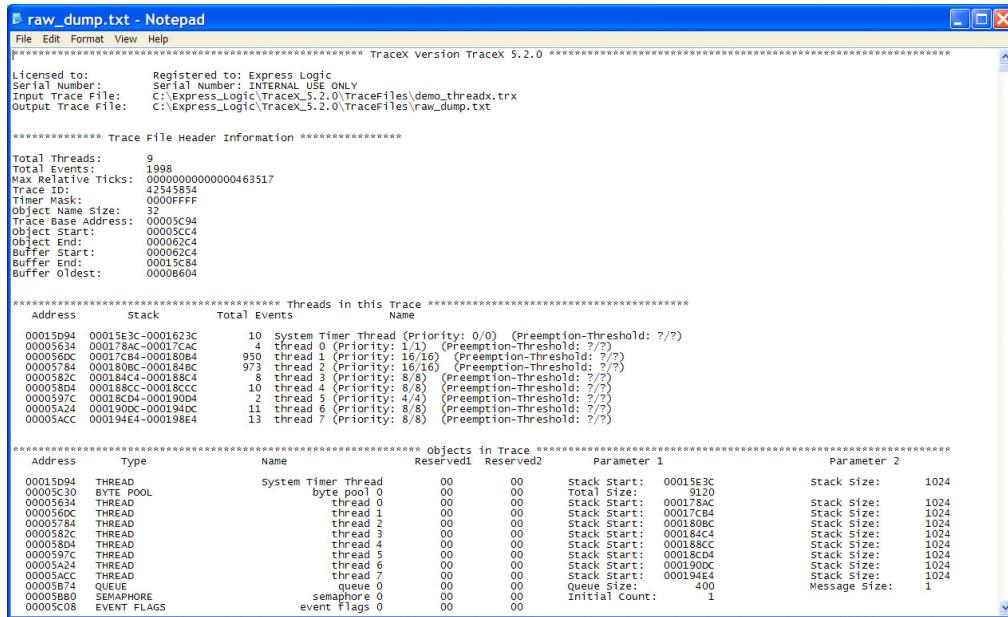
Selecting **View -> Trace File Information** presents some basic information about the opened trace file. This information includes the byte order of the file, size of the time source, maximum number of bytes for each object name, and the base address of all trace file pointers. **Figure 4.11** shows the trace file information for the standard **demo\_threadx.trx** trace file.



**FIGURE 4.11**

# Raw Trace Dump

Selecting **View -> Raw Trace Dump** presents a dialog to name the file containing the raw trace dump. After the file name and path are entered, TraceX builds the raw trace file in text format and launches **notepad.exe** to display it. **Figure 4.12** shows the raw trace file dump for the standard **demo\_threadx.trx** trace file.



The screenshot shows a Windows Notepad window titled "raw\_dump.txt - Notepad". The content of the window is a raw trace dump from TraceX Version TraceX 5.2.0. It includes sections for Trace File Header information, Threads in this Trace, and Objects in Trace.

```

raw_dump.txt - Notepad
File Edit Format View Help
*****
***** TraceX Version TraceX 5.2.0 *****
***** Trace File Header information *****
Licensed to: Registered to: Express Logic
Serial Number: INTERNAL USE ONLY
Input Trace File: C:\Express_Logic\TraceX_5.2.0\TraceFiles\demo_threadx.trx
Output Trace File: C:\Express_Logic\TraceX_5.2.0\TraceFiles\raw_dump.txt

***** Threads in this Trace *****
Address Stack Total Events Name
00015094 000153c-0001623c 10 System Timer Thread (Priority: 0/0) (Preemption-Threshold: ?/?)
00005634 000178ac-00017c4c 4 thread 0 (Priority: 1/1) (Preemption-Threshold: ?/?)
000056bc 00017c84-00018084 950 thread 1 (Priority: 16/16) (Preemption-Threshold: ?/?)
000056d0 00017ca4-000180c4 973 thread 2 (Priority: 16/16) (Preemption-Threshold: ?/?)
0000582c 000184c4-000188c4 8 thread 3 (Priority: 8/8) (Preemption-Threshold: ?/?)
00005804 000188cc-00018ccc 10 thread 4 (Priority: 8/8) (Preemption-Threshold: ?/?)
0000597c 00018cd4-000190d4 2 thread 5 (Priority: 4/4) (Preemption-Threshold: ?/?)
00005a34 000190dc-000194dc 11 thread 6 (Priority: 8/8) (Preemption-Threshold: ?/?)
00005acc 000194e4-000198e4 13 thread 7 (Priority: 8/8) (Preemption-Threshold: ?/?)

***** Objects in Trace *****
Address Type Name Reserved2 Parameter 1
00015094 THREAD System Timer Thread 00 00 Stack Start: 000153c Stack Size: 1024
00005630 BYTE_POOL byte pool 0 00 00 Total Size: 9120 Stack Size: 1024
00005634 THREAD thread 0 00 00 Stack Start: 000178ac Stack Size: 1024
000056dc THREAD thread 1 00 00 Stack Start: 00017c84 Stack Size: 1024
00005784 THREAD thread 2 00 00 Stack Start: 0001808c Stack Size: 1024
00005804 THREAD thread 3 00 00 Stack Start: 000184c4 Stack Size: 1024
0000597c THREAD thread 4 00 00 Stack Start: 000188cc Stack Size: 1024
00005a24 THREAD thread 5 00 00 Stack Start: 00018cd4 Stack Size: 1024
00005a34 THREAD thread 6 00 00 Stack Start: 000190dc Stack Size: 1024
00005874 QUEUE queue 0 00 00 Queue Size: 400 Stack Size: 1
00005bb0 SEMAPHORE semaphore 0 00 00 Initial Count: 1 Message Size: 1
00005c08 EVENT FLAGS event flags 0 00 00

```

**FIGURE 4.12**



# 5

## *Generating Trace Buffers*

This chapter contains a description about how to build a TraceX event buffer and also describes the underlying format of the buffer. This includes the following:

- ThreadX Event Trace Support 70
- Enabling Event Trace 70
- Defining Time-Stamp Constants 70
- Exporting the Trace Buffer 72
- Extended Event Trace API 73
  - Enable event tracing 74
  - Filter specified events 76
  - Unfilter specified events 80
  - Disable event tracing 84
  - Insert ISR enter event 86
  - Insert ISR exit event 88
  - Register trace buffer full application callback 90
- Insert user event 92

## ThreadX Event Trace Support

ThreadX provides built-in event trace support for all ThreadX services, thread state changes, and user-defined events. The ThreadX event-trace capability is primarily designed as a post-mortem tool to analyze the last “n” activities in the application. From this information, the developer may spot problems and/or potential targets of optimization.

TraceX graphically displays the event trace buffer built by ThreadX. The following describes how to build the buffer and describes the underlying format of the buffer.

## Enabling Event Trace

To enable event trace, define the time-stamp constants, build the ThreadX library with **TX\_ENABLE\_EVENT\_TRACE** defined, and enable tracing by calling the **tx\_trace\_enable** function.

## Defining Time-Stamp Constants

The time-stamp constants are designed to provide the developer control over the time-stamp used in the event trace entries. The two time-stamp constants and their default values are as follows:

```
#ifndef TX_TRACE_TIME_SOURCE
#define TX_TRACE_TIME_SOURCE    ++_tx_trace_simulated_time
#endif
#ifndef TX_TRACE_TIME_MASK
#define TX_TRACE_TIME_MASK      0xFFFFFFFFFUL
#endif
```

The above constants are defined in **tx\_port.h** and create a “fake” time-stamp that simply increments by one on each

event. The following is an example of an actual time-stamp definition:

```
#ifndef TX_TRACE_TIME_SOURCE
#define TX_TRACE_TIME_SOURCE * ((ULONG *) 0x13000004)
#endif
#ifndef TX_TRACE_TIME_MASK
#define TX_TRACE_TIME_MASK 0xFFFFFFFFUL
#endif
```

The above constants specify a 32-bit timer that is obtained by reading the address 0x13000004. Most application specific time-stamps should be setup in a similar fashion.

## Exporting the Trace Buffer

TraceX needs the trace buffer in a binary, Intel HEX, or Motorola S-Record file format on the host. The easiest way to accomplish this is to stop the target and instruct your debugger to dump the memory area you supplied to `tx_trace_enable` function into a file on the host.

***Warning: be careful not to stop the target within a trace gathering code itself. Doing so can cause invalid trace information. If the program is halted within ThreadX, it is best to step over any trace insert macro before dumping the trace buffer.***



Appendix D shows how to dump the trace buffer from within a variety of development tools.

## Extended Event Trace API

When ThreadX is built with **TX\_ENABLE\_EVENT\_TRACE** defined, the following new event trace APIs are available to the application:

`tx_trace_enable`  
*Enable event tracing*

`tx_trace_event_filter`  
*Filter specified event(s)*

`tx_trace_event_unfilter`  
*Unfilter specified event(s)*

`tx_trace_disable`  
*Disable event tracing*

`tx_trace_isr_enter_insert`  
*Insert ISR enter trace event*

`tx_trace_isr_exit_insert`  
*Insert ISR exit trace event*

`tx_trace_buffer_full_notify`  
*Register trace buffer full application callback*

`tx_trace_user_event_insert`  
*Insert user event*

## tx\_trace\_enable

Enable event tracing

### Prototype

```
UINT tx_trace_enable(VOID *trace_buffer_start,  
                     ULONG trace_buffer_size, ULONG registry_entries);
```

### Description

This service enables event tracing inside ThreadX. The trace buffer and the maximum number of ThreadX objects are supplied by the application.



The ThreadX library and application must be built with TX\_ENABLE\_EVENT\_TRACE defined in order to use event tracing.

### Input Parameters

<b>trace_buffer_start</b>	Pointer to the start of the user-supplied trace buffer.
<b>trace_buffer_size</b>	Total number of bytes in the memory for the trace buffer. The larger the trace buffer, the more entries it is able to store.
<b>registry_entries</b>	Number of application ThreadX objects to keep in the trace registry. The registry is used to correlate object addresses with object names. This is highly useful for GUI trace analysis tools.

### Return Values

<b>TX_SUCCESS</b>	(0x00) Successful event trace enable.
<b>TX_SIZE_ERROR</b>	(0x05) Specified trace buffer size is too small. It must be large enough for the trace header, the object registry, and at least one trace entry.

- |                                     |   |
|-------------------------------------|---|
| <b>TX_NOT_DONE</b>                  | (0x20) Event tracing was already enabled.   |
| <b>TX FEATURE NOT ENABLED(0xFF)</b> | System was not compiled with trace enabled. |

## Allowed From

Initialization and threads

## Example

```
UCHAR           my_trace_buffer[64000];  
  
/* Enable event tracing using the global "my_trace_buffer" memory and supporting  
   a maximum of 30 ThreadX objects in the registry. */  
status =  tx_trace_enable(&my_trace_buffer, 64000, 30);  
  
/* If status is TX_SUCCESS the event tracing is enabled. */
```

## See Also

`tx_trace_event_filter`, `tx_trace_event_unfilter`, `tx_trace_disable`,  
`tx_trace_isr_enter_insert`, `tx_trace_isr_exit_insert`,  
`tx_trace_buffer_full_notify`, `tx_trace_user_event_insert`

## tx\_trace\_event\_filter

Filter specified events

### Prototype

```
UINT tx_trace_event_filter(ULONG event_filter_bits);
```

### Description

This service filters the specified event(s) from being inserted into the active trace buffer. Note that by default no events are filtered after *tx\_trace\_enable* is called.



The ThreadX library and application must be built with **TX\_ENABLE\_EVENT\_TRACE** defined in order to use event tracing.

### Input Parameters

#### event\_filter\_bits

Bits that correspond to events to filter.  
Multiple events may be filtered by simply or-ing together the appropriate constants. Valid

constants for this variable are defined as follows:

TX_TRACE_ALL_EVENTS	0x000007FF
TX_TRACE_INTERNAL_EVENTS	0x00000001
TX_TRACE_BLOCK_POOL_EVENTS	0x00000002
TX_TRACE_BYTE_POOL_EVENTS	0x00000004
TX_TRACE_EVENT_FLAGS_EVENTS	0x00000008
TX_TRACE_INTERRUPT_CONTROL_EVENT	0x00000010
TX_TRACE_MUTEX_EVENTS	0x00000020
TX_TRACE_QUEUE_EVENTS	0x00000040
TX_TRACE_SEMAPHORE_EVENTS	0x00000080
TX_TRACE_THREAD_EVENTS	0x00000100
TX_TRACE_TIME_EVENTS	0x00000200
TX_TRACE_TIMER_EVENTS	0x00000400
FX_TRACE_ALL_EVENTS	0x00007800
FX_TRACE_INTERNAL_EVENTS	0x00000800
FX_TRACE_MEDIA_EVENTS	0x00001000
FX_TRACE_DIRECTORY_EVENTS	0x00002000
FX_TRACE_FILE_EVENTS	0x00004000
NX_TRACE_ALL_EVENTS	0x00FF8000
NX_TRACE_INTERNAL_EVENTS	0x00008000
NX_TRACE_ARP_EVENTS	0x00010000
NX_TRACE_ICMP_EVENTS	0x00020000
NX_TRACE_IGMP_EVENTS	0x00040000
NX_TRACE_IP_EVENTS	0x00080000
NX_TRACE_PACKET_EVENTS	0x00100000
NX_TRACE_RARP_EVENTS	0x00200000
NX_TRACE_TCP_EVENTS	0x00400000
NX_TRACE_UDP_EVENTS	0x00800000
UX_TRACE_ALL_EVENTS	0x7F000000
UX_TRACE_ERRORS	0x01000000
UX_TRACE_HOST_STACK_EVENTS	0x02000000
UX_TRACE_DEVICE_STACK_EVENTS	0x04000000
UX_TRACE_HOST_CONTROLLER_EVENTS	0x08000000
UX_TRACE_DEVICE_CONTROLLER_EVENTS	0x10000000
UX_TRACE_HOST_CLASS_EVENTS	0x20000000
UX_TRACE_DEVICE_CLASS_EVENTS	0x40000000

## Return Values

**TX\_SUCCESS**

(0x00) Successful event filter.

**TX\_FEATURE\_NOT\_ENABLED(0xFF)** System was not compiled with trace enabled.

## Allowed From

Initialization and threads

## Example

```
/* Filter queue and byte pool events from trace buffer. */  
status = tx_trace_event_filter(TX_TRACE_QUEUE_EVENTS | TX_TRACE_BYTE_POOL_EVENTS);  
  
/* If status is TX_SUCCESS all queue and byte pool events are filtered. */
```

## See Also

`tx_trace_enable`, `tx_trace_event_unfilter`, `tx_trace_disable`,  
`tx_trace_isr_enter_insert`, `tx_trace_isr_exit_insert`, `tx_trace_buffer_full_notify`,  
`tx_trace_user_event_insert`



## tx\_trace\_event\_unfilter

Unfilter specified events

### Prototype

```
UINT tx_trace_event_unfilter(ULONG event_unfilter_bits);
```

### Description

This service unfilters the specified event(s) such that they will be inserted into the active trace buffer.



The ThreadX library and application must be built with TX\_ENABLE\_EVENT\_TRACE defined in order to use event tracing.

### Input Parameters

#### event\_unfilter\_bits

Bits that correspond to events to unfilter.  
Multiple events may be unfiltered by simply or-ing together the appropriate constants.

Valid constants for this variable are defined as follows:

TX_TRACE_ALL_EVENTS	0x000007FF
TX_TRACE_INTERNAL_EVENTS	0x00000001
TX_TRACE_BLOCK_POOL_EVENTS	0x00000002
TX_TRACE_BYTE_POOL_EVENTS	0x00000004
TX_TRACE_EVENT_FLAGS_EVENTS	0x00000008
TX_TRACE_INTERRUPT_CONTROL_EVENT	0x00000010
TX_TRACE_MUTEX_EVENTS	0x00000020
TX_TRACE_QUEUE_EVENTS	0x00000040
TX_TRACE_SEMAPHORE_EVENTS	0x00000080
TX_TRACE_THREAD_EVENTS	0x00000100
TX_TRACE_TIME_EVENTS	0x00000200
TX_TRACE_TIMER_EVENTS	0x00000400
FX_TRACE_ALL_EVENTS	0x00007800
FX_TRACE_INTERNAL_EVENTS	0x00000800
FX_TRACE_MEDIA_EVENTS	0x00001000
FX_TRACE_DIRECTORY_EVENTS	0x00002000
FX_TRACE_FILE_EVENTS	0x00004000
NX_TRACE_ALL_EVENTS	0x00FF8000
NX_TRACE_INTERNAL_EVENTS	0x00008000
NX_TRACE_ARP_EVENTS	0x00010000
NX_TRACE_ICMP_EVENTS	0x00020000
NX_TRACE_IGMP_EVENTS	0x00040000
NX_TRACE_IP_EVENTS	0x00080000
NX_TRACE_PACKET_EVENTS	0x00100000
NX_TRACE_RARP_EVENTS	0x00200000
NX_TRACE_TCP_EVENTS	0x00400000
NX_TRACE_UDP_EVENTS	0x00800000
UX_TRACE_ALL_EVENTS	0x7F000000
UX_TRACE_ERRORS	0x01000000
UX_TRACE_HOST_STACK_EVENTS	0x02000000
UX_TRACE_DEVICE_STACK_EVENTS	0x04000000
UX_TRACE_HOST_CONTROLLER_EVENTS	0x08000000
UX_TRACE_DEVICE_CONTROLLER_EVENTS	0x10000000
UX_TRACE_HOST_CLASS_EVENTS	0x20000000
UX_TRACE_DEVICE_CLASS_EVENTS	0x40000000

## Return Values

**TX\_SUCCESS** (0x00) Successful event unfilter.

**TX\_FEATURE\_NOT\_ENABLED**(0xFF) System was not compiled with trace enabled.

## Allowed From

Initialization and threads

## Example

```
/* Un-filter queue and byte pool events from trace buffer. */  
status =  
    tx_trace_event_unfilter(TX_TRACE_QUEUE_EVENTS | TX_TRACE_BYTE_POOL_EVENTS);  
  
/* If status is TX_SUCCESS all queue and byte pool events are un-filtered. */
```

## See Also

`tx_trace_enable`, `tx_trace_event_filter`, `tx_trace_disable`,  
`tx_trace_isr_enter_insert`, `tx_trace_isr_exit_insert`,  
`tx_trace_buffer_full_notify`, `tx_trace_user_event_insert`



## **tx\_trace\_disable**

---

Disable event tracing

### **Prototype**

```
UINT tx_trace_disable(VOID);
```

### **Description**

This service disables event tracing inside ThreadX. This can be useful if the application wants to freeze the current event trace buffer and possibly transport it externally during run-time. Once disabled, the **tx\_trace\_enable** can be called to start tracing again.



The ThreadX library and application must be built with **TX\_ENABLE\_EVENT\_TRACE** defined in order to use event tracing.

### **Input Parameters**

None.

### **Return Values**

<b>TX_SUCCESS</b>	(0x00)	Successful event trace disable.
<b>TX_NOT_DONE</b>	(0x20)	Event tracing was not enabled.
<b>TX_FEATURE_NOT_ENABLED</b>	(0xFF)	System was not compiled with trace enabled.

### **Allowed From**

Initialization and threads

## Example

```
/* Disable event tracing. */
status = tx_trace_disable();

/* If status is TX_SUCCESS the event tracing is disabled. */
```

## See Also

`tx_trace_enable`, `tx_trace_event_filter`, `tx_trace_event_unfilter`,  
`tx_trace_isr_enter_insert`, `tx_trace_isr_exit_insert`,  
`tx_trace_buffer_full_notify`, `tx_trace_user_event_insert`

## tx\_trace\_isr\_enter\_insert

Insert ISR enter event

### Prototype

```
VOID tx_trace_isr_enter_insert(ULONG isr_id);
```

### Description

This service inserts the ISR enter event into the event trace buffer. It should be called by the application at the beginning of ISR processing. The supplied parameter should identify the specific ISR to the application.



The ThreadX library and application must be built with **TX\_ENABLE\_EVENT\_TRACE** defined in order to use event tracing.

### Input Parameters

<b>isr_id</b>	Application specific value to identify the ISR.
---------------	---

### Return Values

**None**

### Allowed From

ISRs

## Example

```
/* Insert trace event to identify the application's ISR with an
   ID of 3.  */
status = tx_trace_isr_enter_insert(3);

/* If status is TX_SUCCESS the ISR entry event was inserted.  */
```

## See Also

`tx_trace_enable`, `tx_trace_event_filter`, `tx_trace_event_unfilter`,  
`tx_trace_disable`, `tx_trace_isr_exit_insert`, `tx_trace_buffer_full_notify`,  
`tx_trace_user_event_insert`

## tx\_trace\_isr\_exit\_insert

Insert ISR exit event

### Prototype

```
VOID tx_trace_isr_exit_insert(ULONG isr_id);
```

### Description

This service inserts the ISR entry event into the event trace buffer. It should be called by the application at the beginning of ISR processing. The supplied parameter should identify the ISR to the application.



The ThreadX library and application must be built with **TX\_ENABLE\_EVENT\_TRACE** defined in order to use event tracing.

### Input Parameters

<b>isr_id</b>	Application specific value to identify the ISR.
---------------	---

### Return Values

None

### Allowed From

ISRs

## Example

```
/* Insert trace event to identify the application's ISR with an
   ID of 3.  */
status = tx_trace_isr_exit_insert(3);

/* If status is TX_SUCCESS the ISR exit event was inserted.  */
```

## See Also

`tx_trace_enable`, `tx_trace_event_filter`, `tx_trace_event_unfilter`,  
`tx_trace_disable`, `tx_trace_isr_enter_insert`, `tx_trace_buffer_full_notify`,  
`tx_trace_user_event_insert`

## tx\_trace\_buffer\_full\_notify

Register trace buffer full application callback

### Prototype

```
VOID tx_trace_buffer_full_notify(VOID (*full_buffer_callback)(VOID *));
```

### Description

This service registers an application callback function that is called by ThreadX when the trace buffer becomes full. The application can then choose to disable tracing and/or possibly setup a new trace buffer.



The ThreadX library and application must be built with **TX\_ENABLE\_EVENT\_TRACE** defined in order to use event tracing.

### Input Parameters

**full\_buffer\_callback** Application function to call when the trace buffer is full. A value of NULL disables the notification callback.

### Return Values

None

### Allowed From

ISRs

## Example

```
_trace_is_full(void *trace_buffer_start)
{
    /* Application specific processing goes here! */

    /* Register the "my_trace_is_full" function to be called whenever the
     trace buffer fills. */
    status = tx_trace_buffer_full_notify(my_trace_is_full);

    /* If status is TX_SUCCESS the "my_trace_is_full" function is registered. */
}
```

## See Also

`tx_trace_enable`, `tx_trace_event_filter`, `tx_trace_event_unfilter`,  
`tx_trace_disable`, `tx_trace_isr_enter_insert`, `tx_trace_isr_exit_insert`,  
`tx_trace_user_event_insert`

## tx\_trace\_user\_event\_insert

Insert user event

### Prototype

```
UINT tx_trace_user_event_insert(ULONG event_id,  
                               ULONG info_field_1, ULONG info_field_2,  
                               ULONG info_field_3, ULONG info_field_4);
```

### Description

This service inserts the user event into the trace buffer. User event IDs must be greater than the constant **TX\_TRACE\_USER\_EVENT\_START**, which is defined to be 4096. The maximum user event is defined by the constant **TX\_TRACE\_USER\_EVENT\_END**, which is defined to be 65535. All events within this range are available to the application. The information fields are application specific.



The ThreadX library and application must be built with **TX\_ENABLE\_EVENT\_TRACE** defined in order to use event tracing.

### Input Parameters

<b>event_id</b>	Application-specific event identification and must start be greater than <b>TX_TRACE_USER_EVENT_START</b> and less than or equal to <b>TX_TRACE_USER_EVENT_END</b> .
<b>info_field_1</b>	Application-specific information field.
<b>info_field_2</b>	Application-specific information field.
<b>info_field_3</b>	Application-specific information field.
<b>info_field_4</b>	Application-specific information field.

### Return Values

<b>TX_SUCCESS</b>	(0x00)	Successful user event insert.
<b>TX_NOT_DONE</b>	(0x20)	Event tracing is not enabled.
<b>TX_FEATURE_NOT_ENABLED</b>	(0xFF)	The system was not compiled with trace enabled.

## Allowed From

Initialization and threads

## Example

```
/* Insert user event 3000, with info fields of 1, 2, 3, 4.  */
status = tx_trace_user_event_insert(3000, 1, 2, 3, 4);

/* If status is TX_SUCCESS the user event was inserted.  */
```

## See Also

`tx_trace_enable`, `tx_trace_event_filter`, `tx_trace_event_unfilter`,  
`tx_trace_disable`, `tx_trace_isr_enter_insert`, `tx_trace_isr_exit_insert`,  
`tx_trace_buffer_full_notify`



# 6

## ***ThreadX Trace Events***

This chapter describes the ThreadX events.

- List of Events and Icons 96
- Event Descriptions 100

## List of Events and Icons

The following is a list of ThreadX events displayed by TraceX:

Icon	Meaning
	Internal thread resume
	Internal thread suspend
	Interrupt Service Routine (ISR) Enter
	Interrupt Service Routine (ISR) Exit
	Internal time-slice
	Running
	User-Defined Event (See Chapter 4)
	Block pool allocate ( <i>tx_block_allocate</i> )
	Block pool create ( <i>tx_block_pool_create</i> )
	Block pool delete ( <i>tx_block_pool_delete</i> )
	Block pool information get ( <i>tx_block_pool_info_get</i> )
	Block pool performance information get ( <i>tx_block_pool_performance_info_get</i> )
	Block pool system performance information get ( <i>tx_block_pool_performance_system_info_get</i> )
	Block pool prioritize ( <i>tx_block_pool_prioritize</i> )
	Block release to pool ( <i>tx_block_release</i> )
	Byte pool allocate memory ( <i>tx_byte_allocate</i> )
	Byte pool create ( <i>tx_byte_pool_create</i> )
	Byte pool delete ( <i>tx_byte_pool_delete</i> )
	Byte pool information get ( <i>tx_byte_pool_info_get</i> )

<b>P I</b>	<b>Byte pool performance information get</b> <i>(tx_byte_pool_performance_info_get)</i>
<b>P S</b>	<b>Byte pool system performance information get</b> <i>(tx_byte_pool_performance_system_info_get)</i>
<b>P P</b>	<b>Byte pool prioritize</b> ( <i>tx_byte_pool_prioritize</i> )
<b>B R</b>	<b>Byte memory release to pool</b> ( <i>tx_byte_release</i> )
<b>E C</b>	<b>Event flags create</b> ( <i>tx_event_flags_create</i> )
<b>E D</b>	<b>Event flags delete</b> ( <i>tx_event_flags_delete</i> )
<b>E G</b>	<b>Event flags get</b> ( <i>tx_event_flags_get</i> )
<b>I G</b>	<b>Event flags information get</b> ( <i>tx_event_flags_info_get</i> )
<b>P I</b>	<b>Event flags performance information get</b> <i>(tx_event_flags_performance_info_get)</i>
<b>P S</b>	<b>Event flags system performance information get</b> <i>(tx_event_flags_performance_system_info_get)</i>
<b>E S</b>	<b>Event flags set</b> ( <i>tx_event_flags_set</i> )
<b>E N</b>	<b>Event flags set notify</b> ( <i>tx_event_flags_set_notify</i> )
<b>I C</b>	<b>Interrupt enable/disable</b> ( <i>tx_interrupt_control</i> )
<b>M C</b>	<b>Mutex create</b> ( <i>tx_mutex_create</i> )
<b>M D</b>	<b>Mutex delete</b> ( <i>tx_mutex_delete</i> )
<b>M G</b>	<b>Mutex get</b> ( <i>tx_mutex_get</i> )
<b>I G</b>	<b>Mutex information get</b> ( <i>tx_mutex_info_get</i> )
<b>P I</b>	<b>Mutex performance information get</b> <i>(tx_mutex_performance_info_get)</i>
<b>P S</b>	<b>Mutex system performance information get</b> <i>(tx_mutex_performance_system_info_get)</i>
<b>X P</b>	<b>Mutex prioritize</b> ( <i>tx_mutex_prioritize</i> )

<b>M</b>	<b>Mutex put (tx_mutex_put)</b>
<b>Q</b>	<b>Queue create (tx_queue_create)</b>
<b>Q</b>	<b>Queue delete (tx_queue_delete)</b>
<b>Q</b>	<b>Queue flush (tx_queue_flush)</b>
<b>F</b>	<b>Queue front send (tx_queue_front_send)</b>
<b>I</b>	<b>Queue information get (tx_queue_info_get)</b>
<b>P</b>	<b>Queue performance information get (tx_queue_performance_info_get)</b>
<b>P</b>	<b>Queue system performance information get (tx_queue_performance_system_info_get)</b>
<b>Q</b>	<b>Queue prioritize (tx_queue_prioritize)</b>
<b>Q</b>	<b>Queue receive message (tx_queue_receive)</b>
<b>Q</b>	<b>Queue send message (tx_queue_send)</b>
<b>S</b>	<b>Queue send notify (tx_queue_send_notify)</b>
<b>C</b>	<b>Semaphore ceiling put (tx_semaphore_ceiling_put)</b>
<b>S</b>	<b>Semaphore create (tx_semaphore_create)</b>
<b>S</b>	<b>Semaphore delete (tx_semaphore_delete)</b>
<b>S</b>	<b>Semaphore get (tx_semaphore_get)</b>
<b>I</b>	<b>Semaphore information get (tx_semaphore_info_get)</b>
<b>P</b>	<b>Semaphore performance information get (tx_semaphore_performance_info_get)</b>
<b>P</b>	<b>Semaphore system performance information get (tx_semaphore_performance_system_info_get)</b>
<b>S</b>	<b>Semaphore prioritize (tx_semaphore_prioritize)</b>
<b>S</b>	<b>Semaphore put (tx_semaphore_put)</b>

S	N	Semaphore put notify ( <i>tx_semaphore_put_notify</i> )
T	C	Thread create ( <i>tx_thread_create</i> )
T	D	Thread delete ( <i>tx_thread_delete</i> )
T	E	Thread exit/entry notify ( <i>tx_thread_entry_exit_notify</i> )
T	I	Thread identify ( <i>tx_thread_identify</i> )
I	G	Thread information get ( <i>tx_thread_info_get</i> )
P	I	Thread performance information get ( <i>tx_thread_performance_info_get</i> )
P	S	Thread performance system information get ( <i>tx_thread_performance_system_info_get</i> )
C	T	Thread preemption change ( <i>tx_thread_preemption_change</i> )
C	P	Thread priority change ( <i>tx_thread_priority_change</i> )
R	Q	Thread relinquish ( <i>tx_thread_relinquish</i> )
R	S	Thread reset ( <i>tx_thread_reset</i> )
T	R	Thread resume ( <i>tx_thread_resume</i> )
T	Z	Thread Sleep ( <i>tx_thread_sleep</i> )
S	E	Thread stack error notify ( <i>tx_thread_stack_error_notify</i> )
T	S	Thread suspend ( <i>tx_thread_suspend</i> )
T	T	Thread terminate ( <i>tx_thread_terminate</i> )
C	S	Thread time-slice change ( <i>tx_thread_time_slice_change</i> )
W	A	Thread wait abort ( <i>tx_thread_wait_abort</i> )
T	G	Time get ( <i>tx_time_get</i> )
T	S	Time set ( <i>tx_time_set</i> )

<b>T A</b>	Timer activate ( <i>tx_timer_activate</i> )
<b>T C</b>	Timer change ( <i>tx_timer_change</i> )
<b>C R</b>	Timer create ( <i>tx_timer_create</i> )
<b>T D</b>	Timer deactivate ( <i>tx_timer_deactivate</i> )
<b>D E</b>	Timer delete ( <i>tx_timer_delete</i> )
<b>I G</b>	Timer information get ( <i>tx_timer_info_get</i> )
<b>P I</b>	Timer performance information get ( <i>tx_timer_performance_info_get</i> )
<b>P S</b>	Timer performance system information get ( <i>tx_timer_performance_system_info_get</i> )

## Event Descriptions

The following describes each individual event.

<p><b>Internal thread resume</b></p> <p>Icon </p> <p><b>Description</b> This event represents the internal processing in ThreadX that resumes a thread for execution. If the specified thread is the highest priority and preemption-threshold does not block its execution, the system will start executing this newly ready thread.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the thread being resumed.</li> <li>Info Field 2: Previous state of the thread being resumed, as follows:</li> </ul> <table border="0"> <tr><td>TX_READY</td><td>0</td></tr> <tr><td>TX_COMPLETED</td><td>1</td></tr> <tr><td>TX_TERMINATED</td><td>2</td></tr> <tr><td>TX_SUSPENDED</td><td>3</td></tr> <tr><td>TX_SLEEP</td><td>4</td></tr> <tr><td>TX_QUEUE_SUSP</td><td>5</td></tr> <tr><td>TX_SEMAPHORE_SUSP</td><td>6</td></tr> <tr><td>TX_EVENT_FLAG</td><td>7</td></tr> <tr><td>TX_BLOCK_MEMORY</td><td>8</td></tr> <tr><td>TX_BYTEm_MEMORY</td><td>9</td></tr> <tr><td>TX_TCP_IP</td><td>12</td></tr> <tr><td>TX_MUTEX_SUSP</td><td>13</td></tr> </table> <ul style="list-style-type: none"> <li>Info Field 3: Stack pointer value during the call.</li> <li>Info Field 4: Pointer to next highest priority thread to execute.</li> </ul>	TX_READY	0	TX_COMPLETED	1	TX_TERMINATED	2	TX_SUSPENDED	3	TX_SLEEP	4	TX_QUEUE_SUSP	5	TX_SEMAPHORE_SUSP	6	TX_EVENT_FLAG	7	TX_BLOCK_MEMORY	8	TX_BYTEm_MEMORY	9	TX_TCP_IP	12	TX_MUTEX_SUSP	13	<p><b>Internal thread suspend</b></p> <p>Icon </p> <p><b>Description</b> This event represents the internal processing in ThreadX that suspends a thread's execution. The next highest priority thread ready for execution is placed in the fourth information field. If this value is NULL, there is no other thread ready for execution and the system is idle.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the thread being suspended.</li> <li>Info Field 2: New state of the thread being suspended, as follows:</li> </ul> <table border="0"> <tr><td>TX_COMPLETED</td><td>1</td></tr> <tr><td>TX_TERMINATED</td><td>2</td></tr> <tr><td>TX_SUSPENDED</td><td>3</td></tr> <tr><td>TX_SLEEP</td><td>4</td></tr> <tr><td>TX_QUEUE_SUSP</td><td>5</td></tr> <tr><td>TX_SEMAPHORE_SUSP</td><td>6</td></tr> <tr><td>TX_EVENT_FLAG</td><td>7</td></tr> <tr><td>TX_BLOCK_MEMORY</td><td>8</td></tr> <tr><td>TX_BYTEm_MEMORY</td><td>9</td></tr> <tr><td>TX_TCP_IP</td><td>12</td></tr> <tr><td>TX_MUTEX_SUSP</td><td>13</td></tr> </table> <ul style="list-style-type: none"> <li>Info Field 3: Stack pointer value during the call.</li> <li>Info Field 4: Pointer to next highest priority thread to execute. If NULL, the system is idle.</li> </ul>	TX_COMPLETED	1	TX_TERMINATED	2	TX_SUSPENDED	3	TX_SLEEP	4	TX_QUEUE_SUSP	5	TX_SEMAPHORE_SUSP	6	TX_EVENT_FLAG	7	TX_BLOCK_MEMORY	8	TX_BYTEm_MEMORY	9	TX_TCP_IP	12	TX_MUTEX_SUSP	13
TX_READY	0																																														
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TX_MUTEX_SUSP	13																																														
<p><b>Interrupt Service Routine (ISR) enter</b></p> <p>Icon </p> <p><b>Description</b> This event represents entering an Interrupt Service Routine (ISR) in the application. The interrupt service routine execution continues until the ISR exit event takes place.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Stack pointer value during the call.</li> <li>Info Field 2: Application-defined ISR number (optional).</li> <li>Info Field 3: Nested interrupt count.</li> <li>Info Field 4: Internal preemption disable flag.</li> </ul>	<p><b>Interrupt Service Routine (ISR) exit</b></p> <p>Icon </p> <p><b>Description</b> This event represents exiting an Interrupt Service Routine (ISR) in the application.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Stack pointer value during the call.</li> <li>Info Field 2: Application-defined ISR number (optional).</li> <li>Info Field 3: Nested interrupt count.</li> <li>Info Field 4: Internal preemption disable flag.</li> </ul>																																														

<p><b>Internal time-slice</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents the internal processing in ThreadX that performs the time-slice operation. The next thread of the same priority is placed in the first information field. If this value is the same as the current thread, no time-slice was performed.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the next thread to execute. Info Field 2: Nested interrupt count. Info Field 3: Internal preemption disable flag. Info Field 4: Stack pointer value during the call.</p>	<p><b>Running</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents running within a thread context or idle system. It is used to illustrate subsequent changes in context as a result of an interrupt.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Block Allocate</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents allocating a memory block via tx_block_allocate. If successful, the address of the block allocated is returned in the second information field.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the corresponding block pool. Info Field 2: Pointer to the memory block returned (if successful). Info Field 3: The wait option supplied to the tx_block_allocate call. Info Field 4: Remaining available blocks in the pool after this allocation.</p>	<p><b>Block Pool Create</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents creating a memory block pool via tx_block_pool_create.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the corresponding block pool control block. Info Field 2: Pointer to the starting memory area of the pool. Info Field 3: The number of blocks in the pool. Info Field 4: The size of each block in the pool in bytes.</p>
<p><b>Block Pool Delete</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents deleting a memory block pool via tx_block_pool_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the block pool control block. Info Field 2: Stack pointer value during the call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Block Pool Information Get</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting information about a memory block pool via tx_block_pool_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the block pool control block. Info Field 2: Stack pointer value during the call. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Block Pool Performance Information Get</b></p> <p>tx_block_pool_performance_info_get</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents getting performance information about a memory block pool via tx_block_pool_performance_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the block pool control block. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Block Pool Performance System Information Get</b></p> <p>tx_block_pool_performance_system_info_get</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents getting performance information about all memory block pools via tx_block_pool_performance_system_info_get.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Block Pool Prioritize</b></p> <p>tx_block_pool_prioritize</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents placing the highest-priority suspended thread at the front of the block pool suspension list. If this is done prior to calling tx_block_release, the highest priority suspended thread will receive the released block.</p> <p><b>Information Fields</b> Info Field 1: Memory block pool pointer. Info Field 2: Number of threads suspended on this block pool. Info Field 3: Stack pointer at the time of the call. Info Field 4: Not used.</p>	<p><b>Block Release</b></p> <p>tx_block_release</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents releasing a previously allocated block back to the block pool.</p> <p><b>Information Fields</b> Info Field 1: Memory block pool pointer. Info Field 2: Pointer to block to release. Info Field 3: Number of threads suspended on this block pool. Info Field 4: Stack pointer at the time of the call.</p>
<p><b>Byte Allocate</b></p> <p>tx_byte_allocate</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents allocating memory via tx_byte_allocate. If successful, the address of the memory allocated is returned in the second information field.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the corresponding byte pool. Info Field 2: Pointer to the memory returned (if successful). Info Field 3: Number of bytes requested. Info Field 4: The wait option supplied to the tx_byte_allocate call.</p>	<p><b>Byte Pool Create</b></p> <p>tx_byte_pool_create</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents creating a byte pool via tx_byte_pool_create.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the corresponding byte pool. Info Field 2: Pointer to the start of the memory area. Info Field 3: Number of bytes in the byte pool. Info Field 4: The stack pointer at the time of the call.</p>

<p><b>Byte Pool Delete</b></p> <p style="text-align: right;">tx_byte_pool_delete</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents deleting a byte pool via tx_byte_pool_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the corresponding byte pool. Info Field 2: The stack pointer at the time of the call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Byte Pool Information Get</b></p> <p style="text-align: right;">tx_byte_pool_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting byte pool information via tx_byte_pool_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the corresponding byte pool. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Byte Pool Performance Info Get</b></p> <p style="text-align: right;">tx_byte_pool_performance_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting byte pool performance information via tx_byte_pool_performance_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the corresponding byte pool. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Byte Pool Performance System Info Get</b></p> <p style="text-align: right;">tx_byte_pool_performance_system_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting byte pool performance system information via tx_byte_pool_performance_system_info_get.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Byte Pool Prioritize</b></p> <p style="text-align: right;">tx_byte_pool_prioritize</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents prioritizing the byte pool's suspension list via tx_byte_pool_prioritize.</p> <p><b>Information Fields</b> Info Field 1: Pointer to corresponding byte pool. Info Field 2: Number of threads currently suspended on byte pool. Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>	<p><b>Byte Release</b></p> <p style="text-align: right;">tx_byte_release</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents releasing a block of memory allocated from a byte pool via tx_byte_release.</p> <p><b>Information Fields</b> Info Field 1: Pointer to corresponding byte pool. Info Field 2: Pointer to previously allocated byte pool memory. Info Field 3: Number of threads suspended on this byte pool. Info Field 4: Number of available bytes of memory.</p>

<p><b>Event Flags Create</b></p> <p>tx_event_flags_create</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents creating a new event flags group via tx_event_flags_create.</p> <p><b>Information Fields</b> Info Field 1: Pointer to event flags group control block. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Event Flags Delete</b></p> <p>tx_event_flags_delete</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents deleting an event flags group via tx_event_flags_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to event flags group. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Event Flags Get</b></p> <p>tx_event_flags_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents retrieving event flags from an existing event flags group via tx_event_flags_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to event flags group. Info Field 2: Event flags requested. Info Field 3: Event flags currently set in the group. Info Field 4: Option requested on the event flags get.</p>	<p><b>Event Flags Information Get</b></p> <p>tx_event_flags_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents retrieving information regarding an existing event flags group via tx_event_flags_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to event flags group. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Event Flags Performance Information Get</b></p> <p>tx_event_flags_performance_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents retrieving performance information regarding an existing event flags group via tx_event_flags_performance_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to event flags group. Info Field 2: Not used Info Field 3: Not used Info Field 4: Not Used</p>	<p><b>Event Flags Performance System Info Get</b></p> <p>tx_event_flags_performance_system_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents retrieving performance information regarding an existing event flags group via tx_event_flags_performance_info_get.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Event Flags Set</b></p> <p style="text-align: right;">tx_event_flags_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents setting (or clearing) event flags in an existing event flags group via tx_event_flags_set.</p> <p><b>Information Fields</b> Info Field 1: Pointer to event flags group. Info Field 2: Event flags to set (or clear). Info Field 3: AND or OR event flag option. Info Field 4: Number of threads suspended on event flag group.</p>	<p><b>Event Flags Set Notify</b></p> <p style="text-align: right;">tx_event_flags_set_notify</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents registering a notification callback for any event flag set operation on an existing event flags group via tx_event_flags_set_notify.</p> <p><b>Information Fields</b> Info Field 1: Pointer to event flags group. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Interrupt Control</b></p> <p style="text-align: right;">tx_interrupt_control</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents changing the interrupt lockout posture of the processor via tx_interrupt_control.</p> <p><b>Information Fields</b> Info Field 1: New interrupt posture. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Mutex Create</b></p> <p style="text-align: right;">tx_mutex_create</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents creating a mutex via tx_mutex_create.</p> <p><b>Information Fields</b> Info Field 1: Pointer to mutex control block. Info Field 2: Priority inheritance option (TX_INHERIT or TX_NO_INHERIT). Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>
<p><b>Mutex Delete</b></p> <p style="text-align: right;">tx_mutex_delete</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents deleting a mutex via tx_mutex_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to mutex. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Mutex Get</b></p> <p style="text-align: right;">tx_mutex_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents obtaining a mutex via tx_mutex_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to mutex. Info Field 2: The wait option supplied to the tx_mutex_get call. Info Field 3: Pointer to thread that owns the mutex (NULL implies the mutex is not owned). Info Field 4: Number of times the owning thread has called tx_mutex_get.</p>

<p><b>Mutex Information Get</b></p> <p> tx_mutex_info_get</p> <p><b>Description</b> This event represents retrieving mutex information via tx_mutex_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to mutex. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Mutex Performance Information Get</b></p> <p> tx_mutex_performance_info_get</p> <p><b>Description</b> This event represents retrieving mutex performance information via tx_mutex_performance_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to mutex. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Mutex Performance System Info Get</b></p> <p> tx_mutex_performance_system_info_get</p> <p><b>Description</b> This event represents retrieving mutex system performance information via tx_mutex_performance_system_info_get.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Mutex Prioritize</b></p> <p> tx_mutex_prioritize</p> <p><b>Description</b> This event represents prioritizing the mutex's suspension list via tx_mutex_prioritize.</p> <p><b>Information Fields</b> Info Field 1: Pointer to corresponding mutex. Info Field 2: Number of threads currently suspended on the mutex. Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>
<p><b>Mutex Put</b></p> <p> tx_mutex_put</p> <p><b>Description</b> This event represents releasing a previously owned mutex via tx_mutex_put.</p> <p><b>Information Fields</b> Info Field 1: Pointer to corresponding mutex. Info Field 2: Pointer of thread owning the mutex. Info Field 3: Number of outstanding mutex get requests. Info Field 4: Stack pointer at time of call.</p>	<p><b>Queue Create</b></p> <p> tx_queue_create</p> <p><b>Description</b> This event represents creating a message queue via tx_queue_create.</p> <p><b>Information Fields</b> Info Field 1: Pointer to queue control block. Info Field 2: Size of message – in terms of 32-bit words. Info Field 3: Pointer to start of queue memory area. Info Field 4: Number of bytes in the queue memory area.</p>

<p><b>Queue Delete</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents deleting a queue via tx_queue_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to queue. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Queue Flush</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents flushing (clearing all queue contents) of a queue via tx_queue_flush.</p> <p><b>Information Fields</b> Info Field 1: Pointer to queue. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Queue Front Send</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents sending a message to the front of a queue via tx_queue_front_send.</p> <p><b>Information Fields</b> Info Field 1: Pointer to queue. Info Field 2: Pointer to start of message. Info Field 3: Wait option supplied to the tx_queue_front_send call. Info Field 4: Number of messages already enqueued.</p>	<p><b>Queue Information Get</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting information about a queue via tx_queue_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to queue. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Queue Performance Info Get</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting performance information about a queue via tx_queue_performance_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to queue. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Queue Performance System Info Get</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting system performance information about all the queues in the system.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Queue Prioritize</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents prioritizing the queue's suspension list via tx_queue_prioritize.</p> <p><b>Information Fields</b> Info Field 1: Pointer to corresponding queue. Info Field 2: Number of threads currently suspended on the queue. Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>	<p><b>Queue Receive</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents receiving a message from a queue via tx_queue_receive.</p> <p><b>Information Fields</b> Info Field 1: Pointer to queue. Info Field 2: Pointer to destination for message. Info Field 3: Wait option supplied to the call. Info Field 4: Number of messages currently queued.</p>
<p><b>Queue Send</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents sending a message to a queue via tx_queue_send.</p> <p><b>Information Fields</b> Info Field 1: Pointer to queue. Info Field 2: Pointer to message. Info Field 3: Wait option supplied to the call. Info Field 4: Number of messages currently queued.</p>	<p><b>Queue Send Notify</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents registering a callback via tx_queue_send_notify which is called whenever a message is sent to a queue.</p> <p><b>Information Fields</b> Info Field 1: Pointer to queue. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Semaphore Ceiling Put</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents putting to a semaphore via tx_semaphore_ceiling_put. This differs from tx_semaphore_put in that the maximum value of the semaphore is examined such that the put operation is not allowed to exceed the maximum value or ceiling.</p> <p><b>Information Fields</b> Info Field 1: Pointer to semaphore. Info Field 2: Current semaphore count. Info Field 3: Number of threads suspended on the semaphore. Info Field 4: Ceiling limit supplied to the call.</p>	<p><b>Semaphore Create</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents creating a semaphore via tx_semaphore_create.</p> <p><b>Information Fields</b> Info Field 1: Pointer to semaphore control block. Info Field 2: Initial semaphore count. Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>

<p><b>Semaphore Delete</b></p> <p>tx_semaphore_delete</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents deleting a semaphore via tx_semaphore_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to semaphore. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Semaphore Get</b></p> <p>tx_semaphore_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents obtaining a semaphore via tx_semaphore_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to semaphore. Info Field 2: Wait option supplied to the call. Info Field 3: Current semaphore count. Info Field 4: Stack pointer at time of call.</p>
<p><b>Semaphore Information Get</b></p> <p>tx_semaphore_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents obtaining information about a semaphore via tx_semaphore_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to semaphore. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Semaphore Performance Info Get</b></p> <p>tx_semaphore_performance_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents obtaining performance information about a semaphore via tx_semaphore_performance_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to semaphore. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Semaphore Performance System Info</b></p> <p>tx_semaphore_performance_system_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents obtaining performance information about all semaphores in the system via tx_semaphore_performance_system_info_get.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Semaphore Prioritize</b></p> <p>tx_semaphore_prioritize</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents prioritizing the semaphore's suspension list via tx_semaphore_prioritize.</p> <p><b>Information Fields</b> Info Field 1: Pointer to corresponding semaphore. Info Field 2: Number of threads currently suspended on the semaphore. Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>

<p><b>Semaphore Put</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents releasing a semaphore instance via tx_semaphore_put.</p> <p><b>Information Fields</b> Info Field 1: Pointer to corresponding semaphore. Info Field 2: Current semaphore count. Info Field 3: Number of threads suspended on the semaphore. Info Field 4: Stack pointer at time of call.</p>	<p><b>Semaphore Put Notify</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents registering a callback via tx_semaphore_put_notify that is called whenever a semaphore instance is put.</p> <p><b>Information Fields</b> Info Field 1: Pointer to semaphore. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Thread Create</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents creating a thread via tx_thread_create.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread control block. Info Field 2: Priority of thread. Info Field 3: Stack pointer for thread. Info Field 4: Size of stack in bytes.</p>	<p><b>Thread Delete</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents deleting a thread via tx_thread_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Thread Entry/Exit Notify</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents registering a callback via tx_thread_entry_exit_notify that is called whenever a thread is entered or exits.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread. Info Field 2: Thread state at time of the registration. Info Field 3: Pointer to stack at time of call. Info Field 4: Not used.</p>	<p><b>Thread Identify</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting the current thread pointer via tx_thread_identify.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Thread Information Get</b></p> <p>tx_thread_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting information about the specified thread via tx_thread_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread. Info Field 2: State of thread at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Thread Performance Information Get</b></p> <p>tx_thread_performance_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting performance information about the specified thread via tx_thread_performance_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread. Info Field 2: State of thread at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Thread Performance System Info Get</b></p> <p>tx_thread_performance_system_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting performance information about all threads via tx_thread_performance_system_info_get.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Thread Preemption Change</b></p> <p>tx_thread_preemption_change</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents changing a thread's preemption-threshold via tx_thread_preemption_change.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread. Info Field 2: New preemption-threshold. Info Field 3: Previous preemption-threshold. Info Field 4: Thread's state at time of call.</p>
<p><b>Thread Priority Change</b></p> <p>tx_thread_priority_change</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents changing a thread's priority via tx_thread_priority_change.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread. Info Field 2: New priority. Info Field 3: Previous priority. Info Field 4: Thread's state at time of call.</p>	<p><b>Thread Relinquish</b></p> <p>tx_thread_relinquish</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents relinquishing the processor from a thread via tx_thread_relinquish.</p> <p><b>Information Fields</b> Info Field 1: Stack pointer at time of call. Info Field 2: Pointer to the next thread to execute. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Thread Reset</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents resetting a completed or terminated thread via tx_thread_reset.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread. Info Field 2: Thread's state at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Thread Resume</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents resuming a suspended thread via tx_thread_resume.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread. Info Field 2: Thread's state at time of call. Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>
<p><b>Thread Sleep</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents suspending the current thread for a specified number of timer ticks via tx_thread_sleep.</p> <p><b>Information Fields</b> Info Field 1: Number of ticks to suspend for. Info Field 2: Thread's state at time of call. Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>	<p><b>Thread Stack Error Notify</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents registering a thread stack error notification routine via tx_thread_stack_error_notify_event.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Thread Suspend</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents suspending a thread via tx_thread_suspend.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread to suspend. Info Field 2: Thread's state at time of call. Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>	<p><b>Thread Terminate</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents terminating a thread via tx_thread_terminate.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread to terminate. Info Field 2: Thread's state at time of call. Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>

<p><b>Thread Time-Slice Change</b></p> <p style="text-align: right;">tx_thread_time_slice_change</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents changing a thread's time-slice via tx_thread_time_slice_change.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread. Info Field 2: New time-slice. Info Field 3: Previous time-slice. Info Field 4: Not used.</p>	<p><b>Thread Wait Abort</b></p> <p style="text-align: right;">tx_thread_wait_abort</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents aborting a thread's suspension via tx_thread_wait_abort.</p> <p><b>Information Fields</b> Info Field 1: Pointer to thread. Info Field 2: Thread's state at time of call. Info Field 3: Stack pointer at time of call. Info Field 4: Not used.</p>
<p><b>Time Get</b></p> <p style="text-align: right;">tx_time_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting the current number of timer ticks via tx_time_get.</p> <p><b>Information Fields</b> Info Field 1: Current number of timer ticks. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Time Set</b></p> <p style="text-align: right;">tx_time_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents setting the current number of timer ticks via tx_time_set.</p> <p><b>Information Fields</b> Info Field 1: New number of timer ticks. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Timer Activate</b></p> <p style="text-align: right;">tx_timer_activate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents activating the specified timer via tx_timer_activate.</p> <p><b>Information Fields</b> Info Field 1: Pointer to timer. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Timer Change</b></p> <p style="text-align: right;">tx_timer_change</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents changing the specified timer via tx_timer_change.</p> <p><b>Information Fields</b> Info Field 1: Pointer to timer. Info Field 2: Initial expiration ticks. Info Field 3: Reschedule expiration ticks. Info Field 4: Not used.</p>

<p><b>Timer Create</b></p> <p>tx_timer_create</p> <p><b>Icon</b>  </p> <p><b>Description</b> This event represents creating a timer via tx_timer_create.</p> <p><b>Information Fields</b> Info Field 1: Pointer to timer control block. Info Field 2: Initial expiration ticks. Info Field 3: Reschedule expiration ticks. Info Field 4: Automatic enable value—either TX_AUTO_ACTIVATE (1) or TX_NO_ACTIVATE (0).</p>	<p><b>Timer Deactivate</b></p> <p>tx_timer_deactivate</p> <p><b>Icon</b>  </p> <p><b>Description</b> This event represents deactivating a timer via tx_timer_deactivate.</p> <p><b>Information Fields</b> Info Field 1: Pointer to timer. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Timer Delete</b></p> <p>tx_timer_delete</p> <p><b>Icon</b>  </p> <p><b>Description</b> This event represents deleting a timer via tx_timer_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to timer. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Timer Information Get</b></p> <p>tx_timer_info_get</p> <p><b>Icon</b>  </p> <p><b>Description</b> This event represents getting timer information via tx_timer_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to timer. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Timer Performance Information Get</b></p> <p>tx_timer_performance_info_get</p> <p><b>Icon</b>  </p> <p><b>Description</b> This event represents getting timer performance information via tx_timer_performance_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to timer. Info Field 2: Stack pointer at time of call. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Timer System Performance Info Get</b></p> <p>tx_timer_performance_system_info_get</p> <p><b>Icon</b>  </p> <p><b>Description</b> This event represents getting all timer performance information via tx_timer_performance_system_info_get.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>



# 7

## ***FileX Trace Events***

This chapter contains a description of the FileX events.

- List of Events and Icons 118
- Event Descriptions 121

## List of Events and Icons

The following is a list of FileX events displayed by TraceX.

The following describes each event:

Icon	Meaning
	Internal Logical Sector Cache Miss
	Internal Directory Cache Miss
	Internal Media Flush
	Internal Directory Entry Read
	Internal Directory Entry Write
	Internal I/O Driver Read
	Internal I/O Driver Write
	Internal I/O Driver Flush
	Internal I/O Driver Abort
	Internal I/O Driver Initialize
	Internal I/O Driver Boot Read
	Internal I/O Driver Release Sectors
	Internal I/O Driver Boot Write
	Internal I/O Driver Driver Un-initialize
	<b>Directory Attributes Read</b> ( <i>fx_directory_attributes_read</i> )
	<b>Directory Attributes Set</b> ( <i>fx_directory_attributes_set</i> )
	<b>Directory Create</b> ( <i>fx_directory_create</i> )
	<b>Directory Default Get</b> ( <i>fx_directory_default_get</i> )
	<b>Directory Default Set</b> ( <i>fx_directory_default_set</i> )

<b>D D</b>	<b>Directory Delete (<i>fx_directory_delete</i>)</b>
<b>D F</b>	<b>Directory First Entry Find (<i>fx_directory_first_entry_find</i>)</b>
<b>F F</b>	<b>Directory First Full Entry Find (<i>fx_directory_first_full_entry_find</i>)</b>
<b>I G</b>	<b>Directory Information Get (<i>fx_directory_information_get</i>)</b>
<b>L C</b>	<b>Directory Local Path Clear (<i>fx_directory_local_path_clear</i>)</b>
<b>L G</b>	<b>Directory Local Path Get (<i>fx_directory_local_path_get</i>)</b>
<b>L R</b>	<b>Directory Local Path Restore (<i>fx_directory_local_path_restore</i>)</b>
<b>L S</b>	<b>Directory Local Path Set (<i>fx_directory_local_path_set</i>)</b>
<b>N G</b>	<b>Directory Long Name Get (<i>fx_directory_long_name_get</i>)</b>
<b>N T</b>	<b>Directory Name Test (<i>fx_directory_name_test</i>)</b>
<b>N E</b>	<b>Directory Next Entry Find (<i>fx_directory_next_entry_find</i>)</b>
<b>N F</b>	<b>Directory Next Full Entry Find (<i>fx_directory_next_full_entry_find</i>)</b>
<b>D R</b>	<b>Directory Rename (<i>fx_directory_rename</i>)</b>
<b>S G</b>	<b>Directory Short Name Get (<i>fx_directory_short_name_get</i>)</b>
<b>F A</b>	<b>File Allocate (<i>fx_file_allocate</i>)</b>
<b>R F</b>	<b>File Attributes Read (<i>fx_file_attributes_read</i>)</b>
<b>S F</b>	<b>File Attributes Set (<i>fx_file_attributes_set</i>)</b>
<b>B A</b>	<b>File Best Effort Allocate (<i>fx_file_best_effort_allocate</i>)</b>
<b>F C</b>	<b>File Close (<i>fx_file_close</i>)</b>
<b>C R</b>	<b>File Create (<i>fx_file_create</i>)</b>
<b>T S</b>	<b>File Date Time Set (<i>fx_file_date_time_set</i>)</b>
<b>F D</b>	<b>File Delete (<i>fx_file_delete</i>)</b>

- F O** File Open (*fx\_file\_open*)
- F R** File Read (*fx\_file\_read*)
- R S** File Relative Seek (*fx\_file\_relative\_seek*)
- R N** File Rename (*fx\_file\_rename*)
- S K** File Seek (*fx\_file\_seek*)
- F T** File Truncate (*fx\_file\_truncate*)
- T R** File Truncate Release (*fx\_file\_truncate\_release*)
- F W** File Write (*fx\_file\_write*)
- M A** Media Abort (*fx\_media\_abort*)
- C I** Media Cache Invalidate (*fx\_media\_cache\_invalidate*)
- C K** Media Check (*fx\_media\_check*)
- M C** Media Close (*fx\_media\_close*)
- F L** Media Flush (*fx\_media\_flush*)
- M F** Media Format (*fx\_media\_format*)
- M O** Media Open (*fx\_media\_open*)
- M R** Media Read (*fx\_media\_read*)
- S A** Media Space Available (*fx\_media\_space\_available*)
- V G** Media Volume Get (*fx\_media\_volume\_get*)
- V S** Media Volume Set (*fx\_media\_volume\_set*)
- M W** Media Write (*fx\_media\_write*)
- D G** System Date Get (*fx\_system\_date\_get*)
- D S** System Date Set (*fx\_system\_date\_set*)

<b>S</b>	<b>System Initialize</b> ( <i>fx_system_initialize</i> )
<b>T</b>	<b>System Time Get</b> ( <i>fx_system_time_get</i> )
<b>T</b>	<b>System Time Set</b> ( <i>fx_system_time_set</i> )
<b>C</b>	<b>Unicode Directory Create</b> ( <i>fx_unicode_directory_create</i> )
<b>R</b>	<b>Unicode Directory Rename</b> ( <i>fx_unicode_directory_rename</i> )
<b>C</b>	<b>Unicode File Create</b> ( <i>fx_unicode_file_create</i> )
<b>R</b>	<b>Unicode File Rename</b> ( <i>fx_unicode_file_rename</i> )
<b>G</b>	<b>Unicode Length Get</b> ( <i>fx_unicode_length_get</i> )
<b>G</b>	<b>Unicode Name Get</b> ( <i>fx_unicode_name_get</i> )
<b>G</b>	<b>Unicode Short Name Get</b> ( <i>fx_unicode_short_name_get</i> )

## Event Descriptions

The following describes each individual event.

<p><b>Internal Directory Cache Miss</b></p> <p>Icon </p> <p>Internal directory cache miss</p> <p><b>Description</b> This event represents an internal FileX directory cache miss.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Total misses. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Internal Directory Entry Read</b></p> <p>Icon </p> <p>Internal directory entry read</p> <p><b>Description</b> This event represents an internal FileX directory entry read event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Internal Directory Entry Write</b></p> <p>Icon </p> <p>Internal directory entry write</p> <p><b>Description</b> This event represents an internal FileX directory entry write event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Internal I/O Driver Abort</b></p> <p>Icon </p> <p>Internal I/O driver abort</p> <p><b>Description</b> This event represents an internal FileX I/O driver abort event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Internal I/O Driver Boot Sector Read</b></p> <p>Icon </p> <p>Internal I/O driver boot sector read</p> <p><b>Description</b> This event represents an internal FileX I/O driver boot sector read event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Buffer pointer. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Internal I/O Driver Boot Sector Write</b></p> <p>Icon </p> <p>Internal I/O driver boot sector write</p> <p><b>Description</b> This event represents an internal FileX I/O driver boot sector write event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Buffer pointer. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Internal I/O Driver Flush</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal FileX I/O driver flush event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Internal I/O Driver Un-initialize</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal FileX I/O driver un-initialize event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Internal I/O Driver Initialize</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal FileX I/O driver initialize event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Internal I/O Driver Read</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal FileX I/O driver read event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Sector. Info Field 3: Number of sectors. Info Field 4: Buffer pointer.</p>
<p><b>Internal I/O Driver Release Sectors</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal FileX I/O driver release sectors event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Sector. Info Field 3: Number of sectors. Info Field 4: Not used.</p>	<p><b>Internal I/O Driver Write</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal FileX I/O driver write event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Sector. Info Field 3: Number of sectors. Info Field 4: Buffer pointer.</p>

<p><b>Internal Logical Sector Cache Miss</b></p> <p>Internal logical sector cache miss</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal FileX logical sector cache miss.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Sector. Info Field 3: Total misses. Info Field 4: Cache size.</p>	<p><b>Internal Media Flush</b></p> <p>Internal media flush</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal FileX media flush.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Number of dirty sectors. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>File Attributes Read</b></p> <p>fx_file_attributes_read</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a file attributes read event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Attributes bit map: Read Only (0x01) Hidden (0x02) System (0x04) Volume (0x08) Directory (0x10) Archive (0x20)  Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>File Attributes Set</b></p> <p>fx_file_attributes_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a file attributes set event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to file name. Info Field 3: Attributes bit map: Read Only (0x01) Hidden (0x02) System (0x04) Archive (0x20)  Info Field 4: Not used.</p>
<p><b>File Best Effort Allocate</b></p> <p>fx_file_best_effort_allocate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a file best effort allocate event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the file. Info Field 2: Requested size. Info Field 3: Actual size allocated. Info Field 4: Not used.</p>	<p><b>File Close</b></p> <p>fx_file_close</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a file close event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the file. Info Field 2: File size. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Directory Default Set</b></p> <p style="text-align: right;">fx_directory_default_set</p> <p><b>Icon</b>  <b>D</b></p> <p><b>Description</b> This event represents a directory default set event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to new default path name. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Directory Delete</b></p> <p style="text-align: right;">fx_directory_delete</p> <p><b>Icon</b>  <b>D</b></p> <p><b>Description</b> This event represents a directory delete event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to directory name. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Directory First Entry Find</b></p> <p style="text-align: right;">fx_directory_first_entry_find</p> <p><b>Icon</b>  <b>F</b></p> <p><b>Description</b> This event represents a directory first entry find event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to directory name. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Directory First Full Entry Find</b></p> <p style="text-align: right;">fx_directory_first_full_entry_find</p> <p><b>Icon</b>  <b>F</b></p> <p><b>Description</b> This event represents a directory first full entry find event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to directory name. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Directory Information Get</b></p> <p style="text-align: right;">fx_directory_information_get</p> <p><b>Icon</b>  <b>G</b></p> <p><b>Description</b> This event represents a directory information get event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to directory name. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Directory Local Path Clear</b></p> <p style="text-align: right;">fx_directory_local_path_clear</p> <p><b>Icon</b>  <b>L</b></p> <p><b>Description</b> This event represents a directory local path clear event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Directory Local Path Get</b></p> <p style="text-align: center;">fx_directory_local_path_get event</p> <p><b>Icon</b>  </p> <p><b>Description</b> This event represents a directory local path get event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to return path name. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Directory Local Path Restore</b></p> <p style="text-align: center;">fx_directory_local_path_restore event</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a directory local path restore event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to local path structure. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Directory Local Path Set</b></p> <p style="text-align: center;">fx_directory_local_path_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a directory local path set event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to local path structure. Info Field 3: Pointer to new path name. Info Field 4: Not used.</p>	<p><b>Directory Long Name Get</b></p> <p style="text-align: center;">fx_directory_long_name_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a directory long name get event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to short file name. Info Field 3: Pointer to long file name. Info Field 4: Not used.</p>
<p><b>Directory Name Test</b></p> <p style="text-align: center;">fx_directory_name_test</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a directory name test event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to directory name. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Directory Next Entry Find</b></p> <p style="text-align: center;">fx_directory_next_entry_find</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a directory next entry find event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to directory name. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Directory Next Full Entry Find</b></p> <p>fx_directory_next_full_entry_find event</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a directory next full entry find event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to directory name. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Directory Rename</b></p> <p>fx_directory_rename event</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a directory rename event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to old directory name. Info Field 3: Pointer to new directory name. Info Field 4: Not used.</p>
<p><b>Directory Short Name Get</b></p> <p>fx_directory_short_name_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a directory short name get event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to long file name. Info Field 3: Pointer to short file name. Info Field 4: Not used.</p>	<p><b>File Allocate</b></p> <p>fx_file_allocate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a file allocate event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the file. Info Field 2: Requested size. Info Field 3: Current size. Info Field 4: New size.</p>
<p><b>File Create</b></p> <p>fx_file_create</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a file create event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to file name. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>File Date Time Set</b></p> <p>fx_file_date_time_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a file date/time set event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to file name. Info Field 3: Year. Info Field 4: Month.</p>

<p><b>File Delete</b></p> <p> <b>Icon D</b></p> <p><b>Description</b> This event represents a file delete event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to file name. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>File Open</b></p> <p> <b>Icon O</b></p> <p><b>Description</b> This event represents a file open event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to the file control block. Info Field 3: Pointer to file name. Info Field 4: Open type: Open for Read (0x00) Open for Write (0x01) Fast Open for Read (0x02)</p>
<p><b>File Read</b></p> <p> <b>Icon R</b></p> <p><b>Description</b> This event represents a file read event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the file. Info Field 2: Buffer pointer. Info Field 3: Request size. Info Field 4: Actual size read.</p>	<p><b>File Relative Seek</b></p> <p> <b>Icon S</b></p> <p><b>Description</b> This event represents a file relative seek event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the file. Info Field 2: Byte offset. Info Field 3: Seek from: From Beginning (0x00) From End (0x01) Forward (0x02) Backward (0x03) Info Field 4: Previous offset.</p>
<p><b>File Rename</b></p> <p> <b>Icon N</b></p> <p><b>Description</b> This event represents a file rename event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to old file name. Info Field 3: Pointer to new file name. Info Field 4: Not used.</p>	<p><b>File Seek</b></p> <p> <b>Icon K</b></p> <p><b>Description</b> This event represents a file seek event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the file. Info Field 2: Byte offset. Info Field 3: Previous offset. Info Field 4: Not used.</p>

<p><b>File Truncate</b></p> <p> <b>fx_file_truncate</b></p> <p><b>Description</b> This event represents a file truncate event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the file. Info Field 2: Requested size. Info Field 3: Previous size. Info Field 4: New size.</p>	<p><b>File Truncate Release</b></p> <p> <b>fx_file_truncate_release</b></p> <p><b>Description</b> This event represents a file truncate release event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the file. Info Field 2: Requested size. Info Field 3: Previous size. Info Field 4: New size.</p>
<p><b>File Write</b></p> <p> <b>fx_file_write</b></p> <p><b>Description</b> This event represents a file write event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the file. Info Field 2: Buffer pointer. Info Field 3: Request size. Info Field 4: Actual size written.</p>	<p><b>Media Abort</b></p> <p> <b>fx_media_abort</b></p> <p><b>Description</b> This event represents a media abort event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Media Cache Invalidate</b></p> <p> <b>fx_media_cache_invalidate</b></p> <p><b>Description</b> This event represents a media cache invalidate event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Media Check</b></p> <p> <b>fx_media_check</b></p> <p><b>Description</b> This event represents a media check event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Scratch memory pointer. Info Field 3: Scratch memory size. Info Field 4: Errors bit map: FAT Chain Error (0x01) Directory Error (0x02) Lost Cluster Error (0x04) File Size Error (0x08)</p>

<p><b>Media Close</b></p> <p> <b>Icon C</b></p> <p><b>Description</b> This event represents a media close event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Media Flush</b></p> <p> <b>Icon L</b></p> <p><b>Description</b> This event represents a media flush event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Media Format</b></p> <p> <b>Icon F</b></p> <p><b>Description</b> This event represents a media format event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Number of root entries. Info Field 3: Sectors. Info Field 4: Sectors per cluster.</p>	<p><b>Media Open</b></p> <p> <b>Icon O</b></p> <p><b>Description</b> This event represents a media open event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to media driver entry. Info Field 3: Memory pointer. Info Field 4: Memory size.</p>
<p><b>Media Read</b></p> <p> <b>Icon R</b></p> <p><b>Description</b> This event represents a media read event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Logical sector. Info Field 3: Buffer pointer. Info Field 4: Bytes read.</p>	<p><b>Media Space Available</b></p> <p> <b>Icon A</b></p> <p><b>Description</b> This event represents a media space available event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Available bytes pointer. Info Field 3: Number of free clusters. Info Field 4: Not used.</p>

<p><b>Media Volume Get</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a media volume get event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to volume name. Info Field 3: Volume source. Info Field 4: Not used.</p>	<p><b>Media Volume Set</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a media volume set event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to volume name. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Media Write</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a media write event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Logical sector. Info Field 3: Buffer pointer. Info Field 4: Bytes written.</p>	<p><b>System Date Get</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a system date get event.</p> <p><b>Information Fields</b> Info Field 1: Year. Info Field 2: Month. Info Field 3: Day. Info Field 4: Not used.</p>
<p><b>System Date Set</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a system date set event.</p> <p><b>Information Fields</b> Info Field 1: Year. Info Field 2: Month. Info Field 3: Day. Info Field 4: Not used.</p>	<p><b>System Initialize</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a system initialize event.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>System Time Get</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a system time get event.</p> <p><b>Information Fields</b> Info Field 1: Hour. Info Field 2: Minute. Info Field 3: Second. Info Field 4: Not used.</p>	<p><b>System Time Set</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a system time set event.</p> <p><b>Information Fields</b> Info Field 1: Hour. Info Field 2: Minute. Info Field 3: Second. Info Field 4: Not used.</p>
<p><b>Unicode Directory Create</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a Unicode directory create event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to Unicode name. Info Field 3: Size of Unicode name. Info Field 4: Pointer to short name.</p>	<p><b>Unicode Directory Rename</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a Unicode directory rename event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to Unicode name. Info Field 3: Size of Unicode name. Info Field 4: Pointer to short name.</p>
<p><b>Unicode File Create</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a Unicode file create event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to the Unicode name. Info Field 3: Size of Unicode name. Info Field 4: Pointer to short name.</p>	<p><b>Unicode File Rename</b></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a Unicode file rename event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to Unicode name. Info Field 3: Size of Unicode name. Info Field 4: Pointer to short name.</p>

<b>Unicode Length Get</b>  fx_unicode_length_get <b>Description</b> This event represents a Unicode length get event. <b>Information Fields</b> Info Field 1: Pointer to the Unicode name. Info Field 2: Length. Info Field 3: Not used. Info Field 4: Not used.	<b>Unicode Name Get</b>  fx_unicode_name_get <b>Description</b> This event represents a Unicode name get event. <b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Source short name. Info Field 3: Destination Unicode name pointer. Info Field 4: Destination Unicode name length.
<b>Unicode Short Name Get</b>  fx_unicode_short_name_get <b>Description</b> This event represents a Unicode short name get event. <b>Information Fields</b> Info Field 1: Pointer to the media. Info Field 2: Pointer to source Unicode name. Info Field 3: Length of Unicode name. Info Field 4: Pointer to short name.	



# 8

## ***NetX Trace Events***

This chapter contains a description of the NetX events.

- List of Events and Icons 136
- Event Descriptions 143

## List of Events and Icons

The following is a list of NetX events displayed by TraceX.

Icon	Meaning
	Internal ARP Request Receive
	Internal ARP Request Send
	Internal ARP Response Receive
	Internal ARP Response Send
	Internal ICMP Receive
	Internal ICMP Send
	Internal NetX IGMP Receive
	Internal IP Receive
	Internal IP Send
	Internal TCP Data Receive
	Internal TCP Data Send
	Internal TCP FIN Receive
	Internal TCP FIN Send
	Internal TCP RST Receive
	Internal TCP RST Send
	Internal TCP SYN Receive
	Internal TCP SYN Send
	Internal UDP Receive
	Internal UDP Send

<b>P</b>	Internal RARP Receive
<b>I</b>	Internal RARP Send
<b>T</b>	Internal TCP Retry
<b>S</b>	Internal TCP State Change
<b>P</b>	Internal I/O Driver Packet Send
<b>D</b>	Internal I/O Driver Initialize
<b>L</b>	Internal I/O Driver Link Enable
<b>D</b>	Internal I/O Driver Link Disable
<b>P</b>	Internal I/O Driver Packet Broadcast
<b>A</b>	Internal I/O Driver ARP Send
<b>S</b>	Internal I/O Driver ARP Response Send
<b>R</b>	Internal I/O Driver RARP Send
<b>J</b>	Internal I/O Driver Multicast Join
<b>M</b>	Internal I/O Driver Multicast Leave
<b>S</b>	Internal I/O Driver Get Status
<b>G</b>	Internal I/O Driver Get Speed
<b>D</b>	Internal I/O Driver Get Duplex Type
<b>E</b>	Internal I/O Driver Get Error Count
<b>R</b>	Internal I/O Driver Get RX Count
<b>T</b>	Internal I/O Driver Get TX Count
<b>A</b>	Internal I/O Driver Get Allocation Errors
<b>U</b>	Internal I/O Driver Un-initialize

- D P** Internal I/O Driver Deferred Processing
- E I** ARP Dynamic Entries Invalidate  
(*nx\_arp\_dynamic\_entries\_invalidate*)
- E S** ARP Dynamic Entry Set (*nx\_arp\_dynamic\_entry\_set*)
- A E** ARP Enable (*nx\_arp\_enable*)
- G S** ARP Gratuitous Send (*nx\_arp\_gratuitous\_send*)
- H F** ARP Hardware Address Find (*nx\_arp\_hardware\_address\_find*)
- A I** ARP Information Get (*nx\_arp\_info\_get*)
- A F** ARP IP Address Find (*nx\_arp\_ip\_address\_find*)
- S D** ARP Static Entries Delete (*nx\_arp\_static\_entries\_delete*)
- S C** ARP Static Entry Create (*nx\_arp\_static\_entry\_create*)
- D S** ARP Static Entry Delete (*nx\_arp\_static\_entry\_delete*)
- R I** Duo Cache Entry Delete (*nxd\_nd\_cache\_entry\_delete*)
- N S** Duo Cache Entry Set (*nxd\_nd\_cache\_entry\_set*)
- N I** Duo Cache Invalidate (*nxd\_nd\_cache\_invalidate*)
- N E** Duo Cache IP Address Find (*nxd\_nd\_cache\_ip\_address\_find*)
- C S** Duo ICMP Enable (*nxd\_icmp\_enable*)
- C I** Duo ICMP IPv6 Ping (*nxd\_icmp\_ping*)
- M I** Duo IP Max Payload Size Find (*nx\_max\_payload\_size\_find*)
- S I** Duo IP Raw Packet Send (*nxd\_ip\_raw\_packet\_send*)
- D S** Duo IPv6 Default Router Add (*nxd\_ipv6\_default\_router\_add*)
- D U** Duo IPv6 Default Router Delete (*nxd\_ipv6\_default\_router\_delete*)
- G I** Duo IPv6 Enable (*nxd\_ipv6\_enable*)

- 6** Duo IPv6 Global Address Get (*nxd\_ipv6\_global\_address\_get*)
- 6** Duo IPv6 Global Address Set (*nxd\_ipv6\_global\_address\_set*)
- 6** Duo IPv6 Initiate Dad Process (*nxd\_ipv6\_initiate\_dad\_process*)
- A** Duo IPv6 Interface Address Get (*nxd\_ipv6\_interface\_address\_get*)
- A** Duo IPv6 Interface Address Set (*nxd\_ipv6\_interface\_address\_set*)
- L** Duo IPv6 Link Local Address Get (*nxd\_ipv6\_linklocal\_address\_get*)
- L** Duo IPv6 Link Local Address Set (*nxd\_ipv6\_linklocal\_address\_set*)
- S** Duo IPv6 Raw Packet Send (*nxd\_ipv6\_raw\_packet\_send*)
- S** Duo TCP Socket Peer Info Get (*nxd\_tcp\_socket\_peer\_info\_get*)
- T** Duo TCP Socket Set Interface (*nxd\_tcp\_socket\_set\_interface*)
- U** Duo UDP Socket Send (*nxd\_udp\_socket\_send*)
- U** Duo UDP Socket Set Interface (*nxd\_udp\_socket\_set\_interface*)
- U** Duo UDP Source Extract (*nxd\_udp\_source\_extract*)
- C** ICMP Enable (*nx\_icmp\_enable*)
- C** ICMP Information Get (*nx\_icmp\_info\_get*)
- C** ICMP Ping (*nx\_icmp\_ping*)
- G** IGMP Enable (*nx\_igmp\_enable*)
- G** IGMP Information Get (*nx\_igmp\_info\_get*)
- L** IGMP Loopback Disable (*nx\_igmp\_loopback\_disable*)
- L** IGMP Loopback Enable (*nx\_igmp\_loopback\_enable*)
- M** IGMP Multicast Join (*nx\_igmp\_multicast\_join*)
- M** IGMP Multicast Leave (*nx\_igmp\_multicast\_leave*)

- C N** **IP Address Change Notify** (*nx\_ip\_address\_change\_notify*)
- A G** **IP Address Get** (*nx\_ip\_address\_get*)
- A S** **IP Address Set** (*nx\_ip\_address\_set*)
- I C** **IP Create** (*nx\_ip\_create*)
- I D** **IP Delete** (*nx\_ip\_delete*)
- D C** **IP Driver Direct Command** (*nx\_ip\_driver\_direct\_command*)
- F D** **IP Forwarding Disable** (*nx\_ip\_forwarding\_disable*)
- F E** **IP Forwarding Enable** (*nx\_ip\_forwarding\_enable*)
- D F** **IP Fragment Disable** (*nx\_ip\_fragment\_disable*)
- E F** **IP Fragment Enable** (*nx\_ip\_fragment\_enable*)
- G S** **IP Gateway Address Set** (*nx\_ip\_gateway\_address\_set*)
- I I** **IP Information Get** (*nx\_ip\_info\_get*)
- I A** **IP Interface Attach** (*nx\_ip\_interface\_attach*)
- G I** **IP Interface Info Get** (*nx\_ip\_interface\_info\_get*)
- R D** **IP Raw Packet Disable** (*nx\_ip\_raw\_packet\_disable*)
- R E** **IP Raw Packet Enable** (*nx\_ip\_raw\_packet\_enable*)
- R R** **IP Raw Packet Receive** (*nx\_ip\_raw\_packet\_receive*)
- R S** **IP Raw Packet Send** (*nx\_ip\_raw\_packet\_send*)
- S C** **IP Status Check** (*nx\_ip\_status\_check*)
- A R** **IP Static Route Add** (*nx\_ip\_static\_route\_add*)
- D R** **IP Static Route Delete** (*nx\_ip\_static\_route\_delete*)
- S E** **IPSEC Enable** (*nx\_ipsec\_enable*)

- P  
A** **Packet Allocate** (*nx\_packet\_allocate*)
- P  
C** **Packet Copy** (*nx\_packet\_copy*)
- D  
A** **Packet Data Append** (*nx\_packet\_data\_append*)
- E  
O** **Packet Data Extract Offset** (*nx\_packet\_data\_extract\_offset*)
- D  
R** **Packet Data Retrieve** (*nx\_packet\_data\_retrieve*)
- L  
G** **Packet Length Get** (*nx\_packet\_length\_get*)
- P  
C** **Packet Pool Create** (*nx\_packet\_pool\_create*)
- P  
D** **Packet Pool Delete** (*nx\_packet\_pool\_delete*)
- P  
I** **Packet Pool Information Get** (*nx\_packet\_pool\_info\_get*)
- P  
R** **Packet Release** (*nx\_packet\_release*)
- T  
R** **Packet Transmit Release** (*nx\_packet\_transmit\_release*)
- R  
D** **RARP Disable** (*nx\_rarp\_disable*)
- R  
E** **RARP Enable** (*nx\_rarp\_enable*)
- R  
I** **RARP Information Get** (*nx\_rarp\_info\_get*)
- S  
I** **System Initialize** (*nx\_system\_initialize*)
- S  
B** **TCP Client Socket Bind** (*nx\_tcp\_client\_socket\_bind*)
- S  
C** **TCP Client Socket Connect** (*nx\_tcp\_client\_socket\_connect*)
- P  
G** **TCP Client Socket Port Get** (*nx\_tcp\_client\_socket\_port\_get*)
- S  
U** **TCP Client Socket Unbind** (*nx\_tcp\_client\_socket\_unbind*)
- T  
E** **TCP Enable** (*nx\_tcp\_enable*)
- P  
F** **TCP Free Port Find** (*nx\_tcp\_free\_port\_find*)
- T  
I** **TCP Information Get** (*nx\_tcp\_info\_get*)
- S  
A** **TCP Server Socket Accept** (*nx\_tcp\_server\_socket\_accept*)

<b>S L</b>	<b>TCP Server Socket Listen</b> ( <i>nx_tcp_server_socket_listen</i> )
<b>S R</b>	<b>TCP Server Socket Relisten</b> ( <i>nx_tcp_server_socket_relisten</i> )
<b>S U</b>	<b>TCP Server Socket Unaccept</b> ( <i>nx_tcp_server_socket_unaccept</i> )
<b>U L</b>	<b>TCP Server Socket Unlisten</b> ( <i>nx_tcp_server_socket_unlisten</i> )
<b>A B</b>	<b>TCP Socket Bytes Available</b> ( <i>nx_tcp_socket_bytes_available</i> )
<b>S N</b>	<b>TCP Socket Create</b> ( <i>nx_tcp_socket_create</i> )
<b>S D</b>	<b>TCP Socket Delete</b> ( <i>nx_tcp_socket_delete</i> )
<b>D C</b>	<b>TCP Socket Disconnect</b> ( <i>nx_tcp_socket_disconnect</i> )
<b>S I</b>	<b>TCP Socket Information Get</b> ( <i>nx_tcp_socket_info_get</i> )
<b>M G</b>	<b>TCP Socket MSS Get</b> ( <i>nx_tcp_socket_mss_get</i> )
<b>P G</b>	<b>TCP Socket MSS Peer Get</b> ( <i>nx_tcp_socket_mss_peer_get</i> )
<b>M S</b>	<b>TCP Socket MSS Set</b> ( <i>nx_tcp_socket_mss_set</i> )
<b>S G</b>	<b>TCP Socket Peer Info Get</b> ( <i>nx_tcp_socket_peer_info_get</i> )
<b>S R</b>	<b>TCP Socket Receive</b> ( <i>nx_tcp_socket_receive</i> )
<b>R N</b>	<b>TCP Socket Receive Notify</b> ( <i>nx_tcp_socket_receive_notify</i> )
<b>S S</b>	<b>TCP Socket Send</b> ( <i>nx_tcp_socket_send</i> )
<b>S W</b>	<b>TCP Socket State Wait</b> ( <i>nx_tcp_socket_state_wait</i> )
<b>T C</b>	<b>TCP Socket Transmit Configure</b> ( <i>nx_tcp_socket_transmit_configure</i> )
<b>W U</b>	<b>TCP Socket Window Update Notify Set</b> ( <i>nx_tcp_socket_window_update_notify_set</i> )
<b>U E</b>	<b>UDP Enable</b> ( <i>nx_udp_enable</i> )
<b>P F</b>	<b>UDP Free Port Find</b> ( <i>nx_udp_free_port_find</i> )
<b>U I</b>	<b>UDP Information Get</b> ( <i>nx_udp_info_get</i> )

- S  
B** UDP Socket Bind (*nx\_udp\_socket\_bind*)
- A  
B** UDP Socket Bytes Available (*nx\_udp\_socket\_bytes\_available*)
- C  
D** UDP Socket Checksum Disable  
(*nx\_udp\_socket\_checksum\_disable*)
- C  
E** UDP Socket Checksum Enable (*nx\_udp\_socket\_checksum\_enable*)
- S  
C** UDP Socket Create (*nx\_udp\_socket\_create*)
- S  
D** UDP Socket Delete (*nx\_udp\_socket\_delete*)
- S  
I** UDP Socket Information Get (*nx\_udp\_socket\_info\_get*)
- S  
F** UDP Socket Interface Set (*nx\_udp\_socket\_interface\_set*)
- P  
G** UDP Socket Port Get (*nx\_udp\_socket\_port\_get*)
- S  
R** UDP Socket Receive (*nx\_udp\_socket\_receive*)
- R  
N** UDP Socket Receive Notify (*nx\_udp\_socket\_receive\_notify*)
- S  
S** UDP Socket Send (*nx\_udp\_socket\_send*)
- S  
U** UDP Socket Unbind (*nx\_udp\_socket\_unbind*)
- S  
E** UDP Source Extract (*nx\_udp\_source\_extract*)

## Event Descriptions

The following pages describe the NetX Trace Events.

<p><b>Internal ARP Request Receive</b></p> <p>Icon </p> <p>Internal ARP request receive</p> <p><b>Description</b> This event represents an internal NetX ARP request receive event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Source IP address Info Field 3: Pointer to packet Info Field 4: Not used</p>	<p><b>Internal ARP Request Send</b></p> <p>Icon </p> <p>Internal ARP request send</p> <p><b>Description</b> This event represents an internal NetX ARP request send event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Destination IP address Info Field 3: Pointer to packet Info Field 4: Not used</p>
<p><b>Internal ARP Response Receive</b></p> <p>Icon </p> <p>Internal ARP Response Receive</p> <p><b>Description</b> This event represents an internal NetX ARP response receive event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Source IP address Info Field 3: Pointer to packet Info Field 4: Not used</p>	<p><b>Internal ARP Response Send</b></p> <p>Icon </p> <p>Internal ARP response send</p> <p><b>Description</b> This event represents an internal NetX response send event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Destination IP address Info Field 3: Pointer to packet Info Field 4: Not used</p>
<p><b>Internal ICMP Receive</b></p> <p>Icon </p> <p>Internal ICMP receive</p> <p><b>Description</b> This event represents an internal NetX ICMP receive event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Source IP address Info Field 3: Pointer to packet Info Field 4: Word 0 of ICMP header</p>	<p><b>Internal ICMP Send</b></p> <p>Icon </p> <p>Internal ICMP send</p> <p><b>Description</b> This event represents an internal NetX ICMP send event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Destination IP address Info Field 3: Pointer to packet Info Field 4: Word 0 of ICMP header</p>

<p><b>Internal IGMP Receive</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX IGMP receive event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: IP Pointer</li> <li>Info Field 2: Source IP address</li> <li>Info Field 3: Pointer to packet</li> <li>Info Field 4: Word 0 of IGMP header</li> </ul>	<p><b>Internal IP Receive</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX IP receive event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Source IP address</li> <li>Info Field 3: Pointer to packet</li> <li>Info Field 4: Packet length</li> </ul>
<p><b>Internal IP Send</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX IP send event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Destination IP address</li> <li>Info Field 3: Pointer to packet</li> <li>Info Field 4: Packet length</li> </ul>	<p><b>Internal TCP Data Receive</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX TCP data receive event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Source IP address</li> <li>Info Field 3: Pointer to packet</li> <li>Info Field 4: Receive sequence number</li> </ul>
<p><b>Internal TCP Data Send</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX TCP data send event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Pointer to socket</li> <li>Info Field 3: Pointer to packet</li> <li>Info Field 4: Transmit sequence number</li> </ul>	<p><b>Internal TCP FIN Receive</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX TCP FIN receive event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Pointer to socket</li> <li>Info Field 3: Pointer to packet</li> <li>Info Field 4: Receive sequence number</li> </ul>

<p><b>Internal TCP FIN Send</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX TCP FIN send event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to socket Info Field 3: Pointer to packet Info Field 4: Transmit sequence number</p>	<p><b>Internal TCP RST Receive</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX TCP reset receive event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance. Info Field 2: Pointer to socket. Info Field 3: Pointer to packet. Info Field 4: Receive sequence number.</p>
<p><b>Internal TCP RST Send</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX TCP reset send event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to socket Info Field 3: Pointer to packet Info Field 4: Transmit sequence number</p>	<p><b>Internal TCP SYN Receive</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX TCP SYN receive event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to socket Info Field 3: Pointer to packet Info Field 4: Receive sequence number</p>
<p><b>Internal TCP SYN Send</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX TCP SYN send event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to socket Info Field 3: Pointer to packet Info Field 4: Transmit sequence number</p>	<p><b>Internal UDP Receive</b></p> <p>Icon </p> <p><b>Description</b> This event represents an internal NetX UDP receive event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to socket Info Field 3: Pointer to packet Info Field 4: Word 0 of UDP header</p>

<p><b>Internal UDP Send</b></p>  <p>Internal UDP send</p> <p><b>Description</b> This event represents an internal NetX UDP send event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Pointer to socket</li> <li>Info Field 3: Pointer to packet</li> <li>Info Field 4: Word 0 of UDP header</li> </ul>	<p><b>Internal RARP Receive</b></p>  <p>Internal RARP receive</p> <p><b>Description</b> This event represents an internal NetX RARP receive event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Target IP address</li> <li>Info Field 3: Pointer to packet</li> <li>Info Field 4: Word 1 of RARP header</li> </ul>
<p><b>Internal RARP Send</b></p>  <p>Internal RARP send</p> <p><b>Description</b> This event represents an internal NetX RARP send event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Target IP address</li> <li>Info Field 3: Pointer to packet</li> <li>Info Field 4: Word 1 of RARP header</li> </ul>	<p><b>Internal TCP Retry</b></p>  <p>Internal TCP retry</p> <p><b>Description</b> This event represents an internal NetX TCP retry event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Pointer to socket</li> <li>Info Field 3: Pointer to packet</li> <li>Info Field 4: Number of retries</li> </ul>
<p><b>Internal TCP State Change</b></p>  <p>Internal TCP state change</p> <p><b>Description</b> This event represents an internal NetX TCP socket state change event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Pointer to socket</li> <li>Info Field 3: Previous state</li> <li>Info Field 4: New state</li> </ul>	<p><b>Internal I/O Driver Packet Send</b></p>  <p>Internal I/O driver packet send</p> <p><b>Description</b> This event represents an internal NetX I/O driver packet send event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Pointer to packet</li> <li>Info Field 3: Packet size</li> <li>Info Field 4: Not used</li> </ul>

<p><b>Internal I/O Driver Initialize</b></p> <p>Internal I/O driver initialize</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver initialize event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Internal I/O Driver Link Enable</b></p> <p>Internal I/O driver link enable</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver link enable event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Internal I/O Driver Link Disable</b></p> <p>Internal I/O driver link disable</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver link disable event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Internal I/O Driver Packet Broadcast</b></p> <p>Internal I/O driver packet broadcast</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver packet broadcast event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to packet Info Field 3: Packet size Info Field 4: Not used</p>
<p><b>Internal I/O Driver ARP Send</b></p> <p>Internal I/O driver ARP send</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver ARP send event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to packet Info Field 3: Packet size Info Field 4: Not used</p>	<p><b>Internal I/O Driver ARP Response Send</b></p> <p>Internal I/O driver ARP response send</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver ARP response send event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to packet Info Field 3: Packet size Info Field 4: Not used</p>

<p><b>Internal I/O Driver RARP Send</b></p> <p>Internal I/O driver RARP send</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver RARP send event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to packet Info Field 3: Packet size Info Field 4: Not used</p>	<p><b>Internal I/O Driver Multicast Join</b></p> <p>Internal I/O driver multicast join</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver multicast join event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>Internal I/O Driver Multicast Leave</b></p> <p>Internal I/O driver multicast leave</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver multicast leave event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Internal I/O Driver Get Status</b></p> <p>Internal I/O driver get status</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver get status event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>Internal I/O Driver Get Speed</b></p> <p>Internal I/O driver get speed</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver get speed event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Internal I/O Driver Get Duplex Type</b></p> <p>Internal I/O driver get duplex type</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents an internal NetX I/O driver get duplex type event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>

<p><b>Internal I/O Driver Get Error Count</b></p> <p>Internal I/O driver get error count</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents an internal NetX I/O driver get error count event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Internal I/O Driver Get RX Count</b></p> <p>Internal I/O driver get RX count</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents an internal NetX I/O driver get RX count event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>Internal I/O Driver Get TX Count</b></p> <p>Internal I/O driver get TX count</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents an internal NetX I/O driver get TX count event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Internal I/O Driver Get Allocation Errors</b></p> <p>Internal I/O driver get allocation errors</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents an internal NetX I/O driver get allocation errors event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>Internal I/O Driver Un-initialize</b></p> <p>Internal I/O driver un-initialize</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents an internal NetX I/O driver un-initialize event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Internal I/O Driver Deferred Processing</b></p> <p>Internal I/O driver deferred processing</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents an internal NetX I/O driver deferred processing event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to packet Info Field 3: Packet length Info Field 4: Not used</p>

<p><b>ARP Dynamic Entries Invalidate</b></p> <p style="text-align: center;">nx_arp_dynamic_entries_invalidate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents invalidating all dynamic ARP entries via nx_arp_dynamic_entries_invalidate.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Entries invalidated Info Field 3: Not used Info Field 4: Not used</p>	<p><b>ARP Dynamic Entry Set</b></p> <p style="text-align: center;">nx_arp_dynamic_entry_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents setting a dynamic ARP entry via nx_arp_dynamic_entry_set.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: IP address Info Field 3: Physical address (MSW) Info Field 4: Physical address (LSW)</p>
<p><b>ARP Enable</b></p> <p style="text-align: center;">nx_arp_enable</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents enabling ARP via nx_arp_enable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: ARP cache memory pointer Info Field 3: ARP cache memory size Info Field 4: Not used</p>	<p><b>ARP Gratuitous Send</b></p> <p style="text-align: center;">nx_arp_gratuitous_send</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a gratuitous ARP send via nx_arp_gratuitous_send.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>ARP Hardware Address Find</b></p> <p style="text-align: center;">nx_arp_hardware_address_find</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents finding a physical address via nx_arp_hardware_address_find.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: IP address Info Field 3: Physical address (MSW) Info Field 4: Physical address (LSW)</p>	<p><b>ARP Information Get</b></p> <p style="text-align: center;">nx_arp_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting information via nx_arp_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: ARPs sent Info Field 3: ARP responses Info Field 4: ARPs received</p>

<p><b>ARP IP Address Find</b></p> <p style="text-align: right;"><code>nx_arp_ip_address_find</code></p> <p><b>Icon</b>  <b>A</b></p> <p><b>Description</b> This event represents finding an IP address associated with the supplied physical address via <code>nx_arp_ip_address_find</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: IP address Info Field 3: Physical address (MSW) Info Field 4: Physical address (LSW)</p>	<p><b>ARP Static Entries Delete</b></p> <p style="text-align: right;"><code>nx_arp_static_entries_delete</code></p> <p><b>Icon</b>  <b>D</b></p> <p><b>Description</b> This event represents deleting all ARP static entries via <code>nx_arp_static_entries_delete</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Entries deleted Info Field 3: Not used Info Field 4: Not used</p>
<p><b>ARP Static Entry Create</b></p> <p style="text-align: right;"><code>nx_arp_static_entry_create</code></p> <p><b>Icon</b>  <b>C</b></p> <p><b>Description</b> This event represents creating a static ARP entry via <code>nx_arp_static_entry_create</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: IP address Info Field 3: Physical address (MSW) Info Field 4: Physical address (LSW)</p>	<p><b>ARP Static Entry Delete</b></p> <p style="text-align: right;"><code>nx_arp_static_entry_delete</code></p> <p><b>Icon</b>  <b>D</b></p> <p><b>Description</b> This event represents deleting a static ARP entry via <code>nx_arp_static_entry_delete</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: IP address Info Field 3: Physical address (MSW) Info Field 4: Physical address (LSW)</p>
<p><b>Duo Cache Entry Delete</b></p> <p style="text-align: right;"><code>nxd_nd_cache_entry_delete</code></p> <p><b>Icon</b>  <b>N</b></p> <p><b>Description</b> This event represents deleting an entry in the neighbor cache table via <code>nxd_nd_cache_entry_delete</code>.</p> <p><b>Information Fields</b> Info Field 1: Fourth (least significant) word of the IPv6 link local address to delete Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Duo Cache Entry Set</b></p> <p style="text-align: right;"><code>nxd_nd_cache_entry_set</code></p> <p><b>Icon</b>  <b>S</b></p> <p><b>Description</b> This event represents creating a cache entry and adding to the neighbor cache table via <code>nxd_nd_cache_entry_set</code>.</p> <p><b>Information Fields</b> Info Field 1: Fourth (least significant) word of the IPv6 address to add Info Field 2: Physical address msb Info Field 3: Physical address lsb Info Field 4: Not used</p>

<p><b>Duo Cache Invalidate</b></p> <p>nxd_nd_cache_invalidate</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents invalidating the entire neighbor cache table via nxd_nd_cache_invalidate.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Duo Cache IP Address Find</b></p> <p>nxd_nd_cache_ip_address_find</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents retrieving an IP address matching the supplied physical address from the cache table via nxd_nd_cache_ip_address_find.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Fourth (least significant) word of the IPv6 address Info Field 3: Physical address msb Info Field 4: Physical address lsb</p>
<p><b>Duo ICMP Enable</b></p> <p>nxd_icmp_enable</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents ICMPv4 and ICMPv6 services being enabled on the specified IP instance via nxd_icmp_enable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Duo ICMP Ping</b></p> <p>nxd_icmp_ping</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents sending a ping (echo request) to an IPv6 host via nxd_icmp_ping.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: IPv6 address Info Field 3: Pointer to echo data Info Field 4: Size of echo data</p>
<p><b>Duo IP Max Payload Size Find</b></p> <p>nx_ip_max_payload_size</p> <p> <b>Icon</b></p> <p><b>Description</b> This event computes the max payload the specified packet can carry without requiring fragmentation.</p> <p><b>Information Fields</b> Info Field 1: Socket pointer Info Field 2: Peer IP address Info Field 3: Peer port Info Field 4: Not used</p>	<p><b>Duo IP Raw Packet Send</b></p> <p>nxd_ip_raw_packet_send</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents sending a raw IP packet out the specified network interface to the supplied IP destination address via nxd_ip_raw_packet_send.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to packet to send Info Field 3: Pointer to destination address Info Field 4: Packet protocol</p>

<p><b>Duo IPv6 Default Router Delete</b></p> <p>nxd_ipv6_default_router_delete</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents removing a default router from the IP instance's IPv6 routing table via nxd_ipv6_default_router_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance. Info Field 2: Fourth word (least significant) of the default router IPv6 address. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Duo IPv6 Enable</b></p> <p>nxd_ipv6_enable event</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents enabling IPv6 services on the supplied IP instance via nxd_ipv6_enable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>Duo IPv6 Global Address Get</b></p> <p>nxd_ipv6_global_address_get</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents retrieving the global (primary) IP address on the IP instance located at index 1 in the IP instance interface table via nxd_ipv6_global_address_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance. Info Field 2: Fourth word (least significant) of the global address Info Field 3: IPv6 address prefix length. Info Field 4: Index into IP interface table (1).</p>	<p><b>Duo IPv6 Global Address Set</b></p> <p>nxd_ipv6_global_address_set</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents setting the global (primary) IP address on the IP instance located at index 1 in the IP instance interface table via nxd_ipv6_global_address_set.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Fourth word (least significant) of the global address Info Field 3: IPv6 address prefix length Info Field 4: Index into IP interface table (1)</p>
<p><b>Duo IPv6 Initiate Dad Process</b></p> <p>nxd_ipv6_initiate_dad_process</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents the start of the Duplicate Address Detection (DAD) process when the IP instance is assigned a link local or an IP interface address via nxd_ipv6_initiate_dad_process.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Duo IPv6 Interface Address Get</b></p> <p>nxd_ipv6_interface_address_get</p> <p> <b>Icon</b></p> <p><b>Description</b> This event represents retrieving the IP address and prefix at the specified index into the IP instance interface address table via nxd_ipv6_interface_address_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Fourth word (least significant) of the IPv6 address to return Info Field 3: Prefix length Info Field 4: Index of interface into the IP instance interface table</p>

<p><b>Duo IPv6 Interface Address Set</b></p> <p>nxd_ipv6_interface_address_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents setting the IP address and prefix at the specified index into the IP instance interface address table. Not permitted on index zero (link local address) via nxd_ipv6_interface_address_set.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Fourth word (least significant) of the IPv6 address to return Info Field 3: Prefix length Info Field 4: Index of interface into the IP instance interface table</p>	<p><b>Duo IPv6 Link Local Address Get</b></p> <p>nxd_ipv6_linklocal_address_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents retrieving the link local address of the specified IP instance via nxd_ipv6_linklocal_address_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Fourth word (least significant) of the IP v6 link local address Info Field 3: Not used Info Field 4: Not used</p>
<p><b>Duo IPv6 Link Local Address Set</b></p> <p>nxd_ipv6_linklocal_address_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents setting the link local address of the IP instance via nxd_ipv6_linklocal_address_set.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Fourth (least significant) word of the IPv6 link local address Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Duo TCP Socket Peer Info Get</b></p> <p>nxd_tcp_socket_peer_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event extracts the sender data from a received TCP packet on the specified socket. It returns the IP address and port of the sender.</p> <p><b>Information Fields</b> Info Field 1: Socket pointer Info Field 2: Peer IP address Info Field 3: Peer port Info Field 4: Not used</p>
<p><b>Duo TCP Socket Set Interface</b></p> <p>nxd_tcp_socket_set_interface event</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents setting the outgoing socket interface after a client connects with a TCP server on the specified server IP address via nxd_tcp_client_socket_connect.</p> <p><b>Information Fields</b> Info Field 1: Pointer to TCP Socket Info Field 2: Interface ID Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Duo UDP Socket Set Interface</b></p> <p>nxd_udp_socket_set_interface event</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents setting the specified UDP socket outgoing interface to the interface corresponding to the input interface ID via nxd_udp_socket_set_interface.</p> <p><b>Information Fields</b> Info Field 1: Pointer to UDP Socket Info Field 2: Interface ID Info Field 3: Not used Info Field 4: Not used</p>

<p><b>Duo UDP Source Extract</b></p> <p>nxd_udp_source_extract</p> <p> </p> <p><b>Description</b> This event represents extracting the IP address and source port of a received packet (either IPv4 or IPv6). If IPv6, the fourth word (least significant) of the IP address is returned via nxd_udp_source_extract.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the packet Info Field 2: IP version Info Field 3: Source IP address (IPv4 or IPv6) Info Field 4: Source port</p>	<p><b>Duo UDP Source Send</b></p> <p>nxd_udp_source_send event</p> <p> </p> <p><b>Description</b> This event represents enabling checksum processing on a socket via nxd_udp_source_send.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP socket Info Field 2: Pointer to packet Info Field 3: Packet Size Info Field 4: IP Address</p>
<p><b>ICMP Enable</b></p> <p>nx_icmp_enable</p> <p> </p> <p><b>Description</b> This event represents enabling ICMP via nx_icmp_enable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>ICMP Information Get</b></p> <p>nx_icmp_info_get</p> <p> </p> <p><b>Description</b> This event represents getting information via nx_icmp_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pings sent Info Field 3: Ping responses Info Field 4: Pings received</p>
<p><b>ICMP Ping</b></p> <p>nx_icmp_ping</p> <p> </p> <p><b>Description</b> This event represents pinging a target IP address via nx_icmp_ping.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: IP address Info Field 3: Pointer to data Info Field 4: Size of data</p>	<p><b>IGMP Enable</b></p> <p>nx_igmp_enable</p> <p> </p> <p><b>Description</b> This event represents enabling IGMP via nx_igmp_enable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>

<p><b>IGMP Information Get</b></p> <p>nx_igmp_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting information via nx_igmp_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Reports sent Info Field 3: Queries received Info Field 4: Groups joined</p>	<p><b>IGMP Loopback Disable</b></p> <p>nx_igmp_loopback_disable</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents disabling IGMP loopback via nx_igmp_loopback_disable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>IGMP Loopback Enable</b></p> <p>nx_igmp_loopback_enable</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents enabling IGMP loopback via nx_igmp_loopback_enable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>IGMP Multicast Join</b></p> <p>nx_igmp_multicast_join</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents joining a multicast group via nx_igmp_multicast_join.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Group IP address Info Field 3: Not used Info Field 4: Not used</p>
<p><b>IGMP Multicast Leave</b></p> <p>nx_igmp_multicast_leave</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents leaving a multicast group via nx_igmp_multicast_leave.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Group IP address Info Field 3: Not used Info Field 4: Not used</p>	<p><b>IP Address Change Notify</b></p> <p>nx_ip_address_change_notify</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents registering for IP change notification via nx_ip_address_change_notify.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Callback function pointer Info Field 3: Additional information pointer Info Field 4: Not used</p>

<p><b>IP Address Get</b></p> <p>Icon  </p> <p><b>Description</b> This event represents getting the IP address via nx_ip_address_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: IP address Info Field 3: Network mask Info Field 4: Not used</p>	<p><b>IP Address Set</b></p> <p>nx_ip_address_set</p> <p>Icon  </p> <p><b>Description</b> This event represents setting the IP address via nx_ip_address_set.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: IP address Info Field 3: Network mask Info Field 4: Not used</p>
<p><b>IP Create</b></p> <p>nx_ip_create</p> <p>Icon  </p> <p><b>Description</b> This event represents creating an IP instance via nx_ip_create.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: IP address Info Field 3: Network mask Info Field 4: Default packet pool pointer</p>	<p><b>IP Delete</b></p> <p>nx_ip_delete</p> <p>Icon  </p> <p><b>Description</b> This event represents deleting an IP instance via nx_ip_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>IP Driver Direct Command</b></p> <p>nx_ip_driver_direct_command</p> <p>Icon  </p> <p><b>Description</b> This event represents a direct I/O driver command via nx_ip_driver_direct_command.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Driver command Info Field 3: Return value Info Field 4: Not used</p>	<p><b>IP Forwarding Disable</b></p> <p>nx_ip_forwarding_disable</p> <p>Icon  </p> <p><b>Description</b> This event represents disabling IP forwarding via nx_ip_forwarding_disable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>

<p><b>IP Forwarding Enable</b></p> <p style="text-align: right;">nx_ip_address_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents enabling IP forwarding via nx_ip_forwarding_enable.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Not used</li> <li>Info Field 3: Not used</li> <li>Info Field 4: Not used</li> </ul>	<p><b>IP Fragment Disable</b></p> <p style="text-align: right;">nx_ip_fragment_disable</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents disabling IP fragmenting via nx_ip_fragment_disable.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Not used</li> <li>Info Field 3: Not used</li> <li>Info Field 4: Not used</li> </ul>
<p><b>IP Fragment Enable</b></p> <p style="text-align: right;">nx_ip_fragment_enable</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents enabling IP fragmenting via nx_ip_fragment_enable.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Not used</li> <li>Info Field 3: Not used</li> <li>Info Field 4: Not used</li> </ul>	<p><b>IP Gateway Address Set</b></p> <p style="text-align: right;">nx_ip_gateway_address_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents setting the gateway IP address via nx_ip_gateway_address_set.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: Gateway IP address</li> <li>Info Field 3: Not used</li> <li>Info Field 4: Not used</li> </ul>
<p><b>IP Information Get</b></p> <p style="text-align: right;">nx_ip_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting IP information via nx_ip_info_get.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to the IP instance</li> <li>Info Field 2: IP bytes sent</li> <li>Info Field 3: IP bytes received</li> <li>Info Field 4: IP packets dropped</li> </ul>	<p><b>IP Interface Attach</b></p> <p style="text-align: right;">nx_ip_interface_attach</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a secondary network interface being attached to the IP instance via nx_ip_interface_attach.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Pointer to IP instance</li> <li>Info Field 2: Interface IP Address</li> <li>Info Field 3: Index into IP interface table</li> <li>Info Field 4: Not used</li> </ul>

<p><b>IP Interface Info Get</b></p> <p> nx_ip_interface_info_get</p> <p><b>Description</b> This event represents information retrieved from the specified network interface via nx_ip_interface_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Interface IP address Info Field 3: Interface MAC address msb Info Field 4: Interface MAC address lsb</p>	<p><b>IP Raw Packet Disable</b></p> <p> nx_ip_raw_packet_disable</p> <p><b>Description</b> This event represents disabling raw IP packet communication via nx_ip_raw_packet_disable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>IP Raw Packet Enable</b></p> <p> nx_ip_address_get</p> <p><b>Description</b> This event represents enabling raw IP packet communication via nx_ip_raw_packet_enable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>IP Raw Packet Receive</b></p> <p> nx_ip_raw_packet_receive</p> <p><b>Description</b> This event represents receiving a raw IP packet via nx_ip_raw_packet_receive.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to packet Info Field 3: Wait option Info Field 4: Not used</p>
<p><b>IP Raw Packet Send</b></p> <p> nx_ip_raw_packet_send</p> <p><b>Description</b> This event represents sending a raw IP packet via nx_ip_raw_packet_send.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Pointer to packet Info Field 3: Destination IP address Info Field 4: Type of service</p>	<p><b>IP Static Route Add</b></p> <p> nx_ip_static_route_add</p> <p><b>Description</b> This event represents a static route being added to the IP instance routing table via nx_ip_static_route_add.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Network address Info Field 3: Network mask Info Field 4: Next hop</p>

<p><b>IP Static Route Delete</b></p> <p>Icon </p> <p><b>Description</b> This event represents a static route being removed from the IP instance routing table via <code>nx_ip_static_route_delete</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Network address Info Field 3: Network mask Info Field 4: Not used</p>	<p><b>IP Status Check</b></p> <p>Icon </p> <p><b>Description</b> This event represents checking for an IP status via <code>nx_ip_status_check</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the IP instance Info Field 2: Requested status Info Field 3: Actual status Info Field 4: Wait option</p>
<p><b>IPSEC Enable</b></p> <p>Icon </p> <p><b>Description</b> This event represents enabling IPSec services on the supplied IP instance via <code>nx_ipsec_enable</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Packet Allocate</b></p> <p>Icon </p> <p><b>Description</b> This event represents allocating a packet via <code>nx_packet_allocate</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the packet pool Info Field 2: Pointer to packet allocated Info Field 3: Packet type Info Field 4: Available packets</p>
<p><b>Packet Copy</b></p> <p>Icon </p> <p><b>Description</b> This event represents copying a packet via <code>nx_packet_copy</code>.</p> <p><b>Information Fields</b> Info Field 2: New packet pointer Info Field 3: Pointer to packet pool Info Field 4: Wait option</p>	<p><b>Packet Data Append</b></p> <p>Icon </p> <p><b>Description</b> This event represents appending data to a packet via <code>nx_packet_data_append</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the packet Info Field 2: Pointer to data Info Field 3: Size of data Info Field 4: Pointer to packet pool</p>

<p><b>Packet Data Extract Offset</b></p> <p> <code>nx_udp_source_extract_offset</code></p> <p><b>Description</b> This event represents packet data that is extracted into a supplied buffer from a packet via <code>nx_udp_source_extract_offset</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to packet Info Field 2: Size of specified buffer Info Field 3: Number of bytes copied Info Field 4: Not used</p>	<p><b>Packet Data Retrieve</b></p> <p> <code>nx_packet_data_retrieve</code></p> <p><b>Description</b> This event represents retrieving data from a packet via <code>nx_packet_data_retrieve</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the packet Info Field 2: Pointer to start of buffer Info Field 3: Bytes copied Info Field 4: Not used</p>
<p><b>Packet Length Get</b></p> <p> <code>nx_packet_length_get</code></p> <p><b>Description</b> This event represents getting the length of a packet via <code>nx_packet_length_get</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the packet Info Field 2: Packet length Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Packet Pool Create</b></p> <p> <code>nx_packet_pool_create</code></p> <p><b>Description</b> This event represents creating a packet pool via <code>nx_packet_pool_create</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the packet pool Info Field 2: Packet payload size Info Field 3: Pointer to pool memory area Info Field 4: Size of pool memory area</p>
<p><b>Packet Pool Delete</b></p> <p> <code>nx_packet_pool_delete</code></p> <p><b>Description</b> This event represents deleting a packet pool via <code>nx_packet_pool_delete</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the packet pool Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>Packet Pool Information Get</b></p> <p> <code>nx_packet_pool_info_get</code></p> <p><b>Description</b> This event represents getting packet pool information via <code>nx_packet_pool_info_get</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to packet pool Info Field 2: Total packets Info Field 3: Available packets Info Field 4: Empty requests</p>

<p><b>Packet Release</b></p> <p> nx_packet_release</p> <p><b>Description</b> This event represents releasing a packet via nx_packet_release.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the packet Info Field 2: Packet status Info Field 3: Available packets Info Field 4: Not used</p>	<p><b>Packet Transmit Release</b></p> <p> nx_packet_transmit_release</p> <p><b>Description</b> This event represents releasing a transmit packet via nx_packet_transmit_release.</p> <p><b>Information Fields</b> Info Field 1: Pointer to the packet Info Field 2: Packet status Info Field 3: Available packets Info Field 4: Not used</p>
<p><b>RARP Disable</b></p> <p> nx_rarp_disable</p> <p><b>Description</b> This event represents disabling RARP via nx_rarp_disable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>RARP Enable</b></p> <p> nx_rarp_enable</p> <p><b>Description</b> This event represents enabling RARP via nx_rarp_enable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>RARP Information Get</b></p> <p> nx_rarp_info_get</p> <p><b>Description</b> This event represents getting RARP information via nx_rarp_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Requests sent Info Field 3: Responses received Info Field 4: Invalid responses</p>	<p><b>System Initialize</b></p> <p> nx_system_initialize</p> <p><b>Description</b> This event represents initializing NetX via nx_system_initialize.</p> <p><b>Information Fields</b> Info Field 1: Not used Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>

<p><b>TCP Client Socket Bind</b></p> <p style="text-align: right;">nx_tcp_client_socket_bind</p> <p><b>Icon B</b></p> <p><b>Description</b> This event represents binding a client socket to a port via nx_tcp_client_socket_bind.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Port requested Info Field 4: Wait option</p>	<p><b>TCP Client Socket Connect</b></p> <p style="text-align: right;">nx_tcp_client_socket_connect</p> <p><b>Icon C</b></p> <p><b>Description</b> This event represents making a client socket connection via nx_tcp_client_socket_connect.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Server IP address Info Field 4: Server port requested</p>
<p><b>TCP Client Socket Port Get</b></p> <p style="text-align: right;">nx_tcp_client_socket_port_get</p> <p><b>Icon G</b></p> <p><b>Description</b> This event represents getting the client socket port number via nx_tcp_client_socket_port_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Port number Info Field 4: Not used</p>	<p><b>TCP Client Socket Unbind</b></p> <p style="text-align: right;">nx_tcp_client_socket_unbind</p> <p><b>Icon U</b></p> <p><b>Description</b> This event represents unbinding the port associated with the socket via nx_tcp_client_socket_unbind.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Not used Info Field 4: Not used</p>
<p><b>TCP Enable</b></p> <p style="text-align: right;">nx_tcp_enable</p> <p><b>Icon E</b></p> <p><b>Description</b> This event represents enabling TCP via nx_tcp_enable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>TCP Free Port Find</b></p> <p style="text-align: right;">nx_tcp_free_port_find</p> <p><b>Icon F</b></p> <p><b>Description</b> This event represents finding a free TCP port via nx_tcp_free_port_find.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Starting search port number Info Field 3: Free port number Info Field 4: Not used</p>

<p><b>TCP Socket Bytes Available</b></p> <p>nx_tcp_socket_bytes_available</p> <p> <b>Icon B</b></p> <p><b>Description</b> This event represents the number of bytes currently available on the specified TCP receiving socket via nx_tcp_socket_bytes_available.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to TCP socket Info Field 3: Bytes received on the socket Info Field 4: Not used</p>	<p><b>TCP Socket Create</b></p> <p>nx_tcp_socket_create</p> <p> <b>Icon N</b></p> <p><b>Description</b> This event represents creating a TCP socket via nx_tcp_socket_create.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Type of service Info Field 4: Receive window size</p>
<p><b>TCP Socket Delete</b></p> <p>nx_tcp_socket_delete</p> <p> <b>Icon D</b></p> <p><b>Description</b> This event represents deleting a socket via nx_tcp_socket_delete.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Socket state Info Field 4: Not used</p>	<p><b>TCP Socket Disconnect</b></p> <p>nx_tcp_socket_disconnect</p> <p> <b>Icon C</b></p> <p><b>Description</b> This event represents disconnecting a socket via nx_tcp_socket_disconnect.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Wait option Info Field 4: Socket state</p>
<p><b>TCP Socket Information Get</b></p> <p>nx_tcp_socket_info_get</p> <p> <b>Icon I</b></p> <p><b>Description</b> This event represents getting information about a socket via nx_tcp_socket_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Bytes sent through this socket Info Field 4: Bytes received through this socket</p>	<p><b>TCP Socket MSS Get</b></p> <p>nx_tcp_socket_mss_get</p> <p> <b>Icon G</b></p> <p><b>Description</b> This event represents getting the socket's MSS via nx_tcp_socket_mss_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Maximum Segment Size (MSS) Info Field 4: Socket state</p>

<p><b>TCP Socket MSS Peer Get</b></p> <p style="text-align: center;">nx_tcp_socket_mss_peer_get</p> <p><b>Icon</b>  <b>G</b></p> <p><b>Description</b> This event represents getting the MSS value of the socket's peer via nx_tcp_socket_mss_peer_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Peer's MSS Info Field 4: Socket state</p>	<p><b>TCP Socket MSS Set</b></p> <p style="text-align: center;">nx_tcp_socket_mss_set</p> <p><b>Icon</b>  <b>S</b></p> <p><b>Description</b> This event represents setting a socket's MSS via nx_tcp_socket_mss_set.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: MSS Info Field 4: Socket state</p>
<p><b>TCP Socket Peer Info Get</b></p> <p style="text-align: center;">nx_tcp_socket_peer_info_get</p> <p><b>Icon</b>  <b>G</b></p> <p><b>Description</b> This event represents information retrieved from the TCP socket regarding the peer (e.g. connecting host) IP address and port via nx_tcp_socket_peer_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to TCP socket Info Field 2: Peer IP address Info Field 3: Peer port number Info Field 4: Not used</p>	<p><b>TCP Socket Receive</b></p> <p style="text-align: center;">nx_tcp_socket_receive</p> <p><b>Icon</b>  <b>R</b></p> <p><b>Description</b> This event represents receiving data from a socket via nx_tcp_socket_receive.</p> <p><b>Information Fields</b> Info Field 1: Pointer to socket Info Field 2: Pointer to received packet Info Field 3: Received packet length Info Field 4: Receive sequence number</p>
<p><b>TCP Socket Receive Notify</b></p> <p style="text-align: center;">nx_tcp_socket_receive_notify</p> <p><b>Icon</b>  <b>N</b></p> <p><b>Description</b> This event represents registering a receive notify callback for a socket via nx_tcp_socket_receive_notify.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Pointer to receive notify callback Info Field 4: Not used</p>	<p><b>TCP Socket Send</b></p> <p style="text-align: center;">nx_tcp_socket_send</p> <p><b>Icon</b>  <b>S</b></p> <p><b>Description</b> This event represents sending data on a socket via nx_tcp_socket_send.</p> <p><b>Information Fields</b> Info Field 1: Pointer to socket Info Field 2: Pointer to packet Info Field 3: Length of packet Info Field 4: Transmit sequence number</p>

<p><b>TCP Socket State Wait</b></p> <p style="text-align: right;">nx_tcp_socket_state_wait</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents waiting for a socket to enter a particular state via nx_tcp_socket_state_wait.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Desired socket state Info Field 4: Previous socket state</p>	<p><b>TCP Socket Transmit Configure</b></p> <p style="text-align: right;">nx_tcp_socket_transmit_configure</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents configuring the transmit options for a socket via nx_tcp_socket_transmit_configure.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Transmit queue depth Info Field 4: Timeout value</p>
<p><b>TCP Socket Window Update Notify Set</b></p> <p style="text-align: right;">nx_tcp_window_update_notify_set event</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a TCP socket receiving notification of an increase in the remote host receive window via nx_tcp_window_update_notify_set.</p> <p><b>Information Fields</b> Info Field 1: Pointer to TCP socket Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>	<p><b>UDP Enable</b></p> <p style="text-align: right;">nx_udp_enable</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents enabling UDP via nx_udp_enable.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Not used Info Field 3: Not used Info Field 4: Not used</p>
<p><b>UDP Free Port Find</b></p> <p style="text-align: right;">nx_udp_free_port_find</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents finding a free UDP port via nx_udp_free_port_find.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Starting port to search from Info Field 3: Free port Info Field 4: Not used</p>	<p><b>UDP Information Get</b></p> <p style="text-align: right;">nx_udp_info_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting information via nx_udp_info_get.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: UDP bytes sent Info Field 3: UDP bytes received Info Field 4: Invalid packets</p>

<p><b>UDP Socket Bind</b></p> <p style="text-align: right;">nx_udp_socket_bind</p>  <p><b>Description</b> This event represents binding a UDP socket to a port via <code>nx_udp_socket_bind</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Port number Info Field 4: Wait option</p>	<p><b>UDP Socket Bytes Available</b></p> <p style="text-align: right;">nx_udp_socket_bytes_available</p>  <p><b>Description</b> This event represents the current number of bytes received on the UDP socket via <code>nx_udp_socket_bytes_available</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Bytes received on socket Info Field 4: Not used</p>
<p><b>UDP Socket Checksum Disable</b></p> <p style="text-align: right;">nx_udp_socket_checksum_disable</p>  <p><b>Description</b> This event represents disabling the checksum for data on a UDP socket via <code>nx_udp_socket_checksum_disable</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Not used Info Field 4: Not used</p>	<p><b>UDP Socket Checksum Enable</b></p> <p style="text-align: right;">nx_udp_socket_checksum_enable</p>  <p><b>Description</b> This event represents enabling checksum processing on a socket via <code>nx_udp_socket_checksum_enable</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Not used Info Field 4: Not used</p>
<p><b>UDP Socket Create</b></p> <p style="text-align: right;">nx_udp_socket_create</p>  <p><b>Description</b> This event represents creating a UDP socket via <code>nx_udp_socket_create</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Type of service Info Field 4: Maximum receive queue</p>	<p><b>UDP Socket Delete Event</b></p> <p style="text-align: right;">nx_udp_socket_delete event</p>  <p><b>Description</b> This event represents deleting a UDP socket via <code>nx_udp_socket_delete</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Not used Info Field 4: Not used</p>

<p><b>UDP Socket Information Get Event</b></p> <p style="text-align: center;">nx_udp_socket_info_get event</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting information about a UDP socket via <code>nx_udp_socket_info_get</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Bytes sent through socket Info Field 4: Bytes received through socket</p>	<p><b>UDP Socket Interface Set</b></p> <p style="text-align: center;">nx_udp_socket_interface_set event</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents setting the outgoing interface of the specified UDP socket with the specified interface via <code>nx_udp_socket_interface_set</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to UDP socket Info Field 2: Index corresponding to the interface for the socket Info Field 3: Not used Info Field 4: Not used</p>
<p><b>UDP Socket Port Get</b></p> <p style="text-align: center;">nx_udp_socket_port_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents retrieving the UDP port the specified UDP socket is bound to via <code>nx_udp_socket_port_get</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to UDP socket Info Field 3: Port number Info Field 4: Not used</p>	<p><b>UDP Socket Receive</b></p> <p style="text-align: center;">nx_udp_socket_receive</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents receiving data on the specified UDP socket via <code>nx_udp_socket_receive</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to UDP socket Info Field 3: Pointer to received packet Info Field 4: Received packet size</p>
<p><b>UDP Socket Receive Notify</b></p> <p style="text-align: center;">nx_udp_socket_receive_notify</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents registering a receive notify callback via <code>nx_udp_socket_receive_notify</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance Info Field 2: Pointer to socket Info Field 3: Pointer to receive notify function Info Field 4: Not used</p>	<p><b>UDP Socket Send</b></p> <p style="text-align: center;">nx_udp_socket_send</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents sending data through a UDP socket via <code>nx_udp_socket_send</code>.</p> <p><b>Information Fields</b> Info Field 1: Pointer to socket Info Field 2: Pointer to packet Info Field 3: Packet length Info Field 4: Destination IP address</p>

UDP Socket Unbind	UDP Source Extract
<p style="text-align: right;">nx_udp_socket_unbind</p> <p><b>Icon</b>  </p>	<p style="text-align: right;">nx_udp_source_extract</p> <p><b>Icon</b>  </p>
<p><b>Description</b> This event represents unbinding a UDP port with a socket via nx_udp_socket_unbind.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"><li>Info Field 1: Pointer to IP instance</li><li>Info Field 2: Pointer to socket</li><li>Info Field 3: Port number</li><li>Info Field 4: Not used</li></ul>	<p><b>Description</b> This event represents getting the IP address and port number of a received UDP packet via nx_udp_source_extract.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"><li>Info Field 1: Pointer to packet</li><li>Info Field 2: Sender's IP address</li><li>Info Field 3: Sender's port number</li><li>Info Field 4: Not used</li></ul>

# 9

## ***USBX Trace Events***

This chapter contains a description of the USBX events displayed by TraceX.

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## List of Events and Icons

The following is a list of USBX events displayed by TraceX.

Icon	Meaning
C A	<b>Device Class Cdc Activate</b> ( <i>ux_device_class_cdc_activate</i> )
C D	<b>Device Class Cdc Deactivate</b> ( <i>ux_device_class_cdc_deactivate</i> )
C R	<b>Device Class Cdc Read</b> ( <i>ux_device_class_cdc_read</i> )
C W	<b>Device Class Cdc Write</b> ( <i>ux_device_class_cdc_write</i> )
D A	<b>Device Class Dpump Activate</b> ( <i>ux_device_class_dpump_activate</i> )
D D	<b>Device Class Dpump Deactivate</b> ( <i>ux_device_class_dpump_deactivate</i> )
D R	<b>Device Class Dpump Read</b> ( <i>ux_device_class_dpump_read</i> )
D W	<b>Device Class Dpump Write</b> ( <i>ux_device_class_dpump_write</i> )
H A	<b>Device Class Hid Activate</b> ( <i>ux_device_class_hid_activate</i> )
H D	<b>Device Class Hid Deactivate</b> ( <i>ux_device_class_hid_deactivate</i> )
D S	<b>Device Class Hid Descriptor Send</b> ( <i>ux_device_class_hid_descriptor_send</i> )
E G	<b>Device Class Hid Event Get</b> ( <i>ux_device_class_hid_event_get</i> )
E S	<b>Device Class Hid Event Set</b> ( <i>ux_device_class_hid_event_set</i> )
R G	<b>Device Class Hid Report Get</b> ( <i>ux_device_class_hid_report_get</i> )
R S	<b>Device Class Hid Report Set</b> ( <i>ux_device_class_hid_report_set</i> )
P A	<b>Device Class Pima Activate</b> ( <i>ux_device_class_pima_activate</i> )
P D	<b>Device Class Pima Deactivate</b> ( <i>ux_device_class_pima_deactivate</i> )
I S	<b>Device Class Pima Device Info Send</b> ( <i>ux_device_class_pima_device_info_send</i> )
E G	<b>Device Class Pima Event Get</b> ( <i>ux_device_class_pima_event_get</i> )

- E** Device Class Pima Event Set (*ux\_device\_class\_pima\_event\_set*)
- S** Device Class Pima Object Add (*ux\_device\_class\_pima\_object\_add*)
- O** Device Class Pima Object Data Get (*ux\_device\_class\_pima\_object\_data\_get*)
- G** Device Class Pima Object Data Send (*ux\_device\_class\_pima\_object\_data\_send*)
- D** Device Class Pima Object Delete (*ux\_device\_class\_pima\_object\_delete*)
- H** Device Class Pima Object Handles Send  
(*ux\_device\_class\_pima\_object\_handles\_send*)
- C** Device Class Pima Object Info Get (*ux\_device\_class\_pima\_object\_info\_get*)
- I** Device Class Pima Object Info Send (*ux\_device\_class\_pima\_object\_info\_send*)
- N** Device Class Pima Objects Number Send  
(*ux\_device\_class\_pima\_objects\_number\_send*)
- S** Device Class Pima Partial Object Data Get  
(*ux\_device\_class\_pima\_partial\_object\_data\_get*)
- R** Device Class Pima Response Send (*ux\_device\_class\_pima\_response\_send*)
- S** Device Class Pima Storage Id Send (*ux\_device\_class\_pima\_storage\_id\_send*)
- S** Device Class Pima Storage Info Send  
(*ux\_device\_class\_pima\_storage\_info\_send*)
- R** Device Class Rndis Activate (*ux\_device\_class\_rndis\_activate*)
- R** Device Class Rndis Deactivate (*ux\_device\_class\_rndis\_deactivate*)
- M** Device Class Rndis Message Keep Alive  
(*ux\_device\_class\_rndis\_msg\_keep\_alive*)
- M** Device Class Rndis Message Query (*ux\_device\_class\_rndis\_msg\_query*)
- M** Device Class Rndis Message Reset (*ux\_device\_class\_rndis\_msg\_reset*)
- M** Device Class Rndis Message Set (*ux\_device\_class\_rndis\_msg\_set*)
- P** Device Class Rndis Packet Receive (*ux\_device\_class\_rndis\_packet\_receive*)
- P** Device Class Rndis Packet Transmit (*ux\_device\_class\_rndis\_packet\_transmit*)
- S** Device Class Storage Activate (*ux\_device\_class\_storage\_activate*)

-  **Device Class Storage Deactivate** (*ux\_device\_class\_storage\_deactivate*)
-  **Device Class Storage Format** (*ux\_device\_class\_storage\_format*)
-  **Device Class Storage Inquiry** (*ux\_device\_class\_storage\_inquiry*)
-  **Device Class Storage Mode Select** (*ux\_device\_class\_storage\_mode\_select*)
-  **Device Class Storage Mode Sense** (*ux\_device\_class\_storage\_mode\_sense*)
-  **Device Class Storage Prevent Allow Media Removal**  
(*ux\_device\_class\_storage\_prevent\_allow\_media\_removal*)
-  **Device Class Storage Read** (*ux\_device\_class\_storage\_read*)
-  **Device Class Storage Read Capacity** (*ux\_device\_class\_storage\_read\_capacity*)
-  **Device Class Storage Read Format Capacity**  
(*ux\_device\_class\_storage\_read\_format\_capacity*)
-  **Device Class Storage Read TOC** (*ux\_device\_class\_storage\_read\_toc*)
-  **Device Class Storage Request Sense** (*ux\_device\_class\_storage\_request\_sense*)
-  **Device Class Storage Start Stop** (*ux\_device\_class\_storage\_start\_stop*)
-  **Device Class Storage Test Ready** (*ux\_device\_class\_storage\_test\_ready*)
-  **Device Class Storage Verify** (*ux\_device\_class\_storage\_verify*)
-  **Device Class Storage Write** (*ux\_device\_class\_storage\_write*)
-  **Device Stack Alternate Setting Get** (*ux\_device\_stack\_alternate\_setting\_get*)
-  **Device Stack Alternate Setting Set** (*ux\_device\_stack\_alternate\_setting\_set*)
-  **Device Stack Class Register** (*ux\_device\_stack\_class\_register*)
-  **Device Stack Clear Feature** (*ux\_device\_stack\_clear\_feature*)
-  **Device Stack Configuration Get** (*ux\_device\_stack\_configuration\_get*)
-  **Device Stack Configuration Set** (*ux\_device\_stack\_configuration\_set*)
-  **Device Stack Connect** (*ux\_device\_stack\_connect*)

<b>D</b>	<b>Device Stack Descriptor Send</b> ( <i>ux_device_stack_descriptor_send</i> )
<b>S</b>	<b>Device Stack Disconnect</b> ( <i>ux_device_stack_disconnect</i> )
<b>E</b>	<b>Device Stack Endpoint Stall</b> ( <i>ux_device_stack_endpoint_stall</i> )
<b>G</b>	<b>Device Stack Get Status</b> ( <i>ux_device_stack_get_status</i> )
<b>H</b>	<b>Device Stack Host Wakeup</b> ( <i>ux_device_stack_host_wakeup</i> )
<b>S</b>	<b>Device Stack Initialize</b> ( <i>ux_device_stack_initialize</i> )
<b>I</b>	<b>Device Stack Interface Delete</b> ( <i>ux_device_stack_interface_delete</i> )
<b>G</b>	<b>Device Stack Interface Get</b> ( <i>ux_device_stack_interface_get</i> )
<b>I</b>	<b>Device Stack Interface Set</b> ( <i>ux_device_stack_interface_set</i> )
<b>F</b>	<b>Device Stack Set Feature</b> ( <i>ux_device_stack_set_feature</i> )
<b>T</b>	<b>Device Stack Transfer Abort</b> ( <i>ux_device_stack_transfer_abort</i> )
<b>A</b>	<b>Device Stack Transfer All Request Abort</b> ( <i>ux_device_stack_transfer_all_request_abort</i> )
<b>T</b>	<b>Device Stack Transfer Request</b> ( <i>ux_device_stack_transfer_request</i> )
<b>X</b>	<b>Host Class Asix Activate</b> ( <i>ux_host_class_asix_activate</i> )
<b>D</b>	<b>Host Class Asix Deactivate</b> ( <i>ux_host_class_asix_deactivate</i> )
<b>I</b>	<b>Host Class Asix Interrupt Notification</b> ( <i>ux_host_class_asix_interrupt_notification</i> )
<b>R</b>	<b>Host Class Asix Read</b> ( <i>ux_host_class_asix_read</i> )
<b>W</b>	<b>Host Class Asix Write</b> ( <i>ux_host_class_asix_write</i> )
<b>A</b>	<b>Host Class Audio Activate</b> ( <i>ux_host_class_audio_activate</i> )
<b>V</b>	<b>Host Class Audio Control Value Get</b> ( <i>ux_host_class_audio_control_value_get</i> )
<b>S</b>	<b>Host Class Audio Control Value Set</b> ( <i>ux_host_class_audio_control_value_set</i> )
<b>D</b>	<b>Host Class Audio Deactivate</b> ( <i>ux_host_class_audio_deactivate</i> )

- R** Host Class Audio Read (*ux\_host\_class\_audio\_read*)
- S** Host Class Audio Streaming Sampling Get  
    (*ux\_host\_class\_audio\_streaming\_sampling\_get*)
- S** Host Class Audio Streaming Sampling Set  
    (*ux\_host\_class\_audio\_streaming\_sampling\_set*)
- A** Host Class Audio Write (*ux\_host\_class\_audio\_write*)
- C** Host Class Cdc Acm Activate (*ux\_host\_class\_cdc\_acm\_activate*)
- D** Host Class Cdc Acm Deactivate (*ux\_host\_class\_cdc\_acm\_deactivate*)
- P** Host Class Cdc Acm ioctl Abort In Pipe  
    (*ux\_host\_class\_cdc\_acm\_ioctl\_abort\_in\_pipe*)
- I** Host Class Cdc Acm ioctl Abort Out Pipe  
    (*ux\_host\_class\_cdc\_acm\_ioctl\_abort\_out\_pipe*)
- P** Host Class Cdc Acm ioctl Get Device Status  
    (*ux\_host\_class\_cdc\_acm\_ioctl\_get\_device\_status*)
- L** Host Class Cdc Acm ioctl Get Line Coding  
    (*ux\_host\_class\_cdc\_acm\_ioctl\_get\_line\_coding*)
- N** Host Class Cdc Acm ioctl Notification Callback  
    (*ux\_host\_class\_cdc\_acm\_ioctl\_notification\_callback*)
- B** Host Class Cdc Acm ioctl Send Break  
    (*ux\_host\_class\_cdc\_acm\_ioctl\_send\_break*)
- S** Host Class Cdc Acm ioctl Set Line Coding  
    (*ux\_host\_class\_cdc\_acm\_ioctl\_set\_line\_coding*)
- S** Host Class Cdc Acm ioctl Set Line State  
    (*ux\_host\_class\_cdc\_acm\_ioctl\_set\_line\_state*)
- R** Host Class Cdc Acm Read (*ux\_host\_class\_cdc\_acm\_read*)
- S** Host Class Cdc Acm Reception Start (*ux\_host\_class\_cdc\_acm\_reception\_start*)
- X** Host Class Cdc Acm Reception Stop (*ux\_host\_class\_cdc\_acm\_reception\_stop*)
- W** Host Class Cdc Acm Write (*ux\_host\_class\_cdc\_acm\_write*)
- A** Host Class Dpump Activate (*ux\_host\_class\_dpump\_activate*)
- D** Host Class Dpump Deactivate (*ux\_host\_class\_dpump\_deactivate*)
- R** Host Class Dpump Read (*ux\_host\_class\_dpump\_read*)

- D** Host Class Ddump Write (*ux\_host\_class\_dpump\_write*)
- W**
- H** Host Class Hid Activate (*ux\_host\_class\_hid\_activate*)
- A**
- C** Host Class Hid Client Register (*ux\_host\_class\_hid\_client\_register*)
- E**
- D** Host Class Hid Deactivate (*ux\_host\_class\_hid\_deactivate*)
- I**
- I** Host Class Hid Idle Get (*ux\_host\_class\_hid\_idle\_get*)
- S**
- S** Host Class Hid Idle Set (*ux\_host\_class\_hid\_idle\_set*)
- K**
- A** Host Class Hid Keyboard Activate (*ux\_host\_class\_hid\_keyboard\_activate*)
- D**
- D** Host Class Hid Keyboard Deactivate (*ux\_host\_class\_hid\_keyboard\_deactivate*)
- M**
- A** Host Class Hid Mouse Activate (*ux\_host\_class\_hid\_mouse\_activate*)
- D**
- D** Host Class Hid Mouse Deactivate (*ux\_host\_class\_hid\_mouse\_deactivate*)
- G**
- A** Host Class Hid Remote Control Activate  
    (*ux\_host\_class\_hid\_remote\_control\_activate*)
- C**
- D** Host Class Hid Remote Control Deactivate  
    (*ux\_host\_class\_hid\_remote\_control\_deactivate*)
- R**
- G** Host Class Hid Report Get (*ux\_host\_class\_hid\_report\_get*)
- S**
- S** Host Class Hid Report Set (*ux\_host\_class\_hid\_report\_set*)
- H**
- A** Host Class Hub Activate (*ux\_host\_class\_hub\_activate*)
- C**
- D** Host Class Hub Change Detect (*ux\_host\_class\_hub\_change\_detect*)
- H**
- D** Host Class Hub Deactivate (*ux\_host\_class\_hub\_deactivate*)
- P**
- P** Host Class Hub Port Change Connection Process  
    (*ux\_host\_class\_hub\_port\_change\_connection\_process*)
- E**
- P** Host Class Hub Port Change Enable Process  
    (*ux\_host\_class\_hub\_port\_change\_enable\_process*)
- F**
- C** Host Class Hub Port Change Over Current Process  
    (*ux\_host\_class\_hub\_port\_change\_over\_current\_process*)
- R**
- P** Host Class Hub Port Change Reset Process  
    (*ux\_host\_class\_hub\_port\_change\_reset\_process*)

- S P** Host Class Hub Port Change Suspend Process (*ux\_host\_class\_hub\_port\_change\_suspend\_process*)
- P A** Host Class Pima Activate (*ux\_host\_class\_pima\_activate*)
- P D** Host Class Pima Deactivate (*ux\_host\_class\_pima\_deactivate*)
- I G** Host Class Pima Device Info Get (*ux\_host\_class\_pima\_device\_info\_get*)
- D R** Host Class Pima Device Reset (*ux\_host\_class\_pima\_device\_reset*)
- P N** Host Class Pima Notification (*ux\_host\_class\_pima\_notification*)
- H G** Host Class Pima Number Objects Get (*ux\_host\_class\_pima\_num\_objects\_get*)
- G C** Host Class Pima Object Close (*ux\_host\_class\_pima\_object\_close*)
- C O** Host Class Pima Object Copy (*ux\_host\_class\_pima\_object\_copy*)
- O D** Host Class Pima Object Delete (*ux\_host\_class\_pima\_object\_delete*)
- O G** Host Class Pima Object Get (*ux\_host\_class\_pima\_object\_get*)
- I G** Host Class Pima Object Info Get (*ux\_host\_class\_pima\_object\_info\_get*)
- I S** Host Class Pima Object Info Send (*ux\_host\_class\_pima\_object\_info\_send*)
- O M** Host Class Pima Object Move (*ux\_host\_class\_pima\_object\_move*)
- O S** Host Class Pima Object Send (*ux\_host\_class\_pima\_object\_send*)
- T A** Host Class Pima Object Transfer Abort (*ux\_host\_class\_object\_transfer\_abort*)
- P R** Host Class Pima Read (*ux\_host\_class\_pima\_read*)
- R C** Host Class Pima Request Cancel (*ux\_host\_class\_pima\_request\_cancel*)
- S C** Host Class Pima Session Close (*ux\_host\_class\_pima\_session\_close*)
- S O** Host Class Pima Session Open (*ux\_host\_class\_pima\_session\_open*)
- S G** Host Class Pima Storage Ids Get (*ux\_host\_class\_pima\_storage\_ids\_get*)
- G S** Host Class Pima Storage Info Get (*ux\_host\_class\_pima\_storage\_info\_get*)

- T** Host Class Pima Thumb Get (*ux\_host\_class\_pima\_thumb\_get*)
- G**
- P** Host Class Pima Write (*ux\_host\_class\_pima\_write*)
- W**
- P** Host Class Printer Activate (*ux\_host\_class\_printer\_activate*)
- A**
- P** Host Class Printer Deactivate (*ux\_host\_class\_printer\_deactivate*)
- D**
- H** Host Class Printer Name Get (*ux\_host\_class\_printer\_name\_get*)
- G**
- P** Host Class Printer Read (*ux\_host\_class\_printer\_read*)
- R**
- S** Host Class Printer Soft Reset (*ux\_host\_class\_printer\_soft\_reset*)
- R**
- S** Host Class Printer Status Get (*ux\_host\_class\_printer\_status\_get*)
- G**
- P** Host Class Printer Write (*ux\_host\_class\_printer\_write*)
- W**
- P** Host Class Prolific Activate (*ux\_host\_class\_prolific\_activate*)
- A**
- P** Host Class Prolific Deactivate (*ux\_host\_class\_prolific\_deactivate*)
- D**
- P** Host Class Prolific ioctl Abort In Pipe (*ux\_host\_class\_prolific\_ioctl\_abort\_in\_pipe*)
- M**
- P** Host Class Prolific ioctl Abort Out Pipe  
    (*ux\_host\_class\_prolific\_ioctl\_abort\_out\_pipe*)
- O**
- D** Host Class Prolific ioctl Get Device Status  
    (*ux\_host\_class\_prolific\_ioctl\_get\_device\_status*)
- S**
- G** Host Class Prolific ioctl Get Line Coding  
    (*ux\_host\_class\_prolific\_ioctl\_get\_line\_coding*)
- L**
- P** Host Class Prolific ioctl Purge (*ux\_host\_class\_prolific\_ioctl\_purge*)
- P**
- S** Host Class Prolific ioctl Report Device Status Change  
    (*ux\_host\_class\_prolific\_ioctl\_report\_device\_status\_change*)
- C**
- S** Host Class Prolific ioctl Send Break (*ux\_host\_class\_prolific\_ioctl\_send\_break*)
- B**
- S**
- L** Host Class Prolific ioctl Set Line Coding  
    (*ux\_host\_class\_prolific\_ioctl\_set\_line\_coding*)
- I**
- S** Host Class Prolific ioctl Set Line State  
    (*ux\_host\_class\_prolific\_ioctl\_set\_line\_state*)
- E**
- R** Host Class Prolific Read (*ux\_host\_class\_prolific\_read*)
- R**
- I**
- S** Host Class Prolific Reception Start (*ux\_host\_class\_prolific\_reception\_start*)

- S R** Host Class Prolific Reception Stop (*ux\_host\_class\_prolific\_reception\_stop*)
- P W** Host Class Prolific Write (*ux\_host\_class\_prolific\_write*)
- S A** Host Class Storage Activate (*ux\_host\_class\_storage\_activate*)
- S D** Host Class Storage Deactivate (*ux\_host\_class\_storage\_deactivate*)
- C G** Host Class Storage Media Capacity Get  
(*ux\_host\_class\_storage\_media\_capacity\_get*)
- F G** Host Class Storage Media Format Capacity Get  
(*ux\_host\_class\_storage\_media\_format\_capacity\_get*)
- M M** Host Class Storage Media Mount (*ux\_host\_class\_storage\_media\_mount*)
- M O** Host Class Storage Media Open (*ux\_host\_class\_storage\_media\_open*)
- M R** Host Class Storage Media Read (*ux\_host\_class\_storage\_media\_read*)
- M W** Host Class Storage Media Write (*ux\_host\_class\_storage\_media\_write*)
- R S** Host Class Storage Request Sense (*ux\_host\_class\_storage\_request\_sense*)
- S S** Host Class Storage Start Stop (*ux\_host\_class\_storage\_start\_stop*)
- U T** Host Class Storage Unit Ready Test (*ux\_host\_class\_storage\_activate*)
- I C** Host Stack Class Instance Create (*ux\_host\_stack\_class\_instance\_create*)
- I D** Host Stack Class Instance Destroy (*ux\_host\_stack\_class\_instance\_destroy*)
- C D** Host Stack Configuration Delete (*ux\_host\_stack\_configuration\_delete*)
- C E** Host Stack Configuration Enumerate (*ux\_host\_stack\_configuration\_enumerate*)
- C I** Host Stack Configuration Instance Create  
(*ux\_host\_stack\_configuration\_instance\_create*)
- C I** Host Stack Configuration Instance Delete  
(*ux\_host\_stack\_configuration\_instance\_delete*)
- C S** Host Stack Configuration Set (*ux\_host\_stack\_configuration\_set*)
- D S** Host Stack Device Address Set (*ux\_host\_stack\_device\_set*)
- D G** Host Stack Device Configuration Get (*ux\_host\_stack\_device\_configuration\_get*)
- D S** Host Stack Device Configuration Select  
(*ux\_host\_stack\_device\_configuration\_select*)

- D** **R** Host Stack Device Descriptor Read (*ux\_host\_stack\_device\_descriptor\_read*)
- G** **D** Host Stack Device Get (*ux\_host\_stack\_device\_get*)
- A** **E** Host Stack Device Remove (*ux\_host\_stack\_device\_get*)
- K** **C** Host Stack Device Resource Free (*ux\_host\_stack\_device\_resource\_free*)
- E** **C** Host Stack Endpoint Instance Create (*ux\_host\_stack\_endpoint\_instance\_create*)
- E** **D** Host Stack Endpoint Instance Delete (*ux\_host\_stack\_endpoint\_instance\_delete*)
- E** **R** Host Stack Endpoint Reset (*ux\_host\_stack\_endpoint\_reset*)
- E** **A** Host Stack Endpoint Transfer Abort (*ux\_host\_stack\_endpoint\_transfer\_abort*)
- H** **R** Host Stack Host Controller Register (*ux\_host\_stack\_hcd\_register*)
- H** **I** Host Stack Initialize (*ux\_host\_stack\_initialize*)
- I** **G** Host Stack Interface Endpoint Get (*ux\_host\_stack\_interface\_endpoint\_get*)
- H** **I** Host Stack Interface Instance Create (*ux\_host\_stack\_interface\_instance\_create*)
- D** **I** Host Stack Interface Instance Delete (*ux\_host\_stack\_interface\_instance\_delete*)
- S** **T** Host Stack Interface Set (*ux\_host\_stack\_interface\_set*)
- S** **S** Host Stack Interface Setting Select (*ux\_host\_stack\_interface\_setting\_select*)
- H** **C** Host Stack New Configuration Create (*ux\_host\_stack\_new\_configuration\_create*)
- H** **D** Host Stack New Device Create (*ux\_host\_stack\_new\_device\_create*)
- H** **E** Host Stack New Endpoint Create (*ux\_host\_stack\_new\_endpoint\_create*)
- R** **C** Host Stack Root Hub Change Process (*ux\_host\_stack\_rh\_change\_process*)
- H** **E** Host Stack Root Hub Device Extraction (*ux\_host\_stack\_rh\_device\_extraction*)
- H** **I** Host Stack Root Hub Device Insertion (*ux\_host\_stack\_rh\_device\_insertion*)
- H** **X** Host Stack Transfer Request (*ux\_host\_stack\_transfer\_request*)



**Host Stack Transfer Request Abort** (*ux\_host\_stack\_transfer\_request\_abort*)



**USBX Error** (*ux\_error*)

## Event Descriptions

The following pages describe the USBX Trace Events.

<p><b>Device Class Cdc Activate</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Cdc Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Cdc Deactivate</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Cdc Deactivate.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Cdc Read</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Cdc Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>	<p><b>Device Class Cdc Write</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Cdc Write Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>
<p><b>Device Class Ddump Activate</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Ddump Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Ddump Deactivate</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Ddump Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Device Class Dpump Read</b></p> <p style="text-align: right;">ux_device_class_dpump_read</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Dpump Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Buffer. Info Field 3: Requested length. Info Field 4: Not used.</p>	<p><b>Device Class Dpump Write</b></p> <p style="text-align: right;">ux_device_class_dpump_write</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Dpump Write Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>
<p><b>Device Class Hid Activate</b></p> <p style="text-align: right;">ux_device_class_hid_activate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Hid Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Hid Deactivate</b></p> <p style="text-align: right;">ux_device_class_hid_deactivate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Hid Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Hid Descriptor Send</b></p> <p style="text-align: right;">ux_device_class_hid_descriptor_send</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Hid Descriptor Send Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Descriptor type. Info Field 3: Request index. Info Field 4: Not used.</p>	<p><b>Device Class Hid Event Get</b></p> <p style="text-align: right;">ux_device_class_hid_event_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Hid Event Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Device Class Hid Event Set</b></p> <p style="text-align: right;">ux_device_class_hid_event_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Hid Event Set.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Hid Report Get</b></p> <p style="text-align: right;">ux_device_class_hid_report_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Hid Report Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Descriptor type. Info Field 3: Request index. Info Field 4: Not used.</p>
<p><b>Device Class Hid Report Set</b></p> <p style="text-align: right;">ux_device_class_hid_report_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Hid Report Set Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Descriptor type. Info Field 3: Request index. Info Field 4: Not used.</p>	<p><b>Device Class Pima Activate</b></p> <p style="text-align: right;">ux_device_class_pima_activate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Pima Deactivate</b></p> <p style="text-align: right;">ux_device_class_pima_deactivate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Pima Device Info Send</b></p> <p style="text-align: right;">ux_device_class_pima_device_info_send</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Device Info Send Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Device Class Pima Event Get</b></p> <p style="text-align: center;">ux_device_class_pima_event_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Event Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Pima event. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Pima Event Set</b></p> <p style="text-align: center;">ux_device_class_pima_event_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Event Set Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Pima event. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Pima Object Add</b></p> <p style="text-align: center;">ux_device_class_pima_object_add</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Object Add Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Object handle. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Pima Object Data Get</b></p> <p style="text-align: center;">ux_device_class_pima_object_data_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Object Data Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Object handle. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Pima Object Data Send</b></p> <p style="text-align: center;">ux_device_class_pima_object_data_send</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Object Data Send Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Object handle. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Pima Object Delete</b></p> <p style="text-align: center;">ux_device_class_pima_object_delete</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Object Delete Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Object handle. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Device Class Pima Object Handles Send</b></p> <p>Icon  ux_device_class_pima_object_handles_send</p> <p><b>Description</b> This event represents a USBX Device Class Pima Object Handles Send Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Storage ID. Info Field 3: Object format code. Info Field 4: Object association.</p>	<p><b>Device Class Pima Object Info Get</b></p> <p>Icon  ux_device_class_pima_object_info_get</p> <p><b>Description</b> This event represents a USBX Device Class Pima Object Info Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Object handle. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Pima Object Info Send</b></p> <p>Icon  ux_device_class_pima_object_info_send</p> <p><b>Description</b> This event represents a USBX Device Class Pima Object Info Send Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Pima Objects NumberSend</b></p> <p>Icon  ux_device_class_pima_objects_number_send</p> <p><b>Description</b> This event represents a USBX Device Class Pima Object Number Send event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Storage ID. Info Field 3: Object format code. Info Field 4: Object associate.</p>
<p><b>Device Class Pima Partial Object Data Get</b></p> <p>Icon  ux_device_class_pima_partial_object_data_get</p> <p><b>Description</b> This event represents a USBX Device Class Pima Partial Object Data Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Object handle. Info Field 3: Offset requested. Info Field 4: Length requested.</p>	<p><b>Device Class Pima Response Send</b></p> <p>Icon  ux_device_class_pima_response_send</p> <p><b>Description</b> This event represents a USBX Device Class Pima Response Send Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Response code. Info Field 3: Number parameter. Info Field 4: Pima parameter 1.</p>

<p><b>Device Class Pima Storage Id Send</b></p> <p>ux_device_class_pima_storage_id_send</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Storage Id Send Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Pima Storage Info Send</b></p> <p>ux_device_class_pima_storage_info_send</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Pima Storage Info Send Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Rndis Activate</b></p> <p>ux_device_class_rndis_activate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Rndis Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Rndis Deactivate</b></p> <p>ux_device_class_rndis_deactivate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Rndis Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Rndis Message Keep Alive</b></p> <p>ux_device_class_rndis_msg_keep_alive</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Rndis Message Keep Alive Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Rndis Message Query</b></p> <p>ux_device_class_rndis_msg_query</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Rndis Message Query Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Rndis OID. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Device Class Rndis Message Reset</b></p>  <p>ux_device_class_rndis_msg_reset</p> <p><b>Description</b> This event represents a USBX Device Class Rndis Message Reset Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Class Instance.</li> <li>Info Field 2: Not used.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>	<p><b>Device Class Rndis Message Set</b></p>  <p>ux_device_class_rndis_msg_set</p> <p><b>Description</b> This event represents a USBX Device Class Rndis Message Set Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Class Instance.</li> <li>Info Field 2: Rndis OID.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>
<p><b>Device Class Rndis Packet Receive</b></p>  <p>ux_device_class_rndis_packet_receive</p> <p><b>Description</b> This event represents a USBX Device Class Rndis Packet Receive Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Class Instance.</li> <li>Info Field 2: Not used.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>	<p><b>Device Class Rndis Packet Transmit</b></p>  <p>ux_device_class_rndis_packet_transmit</p> <p><b>Description</b> This event represents a USBX Device Class Rndis Packet Transmit Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Class Instance.</li> <li>Info Field 2: Not used.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>
<p><b>Device Class Storage Activate</b></p>  <p>ux_device_class_storage_activate</p> <p><b>Description</b> This event represents a USBX Device Class Storage Activate Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Class Instance.</li> <li>Info Field 2: Not used.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>	<p><b>Device Class Storage Deactivate</b></p>  <p>ux_device_class_storage_deactivate</p> <p><b>Description</b> This event represents a USBX Device Class Storage Deactivate Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Class Instance.</li> <li>Info Field 2: Not used.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>

<p><b>Device Class Storage Format</b></p> <hr/> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Storage Format Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Storage Inquiry</b></p> <hr/> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Storage Inquiry Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Storage Mode Select</b></p> <hr/> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Storage Mode Select Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Storage Mode Sense</b></p> <hr/> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Storage Mode Sense Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Storage Prevent Allow Media Removal</b></p> <hr/> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Storage Prevent Allow Media Removal Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Storage Read</b></p> <hr/> <p>Icon </p> <p><b>Description</b> This event represents a USBX Device Class Storage Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Sector. Info Field 4: Number sectors.</p>

<p><b>Device Class Storage Read Capacity</b></p> <p>ux_device_class_storage_read_capacity</p>  <p><b>Description</b> This event represents a USBX Device Class Storage Read Capacity Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Storage Read Format Capacity</b></p> <p>ux_device_class_storage_read_format_capacity</p>  <p><b>Description</b> This event represents a USBX Device Class Storage Read Format Capacity Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Class Storage Read TOC</b></p> <p>ux_device_class_storage_read_toc</p>  <p><b>Description</b> This event represents a USBX Device Class Storage Read TOC Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Storage Request Sense</b></p> <p>ux_device_class_storage_request_sense</p>  <p><b>Description</b> This event represents a USBX Device Class Storage Request Sense Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Sense key. Info Field 4: Code.</p>
<p><b>Device Class Storage Start Stop</b></p> <p>ux_device_class_storage_start_stop</p>  <p><b>Description</b> This event represents a USBX Device Class Storage Start Stop Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Storage Test Ready</b></p> <p>ux_device_class_storage_test_ready</p>  <p><b>Description</b> This event represents a USBX Device Class Storage Test Ready Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Device Class Storage Verify</b></p> <p style="text-align: right;">ux_device_class_storage_verify</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Storage Verify Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Class Storage Write</b></p> <p style="text-align: right;">ux_device_class_storage_write</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Class Storage Write Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Lun. Info Field 3: Sector. Info Field 4: Number sectors.</p>
<p><b>Device Stack Alternate Setting Get</b></p> <p style="text-align: right;">ux_device_stack_events_base</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Alternate Setting Get Event.</p> <p><b>Information Fields</b> Info Field 1: Interface value. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Stack Alternate Setting Set</b></p> <p style="text-align: right;">ux_device_stack_alternate_setting_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Alternate Setting Set Event.</p> <p><b>Information Fields</b> Info Field 1: Interface value. Info Field 2: Alternate setting value. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Stack Class Register</b></p> <p style="text-align: right;">ux_device_stack_class_register</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Class Register Event.</p> <p><b>Information Fields</b> Info Field 1: Class name. Info Field 2: Interface number. Info Field 3: Parameter. Info Field 4: Not used.</p>	<p><b>Device Stack Clear Feature</b></p> <p style="text-align: right;">ux_device_stack_clear_feature</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Clear Feature Event.</p> <p><b>Information Fields</b> Info Field 1: Request type. Info Field 2: Request value. Info Field 3: Request index. Info Field 4: Not used.</p>

<p><b>Device Stack Configuration Get</b></p> <p>Icon </p> <p><code>ux_device_stack_configuration_get t</code></p> <p><b>Description</b> This event represents a USBX Device Stack Configuration Get Event.</p> <p><b>Information Fields</b> Info Field 1: Configuration value. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Stack Configuration Set</b></p> <p>Icon </p> <p><code>ux_device_stack_configuration_set</code></p> <p><b>Description</b> This event represents a USBX Device Stack Configuration Set Event.</p> <p><b>Information Fields</b> Info Field 1: Configuration value. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Stack Connect</b></p> <p>Icon </p> <p><code>ux_device_stack_connect</code></p> <p><b>Description</b> This event represents a USBX Device Stack Descriptor Send Event.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Stack Descriptor Send</b></p> <p>Icon </p> <p><code>ux_device_stack_descriptor_send</code></p> <p><b>Description</b> This event represents a USBX Device Stack Descriptor Send Event.</p> <p><b>Information Fields</b> Info Field 1: Descriptor type. Info Field 2: Request index. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Stack Disconnect</b></p> <p>Icon </p> <p><code>ux_device_stack_disconnect</code></p> <p><b>Description</b> This event represents a USBX Device Stack Disconnect Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Stack Endpoint Stall</b></p> <p>Icon </p> <p><code>ux_device_stack_endpoint_stall</code></p> <p><b>Description</b> This event represents a USBX Device Stack Endpoint Stall Event.</p> <p><b>Information Fields</b> Info Field 1: Endpoint. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Device Stack Get Status</b></p> <p style="text-align: right;">ux_device_stack_get_status</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Get Status Event.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Stack Host Wakeup</b></p> <p style="text-align: right;">ux_device_stack_host_wakeup</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Host Wakeup Event.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Stack Initialize</b></p> <p style="text-align: right;">ux_device_stack_initialize</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Initialize Event.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Stack Interface Delete</b></p> <p style="text-align: right;">ux_device_stack_interface_delete</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Interface Delete Event.</p> <p><b>Information Fields</b> Info Field 1: Interface. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Device Stack Interface Get</b></p> <p style="text-align: right;">ux_device_stack_interface_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Interface Get Event.</p> <p><b>Information Fields</b> Info Field 1: Interface value. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Device Stack Interface Set</b></p> <p style="text-align: right;">ux_device_stack_interface_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Interface Set Event.</p> <p><b>Information Fields</b> Info Field 1: Request value. Info Field 2: Request index. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Device Stack Set Feature</b></p> <p style="text-align: right;">ux_device_stack_set_feature</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Set Feature Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Request value.</li> <li>Info Field 2: Request index.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>	<p><b>Device Stack Transfer Abort</b></p> <p style="text-align: right;">ux_device_stack_transfer_abort</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Transfer Abort Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Transfer request.</li> <li>Info Field 2: Completion code.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>
<p><b>Device Stack Transfer All Request Abort</b></p> <p style="text-align: right;">ux_device_stack_transfer_all_request_abort</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Transfer All Request Abort Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Endpoint.</li> <li>Info Field 2: Completion code.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>	<p><b>Device Stack Transfer Request</b></p> <p style="text-align: right;">ux_device_stack_transfer_request</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Device Stack Transfer Request Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Transfer request.</li> <li>Info Field 2: Not used.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>
<p><b>Host Class Asix Activate</b></p> <p style="text-align: right;">ux_host_class_asix_activate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Asix Activate.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Class Instance.</li> <li>Info Field 2: Not used.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>	<p><b>Host Class Asix Deactivate</b></p> <p style="text-align: right;">ux_host_class_asix_deactivate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Asix Deactivate Event.</p> <p><b>Information Fields</b></p> <ul style="list-style-type: none"> <li>Info Field 1: Class Instance.</li> <li>Info Field 2: Not used.</li> <li>Info Field 3: Not used.</li> <li>Info Field 4: Not used.</li> </ul>

<p><b>Host Class Asix Interrupt Notification</b></p> <p>Icon  ux_host_class_asix_interrupt_notification</p> <p><b>Description</b> This event represents a USBX Host Class Asix Interrupt Notification Event.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Asix Read</b></p> <p>Icon  ux_host_class_asix_read</p> <p><b>Description</b> This event represents a USBX Host Class Asix Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>
<p><b>Host Class Asix Write</b></p> <p>Icon  ux_host_class_asix_write</p> <p><b>Description</b> This event represents a USBX Host Class Asix Write Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>	<p><b>Host Class Audio Activate</b></p> <p>Icon  ux_host_class_audio_activate</p> <p><b>Description</b> This event represents a USBX Host Class Audio Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Audio Control Value Get</b></p> <p>Icon  ux_host_class_audio_control_value_get</p> <p><b>Description</b> This event represents a USBX Host Class Audio Control Value Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Audio Control Value Set</b></p> <p>Icon  ux_host_class_audio_control_value_set</p> <p><b>Description</b> This event represents an internal NetX I/O driver deferred processing event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Audio control. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Audio Deactivate</b></p> <p style="text-align: center;">ux_host_class_audio_deactivate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Audio Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Audio Read</b></p> <p style="text-align: center;">ux_host_class_audio_read</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Audio Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>
<p><b>Host Class Audio Streaming Sampling Get</b></p> <p style="text-align: center;">ux_host_class_audio_streaming_sampling_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Audio Streaming Sampling Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Audio Streaming Sampling Set</b></p> <p style="text-align: center;">ux_host_class_audio_streaming_sampling_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Audio Streaming Sampling Set Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Audio Sampling. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Audio Write</b></p> <p style="text-align: center;">ux_host_class_audio_write</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Audio Write Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>	<p><b>Host Class Cdc Acm Activate</b></p> <p style="text-align: center;">ux_host_class_cdc_acm_activate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Cdc Acm Deactivate</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Cdc Acm ioctl Abort In Pipe</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm ioctl Abort In Pipe Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Endpoint. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Cdc Acm ioctl Abort Out Pipe</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm ioctl Abort Out Pipe Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Endpoint. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Cdc Acm ioctl Get Device Status</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm ioctl Get Device Status Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Device status. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Cdc Acm ioctl Get Line Coding</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm ioctl Get Line Coding Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Parameter. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Cdc Acm ioctl Notification Callback</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm ioctl Notification Callback Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Parameter. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Cdc Acm ioctl Send Break</b></p>  <p>ux_host_class_cdc_acm_ioctl_send_break</p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm ioctl Send Break Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Parameter. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Cdc Acm ioctl Set Line Coding</b></p>  <p>ux_host_class_cdc_acm_ioctl_set_line_coding</p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm ioctl Set Line Coding Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Parameter. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Cdc Acm ioctl Set Line State</b></p>  <p>ux_host_class_cdc_acm_ioctl_set_line_state</p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm ioctl Set Line State Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Parameter. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Cdc Acm Read</b></p>  <p>ux_host_class_cdc_acm_read</p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested Length. Info Field 4: Not used.</p>
<p><b>Host Class Cdc Acm Reception Start</b></p>  <p>ux_host_class_cdc_acm_reception_start</p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm Reception Start Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Cdc Acm Reception Stop</b></p>  <p>ux_host_class_cdc_acm_reception_stop</p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm Reception Stop Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Cdc Acm Write</b></p> <p style="text-align: right;">ux_host_class_cdc_acm_write</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Cdc Acm Write Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested Length. Info Field 4: Not used.</p>	<p><b>Host Class Dpump Activate</b></p> <p style="text-align: right;">ux_host_class_dpump_activate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Dpump Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Dpump Deactivate</b></p> <p style="text-align: right;">ux_host_class_dpump_deactivate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Dpump Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Dpump Read</b></p> <p style="text-align: right;">ux_host_class_dpump_read</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Dpump Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>
<p><b>Host Class Dpump Write</b></p> <p style="text-align: right;">ux_host_class_dpump_write</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Dpump Write Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>	<p><b>Host Class Hid Activate</b></p> <p style="text-align: right;">ux_host_class_hid_activate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Hid Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Hid Client Register</b></p> <p style="text-align: right;">ux_host_class_hid_client_register</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Hid Client Register Event.</p> <p><b>Information Fields</b> Info Field 1: Hid client name. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Hid Deactivate</b></p> <p style="text-align: right;">ux_host_class_hid_deactivate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Hid Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Hid Idle Get</b></p> <p style="text-align: right;">ux_host_class_hid_idle_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Hid Idle Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Hid Idle Set</b></p> <p style="text-align: right;">ux_host_class_hid_idle_set</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Hid Idle Set Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Hid Keyboard Activate</b></p> <p style="text-align: right;">ux_host_class_hid_keyboard_activate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Hid Keyboard Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Hid client instance. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Hid Keyboard Deactivate</b></p> <p style="text-align: right;">ux_host_class_hid_keyboard_deactivate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Hid Keyboard Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Hid client instance. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Hid Mouse Activate</b></p> <p>ux_host_class_hid_mouse_activate</p>  <b>Icon</b> <p><b>Description</b> This event represents a USBX Host Class Hid Mouse Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Hid client instance. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Hid Mouse Deactivate</b></p> <p>ux_host_class_hid_mouse_deactivate</p>  <b>Icon</b> <p><b>Description</b> This event represents a USBX Host Class Hid Mouse Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Hid client instance. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Hid Remote Control Activate</b></p> <p>ux_host_class_hid_remote_control_activate</p>  <b>Icon</b> <p><b>Description</b> This event represents a USBX Host Class Hid Remote Control Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Hid client instance. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Hid Remote Control Deactivate</b></p> <p>ux_host_class_hid_remote_control_deactivate</p>  <b>Icon</b> <p><b>Description</b> This event represents a USBX Host Class Hid Remote Control Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Hid client instance. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Hid Report Get</b></p> <p>ux_host_class_hid_report_get</p>  <b>Icon</b> <p><b>Description</b> This event represents a USBX Host Class Hid Report Get.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Client report. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Hid Report Set</b></p> <p>ux_host_class_hid_report_set</p>  <b>Icon</b> <p><b>Description</b> This event represents a USBX Host Class Hid Report Set.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Client report. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Hub Activate</b></p> <p style="text-align: right;">ux_host_class_hub_activate</p>  <p><b>Icon A</b></p> <p><b>Description</b> This event represents a USBX Host Class Hub Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Hub Change Detect</b></p> <p style="text-align: right;">ux_host_class_hub_change_detect</p>  <p><b>Icon D</b></p> <p><b>Description</b> This event represents a USBX Host Class Hub Change Detect Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Hub Deactivate</b></p> <p style="text-align: right;">ux_host_class_hub_deactivate</p>  <p><b>Icon D</b></p> <p><b>Description</b> This event represents a USBX Host Class Hub Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Hub Port Change Connection Process</b></p> <p style="text-align: right;">ux_host_class_hub_port_change_connection_process</p>  <p><b>Icon P</b></p> <p><b>Description</b> This event represents a USBX Host Class Hub Port Change Connection Process Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Port. Info Field 3: Port status. Info Field 4: Not used.</p>
<p><b>Host Class Hub Port Change Enable Process</b></p> <p style="text-align: right;">ux_host_class_hub_port_change_enable_process</p>  <p><b>Icon P</b></p> <p><b>Description</b> This event represents a USBX Host Class Hub Port Change Enable Process Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Port. Info Field 3: Port status. Info Field 4: Not used.</p>	<p><b>Host Class Hub Port Change Over Current Process</b></p> <p style="text-align: right;">ux_host_class_hub_port_change_over_current_process</p>  <p><b>Icon C</b></p> <p><b>Description</b> This event represents allocating a packet via nx_packet_allocate.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Port. Info Field 3: Port status. Info Field 4: Not used.</p>

<p><b>Host Class Hub Port Change Reset Process</b></p> <p><code>ux_host_class_hub_port_change_reset_process</code></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Hub Port Change Reset Process Event.</p> <p><b>Information Fields</b> Info Field 1: Hub. Info Field 2: Port. Info Field 3: Port status. Info Field 4: Not used.</p>	<p><b>Host Class Hub Port Change Suspend Process</b></p> <p><code>ux_host_class_hub_port_change_suspend_process</code></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Hub Port Change Suspend Process Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Port. Info Field 3: Port status. Info Field 4: Not used.</p>
<p><b>Host Class Pima Activate</b></p> <p><code>ux_host_class_pima_activate</code></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Pima Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Pima Deactivate</b></p> <p><code>ux_host_class_pima_deactivate</code></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Pima Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Pima Device Info Get</b></p> <p><code>ux_host_class_pima_device_info_get</code></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Pima Device Info Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Pima device. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Pima Device Reset</b></p> <p><code>ux_host_class_pima_device_reset</code></p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Pima Device Reset Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Pima Notification</b></p> <p style="text-align: center;">ux_host_class_pima_notification</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Pima Notification Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Event code. Info Field 3: Transaction ID. Info Field 4: Parameter1.</p>	<p><b>Host Class Pima Number Objects Get</b></p> <p style="text-align: center;">ux_host_class_pima_num_objects_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Pima Number Objects Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Pima Object Close</b></p> <p style="text-align: center;">ux_host_class_pima_object_close</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Pima Object Close Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Object. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Pima Object Copy</b></p> <p style="text-align: center;">ux_host_class_pima_object_copy</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Pima Object Copy Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Object handle. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Pima Object Delete</b></p> <p style="text-align: center;">ux_host_class_pima_object_delete</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Pima Object Delete Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Object handle. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Pima Object Get</b></p> <p style="text-align: center;">ux_host_class_pima_object_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents getting RARP information via nx_rarp_info_get.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Object handle. Info Field 3: Object. Info Field 4: Not used.</p>

<p><b>Host Class Pima Object Info Get</b></p> <p>Icon </p> <p><code>ux_host_class_pima_object_info_get</code></p> <p><b>Description</b> This event represents a USBX Host Class Pima Object Info Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Object handle. Info Field 3: Object. Info Field 4: Not used.</p>	<p><b>Host Class Pima Object Info Send</b></p> <p>Icon </p> <p><code>ux_host_class_pima_object_info_send</code></p> <p><b>Description</b> This event represents a USBX Host Class Pima Object Info Send Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Object. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Pima Object Move</b></p> <p>Icon </p> <p><code>ux_host_class_pima_object_move</code></p> <p><b>Description</b> This event represents a USBX Host Class Pima Object Move Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Object handle. Info Field 3: Object. Info Field 4: Not used.</p>	<p><b>Host Class Pima Object Send</b></p> <p>Icon </p> <p><code>ux_host_class_pima_object_send</code></p> <p><b>Description</b> This event represents a USBX Host Class Pima Object Send Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Object. Info Field 3: Object buffer. Info Field 4: Object length.</p>
<p><b>Host Class Pima Object Transfer Abort</b></p> <p>Icon </p> <p><code>ux_host_class_pima_object_transfer_abort</code></p> <p><b>Description</b> This event represents a USBX Host Class Pima Object Transfer Abort Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Object handle. Info Field 3: Object. Info Field 4: Not used.</p>	<p><b>Host Class Pima Read</b></p> <p>Icon </p> <p><code>ux_host_class_pima_read</code></p> <p><b>Description</b> This event represents a USBX Host Class Pima Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Data length. Info Field 4: Not used.</p>

<p><b>Host Class Pima Request Cancel</b></p> <p>Icon  ux_host_class_pima_request_cancel</p> <p><b>Description</b> This event represents a USBX Host Class Pima Request Cancel Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Pima Session Close</b></p> <p>Icon  ux_host_class_pima_session_close</p> <p><b>Description</b> This event represents a USBX Host Class Pima Session Close Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Pima session. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Pima Session Open</b></p> <p>Icon  ux_host_class_pima_session_open</p> <p><b>Description</b> This event represents a USBX Host Class Pima Session Open Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Pima session. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Pima Storage Ids Get</b></p> <p>Icon  ux_host_class_pima_storage_ids_get</p> <p><b>Description</b> This event represents a USBX Host Class Pima Storage Ids Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Storage ID array. Info Field 3: Storage ID length. Info Field 4: Not used.</p>
<p><b>Host Class Pima Storage Info Get</b></p> <p>Icon  ux_host_class_pima_storage_info_get</p> <p><b>Description</b> This event represents a USBX Host Class Pima Storage Info Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Storage ID. Info Field 3: Storage. Info Field 4: Not used.</p>	<p><b>Host Class Pima Thumb Get</b></p> <p>Icon  ux_host_class_pima_thumb_get</p> <p><b>Description</b> This event represents unaccepting a TCP server connection via nx_tcp_server_socket_unaccept.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Object handle. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Pima Write</b></p> <p style="text-align: right;">ux_host_class_pima_write</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Pima Write.</p> <p><b>Information Fields</b> Info Field 1: Class Instance. Info Field 2: Data pointer. Info Field 3: Data length. Info Field 4: Not used.</p>	<p><b>Host Class Printer Activate</b></p> <p style="text-align: right;">ux_host_class_printer_activate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Printer Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Pointer to IP instance. Info Field 2: Pointer to socket. Info Field 3: Type of service. Info Field 4: Receive window size.</p>
<p><b>Host Class Printer Deactivate</b></p> <p style="text-align: right;">ux_host_class_printer_deactivate</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Printer Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Printer Name Get</b></p> <p style="text-align: right;">ux_host_class_printer_name_get</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Printer Name Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Printer Read</b></p> <p style="text-align: right;">ux_host_class_printer_read</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Printer Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>	<p><b>Host Class Printer Soft Reset</b></p> <p style="text-align: right;">ux_host_class_printer_soft_reset</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Class Printer Soft Reset Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Printer Status Get</b></p> <p>Icon </p> <p><code>ux_host_class_printer_status_get</code></p> <p><b>Description</b> This event represents a USBX Host Class Printer Status Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Printer status. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Printer Write</b></p> <p>Icon </p> <p><code>ux_host_class_printer_write</code></p> <p><b>Description</b> This event represents a USBX Host Class Printer Write.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>
<p><b>Host Class Prolific Activate</b></p> <p>Icon </p> <p><code>ux_host_class_prolific_activate</code></p> <p><b>Description</b> This event represents a USBX Host Class Prolific Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Prolific Deactivate</b></p> <p>Icon </p> <p><code>ux_host_class_prolific_deactivate</code></p> <p><b>Description</b> This event represents a USBX Host Class Prolific Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Prolific ioctl Abort In Pipe</b></p> <p>Icon </p> <p><code>ux_host_class_prolific_ioctl_abort_in_pipe</code></p> <p><b>Description</b> This event represents a USBX Host Class Prolific ioctl Abort In Pipe Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Endpoint. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Prolific ioctl Abort Out Pipe</b></p> <p>Icon </p> <p><code>ux_host_class_prolific_ioctl_abort_out_pipe</code></p> <p><b>Description</b> This event represents a USBX Host Class Prolific ioctl Abort Out Pipe Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Endpoint. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Prolific ioctl Get Device Status</b></p> <p>Icon  ux_host_class_prolific_ioctl_get_device_status</p> <p><b>Description</b> This event represents a USBX Host Class Prolific ioctl Get Device Status Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Device status. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Prolific ioctl Get Line Coding</b></p> <p>Icon  ux_host_class_prolific_ioctl_get_line_coding</p> <p><b>Description</b> This event represents a USBX Host Class Prolific ioctl Get Line Coding Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Parameter. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Prolific ioctl Purge</b></p> <p>Icon  ux_host_class_prolific_ioctl_purge</p> <p><b>Description</b> This event represents a USBX Host Class Prolific ioctl Purge Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Parameter. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Prolific ioctl Report Device Status Change</b></p> <p>Icon  ux_host_class_prolific_ioctl_report_device_status_change</p> <p><b>Description</b> This event represents a USBX Host Class Prolific ioctl Report Device Status Change Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Parameter. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Prolific ioctl Send Break</b></p> <p>Icon  ux_host_class_prolific_ioctl_send_break</p> <p><b>Description</b> This event represents a USBX Host Class Prolific ioctl Send Break Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Prolific ioctl Set Line Coding</b></p> <p>Icon  ux_host_class_prolific_ioctl_set_line_coding</p> <p><b>Description</b> This event represents a USBX Host Class Prolific ioctl Set Line Coding Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Parameter. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Prolific ioctl Set Line State</b></p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Class Prolific ioctl Set Line State Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Parameter. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Prolific Read</b></p> <p>ux_host_class_prolific_read</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Class Prolific Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>
<p><b>Host Class Prolific Reception Start</b></p> <p>ux_host_class_prolific_reception_start</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Class Prolific Reception Start Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Prolific Reception Stop</b></p> <p>ux_host_class_prolific_reception_stop</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Class Prolific Reception Stop Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Prolific Write</b></p> <p>ux_host_class_prolific_write</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Class Prolific Write Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Data pointer. Info Field 3: Requested length. Info Field 4: Not used.</p>	<p><b>Host Class Storage Activate</b></p> <p>ux_host_class_storage_activate</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Class Storage Activate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Class Storage Deactivate</b></p> <p> <code>ux_host_class_storage_deactivate</code></p> <p><b>Description</b> This event represents a USBX Host Class Storage Deactivate Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Storage Media Capacity Get</b></p> <p> <code>ux_host_class_storage_media_capacity_get</code></p> <p><b>Description</b> This event represents a USBX Host Class Storage Media Capacity Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Storage Media Format Capacity Get</b></p> <p> <code>ux_host_class_storage_media_format_capacity_get</code></p> <p><b>Description</b> This event represents a USBX Host Class Storage Media Format Capacity Get Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Storage Media Mount</b></p> <p> <code>ux_host_class_storage_media_mount</code></p> <p><b>Description</b> This event represents a USBX Host Class Storage Media Mount Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Sector. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Storage Media Open</b></p> <p> <code>ux_host_class_storage_media_open</code></p> <p><b>Description</b> This event represents a USBX Host Class Storage Media Open Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Media. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Storage Media Read</b></p> <p> <code>ux_host_class_storage_media_read</code></p> <p><b>Description</b> This event represents a USBX Host Class Storage Media Read Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Sector start. Info Field 3: Sector count. Info Field 4: Data pointer.</p>

<p><b>Host Class Storage Media Write</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Class Storage Media Write Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Sector start. Info Field 3: Sector count. Info Field 4: Data pointer.</p>	<p><b>Host Class Storage Request Sense</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Class Storage Request Sense Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Class Storage Start Stop</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Class Storage Start Stop Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Start stop signal. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Class Storage Unit Ready Test</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Class Storage Unit Ready Test Event.</p> <p><b>Information Fields</b> Info Field 1: Class instance. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Class Instance Create</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Stack Class Instance Create Event.</p> <p><b>Information Fields</b> Info Field 1: Class. Info Field 2: Class Instance. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Class Instance Destroy</b></p> <p>Icon </p> <p><b>Description</b> This event represents a USBX Host Stack Class Instance Destroy Event.</p> <p><b>Information Fields</b> Info Field 1: Class. Info Field 2: Class Instance. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Stack Configuration Delete</b></p> <p>ux_host_stack_configuration_delete</p> <p> <b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Configuration Delete Event.</p> <p><b>Information Fields</b> Info Field 1: Configuration. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Configuration Enumerate</b></p> <p>ux_host_stack_configuration_enumerate</p> <p> <b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Configuration Enumerate Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Configuration Instance Create</b></p> <p>ux_host_stack_configuration_instance_create</p> <p> <b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Configuration Instance Create Event.</p> <p><b>Information Fields</b> Info Field 1: Configuration. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Configuration Instance Delete</b></p> <p>ux_host_stack_configuration_instance_delete</p> <p> <b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Configuration Instance Delete Event.</p> <p><b>Information Fields</b> Info Field 1: Configuration. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Configuration Set</b></p> <p>ux_host_stack_configuration_set</p> <p> <b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Configuration Set Event.</p> <p><b>Information Fields</b> Info Field 1: Configuration. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Device Address Set</b></p> <p>ux_host_stack_device_address_set</p> <p> <b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Device Address Set Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Device Address. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Stack Device Configuration Get</b></p> <p>Icon </p> <p><code>ux_host_stack_device_configuration_get</code></p> <p><b>Description</b> This event represents a USBX Host Stack Device Configuration Get Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Configuration. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Device Configuration Select</b></p> <p>Icon </p> <p><code>ux_host_stack_device_configuration_select</code></p> <p><b>Description</b> This event represents a USBX Host Stack Device Configuration Select Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Configuration. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Device Descriptor Read</b></p> <p>Icon </p> <p><code>ux_host_stack_device_descriptor_read</code></p> <p><b>Description</b> This event represents a USBX Host Stack Device Descriptor Read Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Device Get</b></p> <p>Icon </p> <p><code>ux_host_stack_device_get</code></p> <p><b>Description</b> This event represents a USBX Host Stack Device Get Event.</p> <p><b>Information Fields</b> Info Field 1: Device index. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Device Remove</b></p> <p>Icon </p> <p><code>ux_host_stack_device_remove</code></p> <p><b>Description</b> This event represents a USBX Host Stack Device Remove Event.</p> <p><b>Information Fields</b> Info Field 1: Hcd. Info Field 2: Parent. Info Field 3: Port Index. Info Field 4: Device.</p>	<p><b>Host Stack Device Resource Free</b></p> <p>Icon </p> <p><code>ux_host_stack_device_resource_free</code></p> <p><b>Description</b> This event represents a USBX Host Stack Device Resource Free Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Stack Endpoint Instance Create</b></p> <p>ux_host_stack_endpoint_instance_create</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Endpoint Instance Create Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Endpoint. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Endpoint Instance Delete</b></p> <p>ux_host_stack_endpoint_instance_delete</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Endpoint Instance Delete Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Endpoint. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Endpoint Reset</b></p> <p>ux_host_stack_endpoint_reset</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Endpoint Reset Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Endpoint. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Endpoint Transfer Abort</b></p> <p>ux_host_stack_endpoint_transfer_abort</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Endpoint Transfer Abort Event.</p> <p><b>Information Fields</b> Info Field 1: Endpoint. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Host Controller Register</b></p> <p>ux_host_stack_hcd_register</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Host Controller Register.</p> <p><b>Information Fields</b> Info Field 1: Hcd Name. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Initialize</b></p> <p>ux_host_stack_initialize</p> <p><b>Icon</b> </p> <p><b>Description</b> This event represents a USBX Host Stack Initialize Event.</p> <p><b>Information Fields</b> Info Field 1: Not used. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Stack Interface Endpoint Get</b></p> <p>Icon </p> <p>Internal TCP retry event</p> <p><b>Description</b> This event represents an internal NetX TCP retry event.</p> <p><b>Information Fields</b> Info Field 1: Interface. Info Field 2: Endpoint index. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Interface Instance Create</b></p> <p>Icon </p> <p>ux_host_stack_interface_instance_create</p> <p><b>Description</b> This event represents a USBX Host Stack Interface Instance Create Event.</p> <p><b>Information Fields</b> Info Field 1: Interface. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Interface Instance Delete</b></p> <p>Icon </p> <p>ux_host_stack_interface_instance_delete</p> <p><b>Description</b> This event represents a USBX Host Stack Interface Instance Delete Event.</p> <p><b>Information Fields</b> Info Field 1: Interface. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Interface Set</b></p> <p>Icon </p> <p>ux_host_stack_interface_set</p> <p><b>Description</b> This event represents a USBX Host Stack Interface Set Event.</p> <p><b>Information Fields</b> Info Field 1: Interface. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Interface Setting Select</b></p> <p>Icon </p> <p>ux_host_stack_interface_setting_select</p> <p><b>Description</b> This event represents a USBX Host Stack Interface Setting Select Event.</p> <p><b>Information Fields</b> Info Field 1: Interface. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack New Configuration Create</b></p> <p>Icon </p> <p>ux_host_stack_new_configuration_create</p> <p><b>Description</b> This event represents a USBX Host Stack New Configuration Create Event.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Configuration. Info Field 3: Not used. Info Field 4: Not used.</p>

<p><b>Host Stack New Device Create</b></p> <p>ux_host_stack_new_device_create</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Stack New Device Create Event.</p> <p><b>Information Fields</b> Info Field 1: Hcd. Info Field 2: Device owner. Info Field 3: Port index. Info Field 4: Device.</p>	<p><b>Host Stack New Endpoint Create</b></p> <p>ux_host_stack_new_endpoint_create</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Stack New Endpoint Create Event.</p> <p><b>Information Fields</b> Info Field 1: Interface. Info Field 2: Endpoint. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Root Hub Change Process</b></p> <p>ux_host_stack_rh_change_process</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Stack Root Hub Change Process.</p> <p><b>Information Fields</b> Info Field 1: Port index. Info Field 2: Not used. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Root Hub Device Extraction</b></p> <p>ux_host_stack_rh_device_extraction</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Stack Root Hub Device Extraction Event.</p> <p><b>Information Fields</b> Info Field 1: Hcd. Info Field 2: Port index. Info Field 3: Not used. Info Field 4: Not used.</p>
<p><b>Host Stack Root Hub Device Insertion</b></p> <p>ux_host_stack_rh_device_insertion</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Stack Root Hub Device Insertion.</p> <p><b>Information Fields</b> Info Field 1: Hcd. Info Field 2: Port index. Info Field 3: Not used. Info Field 4: Not used.</p>	<p><b>Host Stack Transfer Request</b></p> <p>ux_host_stack_transfer_request</p> <p>Icon  </p> <p><b>Description</b> This event represents a USBX Host Stack Transfer Request.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Endpoint. Info Field 3: Transfer request. Info Field 4: Not used.</p>

Host Stack Transfer Request Abort	USB Error
<p>Icon </p> <p><b>Description</b> This event represents a USBX Host Stack Transfer Request Abort.</p> <p><b>Information Fields</b> Info Field 1: Device. Info Field 2: Endpoint. Info Field 3: Transfer request. Info Field 4: Not used.</p>	<p>Internal I/O driver get status</p> <p>Icon </p> <p>ux_error</p> <p><b>Description</b> This event represents a USBX Error Event.</p> <p><b>Information Fields</b> Info Field 1: USBX error. Info Field 2: Error Name. Info Field 3: Not used. Info Field 4: Not used.</p>



# 10

## ***Customer User Events***

This chapter contains a description of how to create user-defined events and custom icons and information fields for such events.  
This chapter includes the following sections:

- Inserting User-Defined Events 222
- Default Display of User-Defined Events 222
- Defining Custom User-Defined Event Icons 224

## Inserting User-Defined Events

ThreadX provides the ability for developers to log their own user-defined events, providing even more useful information that can be viewed graphically by TraceX.

User-defined event numbers range from **TX\_TRACE\_USER\_EVENT\_START** (4096) through **TX\_TRACE\_USER\_EVENT\_END** (65535), inclusive. The placement of the events in the trace buffer is done via the ***tx\_trace\_user\_event\_insert***, defined in Chapter 5. The following example calls insert two user-defined events into the current trace buffer on the target, namely user-defined event 4096 and event 4098:

```
tx_trace_user_event_insert(4096, 1, 2, 3, 4);  
tx_trace_user_event_insert(4098, 0x100, 0x200, 0x300, 0x400);
```

## Default Display of User-Defined Events

By default, TraceX displays all user events with a default user-defined Event icon as described in Chapter 6. Figure 10.1 shows the default user-defined event icon for events 202

and 203, which were placed in the event buffer via the previous ***tx\_trace\_user\_event\_insert*** examples.

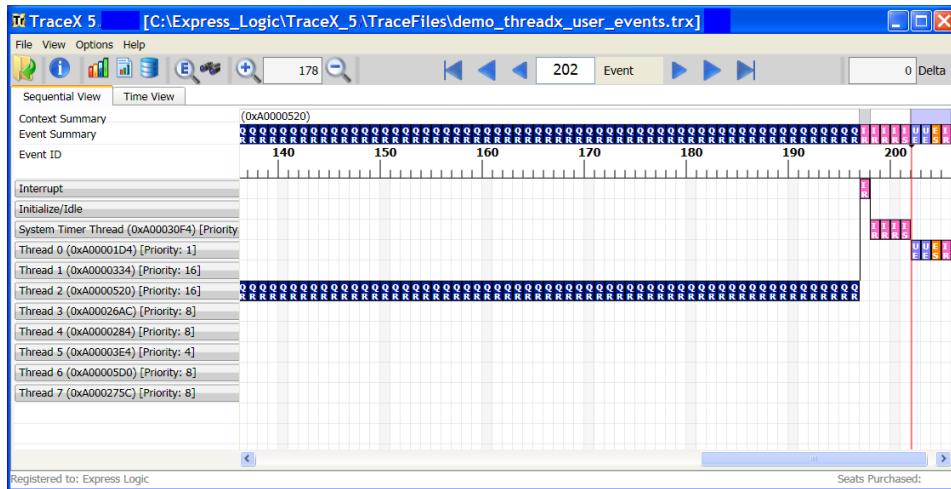


FIGURE 10.1

Detailed information is also available for user-defined Events. Figure 10.2 shows the detailed event information for event 202, which has event number 4096 and shows the specified 4 information fields.

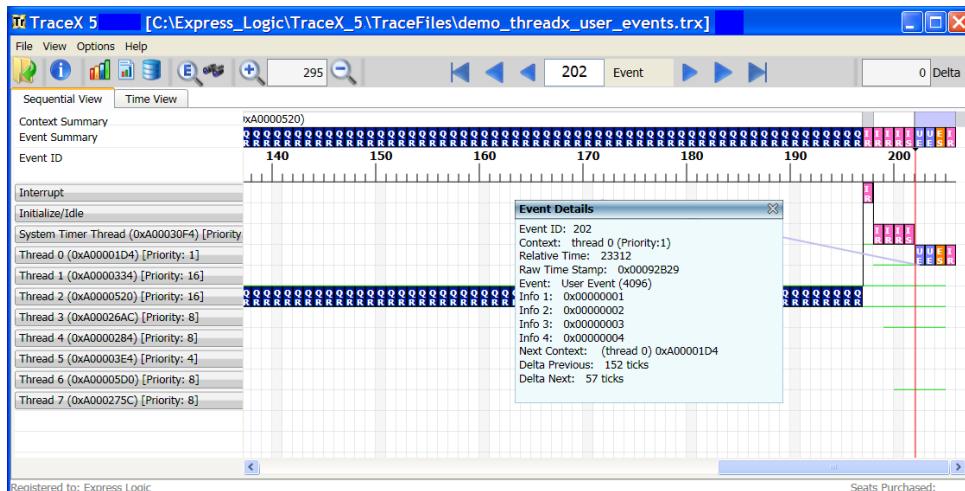


FIGURE 10.2

## Defining Custom User-Defined Event Icons

TraceX also provides the user the ability to create custom user-defined event icons as well as custom information field labels. This is achieved by adding event icon specifications to the **eltrxcustom.trxc** configuration file, which is located in the **CustomEvents** sub-directory in the main TraceX installation directory. An example directory path is shown in Figure 10.3.

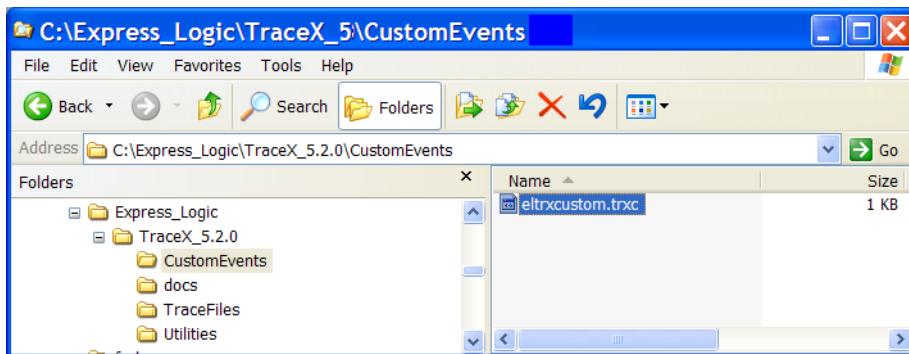


FIGURE 10.3

The **eltrxcustom.trxc** custom event configuration file is a simple ASCII text file containing 0 or more custom event definitions. The format of the file is as follows:

```
//Comments  
Start  
[custom event definition(s)]  
End
```

Each line between Start and End is used to define a single custom event. TraceX provides a template version of this file with no custom events defined (nothing between the “Start”

and “End” labels). The format of a custom event definition is as follows:

```
number, name, abbreviation, top_color, bottom_color, label1, label2, label3, label4
```

where:

number	Defines the user-defined event number, between 4096 and 65535, inclusive.
name	Defines the logical name for the user-defined event.
abbreviation	Defines the 2-letter user-defined event abbreviation.
top_color	Defines the RGB value for the top-half of the icon, which is a three digit number in parenthesis. Some typical RGB definitions are  BLACK = (0,0,0) WHITE = (255,255,255) RED = (255,0,0) GREEN = (0,255,0) BLUE = (0,0,255) YELLOW = (255,255,0) CYAN = (0,255,255) MAGENTA = (255,0,255)

Using the RGB specification gives the user a broad range of colors for each user-defined icon. For more information on RGB color definition, please see:

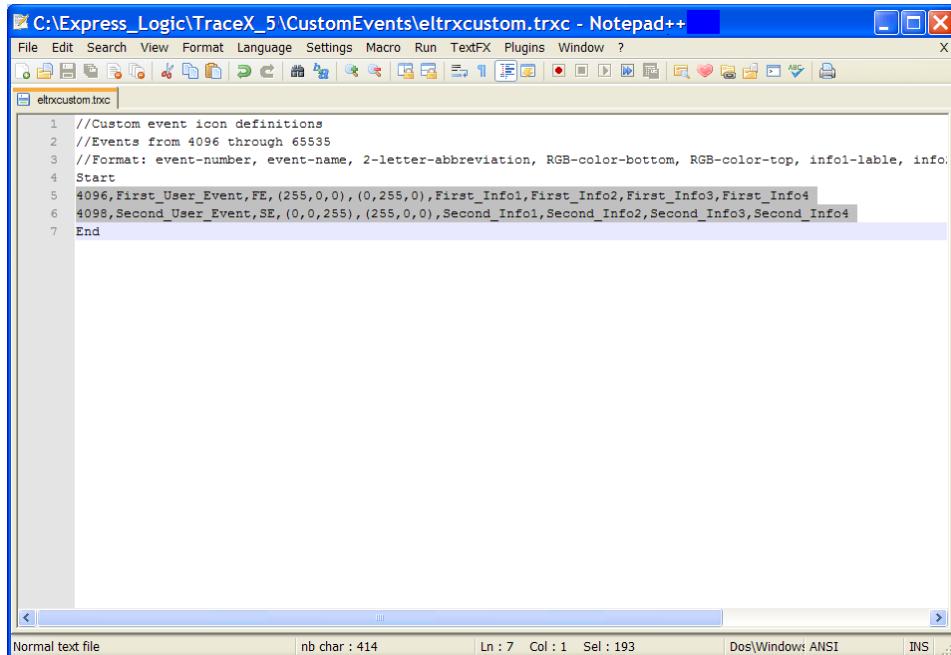
[http://en.wikipedia.org/wiki/RGB#Digital\\_representations](http://en.wikipedia.org/wiki/RGB#Digital_representations)

bottom_color	Defines the RGB value for the bottom-half of the icon, which is a three digit number in parenthesis.
--------------	--

label1	Label for <i>info_field_1</i> , as specified in the <b><i>tx_trace_user_event_insert</i></b> call.
label2	Label for <i>info_field_2</i> , as specified in the <b><i>tx_trace_user_event_insert</i></b> call.
label3	Label for <i>info_field_3</i> , as specified in the <b><i>tx_trace_user_event_insert</i></b> call.
label4	Label for <i>info_field_4</i> , as specified in the <b><i>tx_trace_user_event_insert</i></b> call.

Example definitions for each of the two user-defined events used in this chapter are shown in Figure 10.4. The first definition is for event 4096 at line 5 of the ***eltrxcustom.trxc*** file. This definition gives user-defined event 4096 the name **First\_User\_Event**, specifies a two-letter abbreviation of **FE**, makes the top portion of the icon **red**, the bottom portion of the icon **green**, and names the information fields as **First\_Info1**, **First\_Info2**, **First\_Info3**, and

**First\_Info4.** User-defined event 4098 is defined similarly at line 6 of **eltrxcustom.trxc**.



The screenshot shows a Notepad++ window with the title bar "C:\Express\_Logic\TraceX\_5\CustomEvents\eltrxcustom.trxc - Notepad++". The menu bar includes File, Edit, Search, View, Format, Language, Settings, Macro, Run, TextFX, Plugins, Window, and ?.

The code in the editor is:

```
1 //Custom event icon definitions
2 //Events from 4096 through 65535
3 //Format: event-number, event-name, 2-letter-abbreviation, RGB-color-bottom, RGB-color-top, info1-lable, info2-lable
4 Start
5 4096,First_User_Event,FE,(255,0,0),(0,255,0),First_Info1,First_Info2,First_Info3,First_Info4
6 4098,Second_User_Event,SE,(0,0,255),(255,0,0),Second_Info1,Second_Info2,Second_Info3,Second_Info4
7 End
```

The status bar at the bottom shows "Normal text file", "nb char : 414", "Ln : 7 Col : 1 Sel : 193", "Dos\Window: ANSI", and "INS".

**FIGURE 10.4**

Since the **eltrxcustom.trxc** file is read by TraceX during initialization, TraceX must be exited and restarted before the custom icon definitions can take effect. Figure 10.5 shows the TraceX display of user-

defined events 202 and 203 with the custom event icons defined in *eltrxcustom.trxc*.

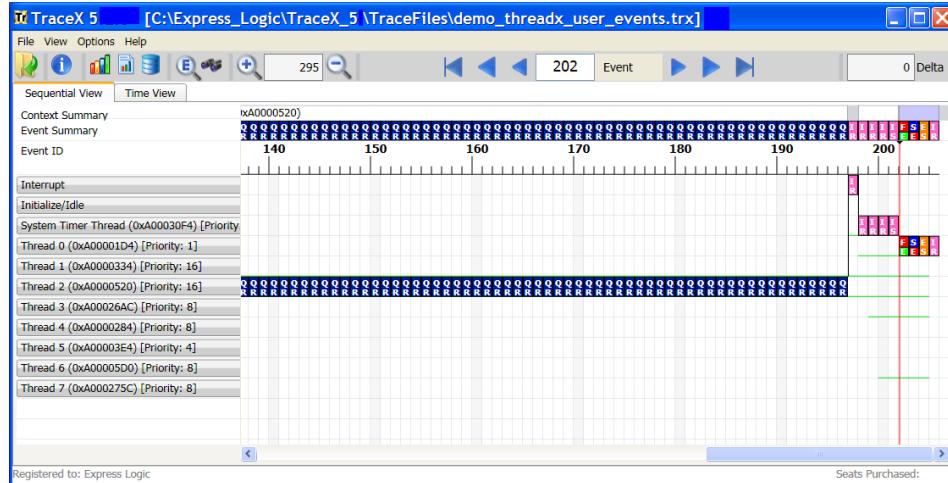


FIGURE 10.5

The additional information in the custom event definition is shown when the event is selected via a double-click, mouse-over, or selection of the current event button. Figure 10.6 shows the double-click selection on event 202. Note that the event name

and information fields all match the sample definition that was added to **eltrxcustom.trxc**.

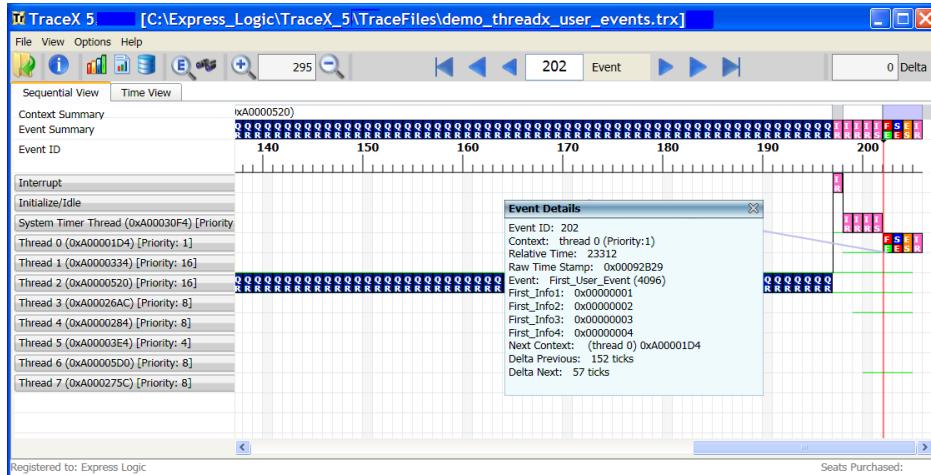


FIGURE 10.6



# 11

## *Format of Event Trace Buffer*

ThreadX provides built-in event trace support for all ThreadX services, thread state changes, and user-defined events. To use event trace, simply build the ThreadX, NetX, and FileX libraries with **TX\_ENABLE\_EVENT\_TRACE** defined and enable tracing by calling the **tx\_trace\_enable** function. This chapter describes that process.

- Event Trace Format 232
  - Event Trace Control Header 232
    - Control Header ID 233
    - Timer Valid Mask 233
    - Trace Base Address 234
    - Registry Start and End Pointers 234
    - Registry Name Size 234
    - Buffer Start and End Pointers 234
    - Current Buffer Pointer 235
  - Event Trace Object Registry 235
    - Object Available Flag 235
    - Object Entry Type 236
    - Object Pointer 237
    - Object Reserved Fields 237
    - Object Parameters 237
    - Object Name 237
  - Event Trace Entries 238
    - Thread Pointer 238
    - Thread Priority 239
    - Event ID 239
  - Information Fields (1-4) 239

## Event Trace Format

The format of the ThreadX event trace buffer is divided into three sections, namely the control header, object registry, and the trace entries. The following describes the general layout of the ThreadX event trace buffer:

**Control Header**  
**Object Registry Entry 0**  
...  
**Object Register Entry “n”**  
**Event Trace Entry 0**  
...  
**Event Trace Entry “n”**

## Event Trace Control Header

The control header defines the exact layout of the event trace buffer. This includes how many ThreadX objects can be registered as well as how many events can be recorded. In addition, the control header defines where each of the elements of the

trace buffer resides. The following data structure defines the control header:

```
typedef struct TX_TRACE_CONTROL_HEADER_STRUCT
{
    ULONG      tx_trace_control_header_id;
    ULONG      tx_trace_control_header_timer_valid_mask;
    ULONG      tx_trace_control_header_trace_base_address;
    ULONG      tx_trace_control_header_object_registry_start_pointer;
    USHORT     tx_trace_control_header_reserved1;
    USHORT     tx_trace_control_header_object_registry_name_size;
    ULONG      tx_trace_control_header_object_registry_end_pointer;
    ULONG      tx_trace_control_header_buffer_start_pointer;
    ULONG      tx_trace_control_header_buffer_end_pointer;
    ULONG      tx_trace_control_header_buffer_current_pointer;
    ULONG      tx_trace_control_header_reserved2;
    ULONG      tx_trace_control_header_reserved3;
    ULONG      tx_trace_control_header_reserved4;
} TX_TRACE_CONTROL_HEADER;
```

## Control Header ID

The control header ID consists of the 32-bit HEX value of 0x54585442, which corresponds to the ASCII characters **TXTB**. Since this value is written as a 32-bit unsigned variable, it can also be used to detect the endianness of the event trace buffer. For example, if the value in the first four bytes of memory is 0x54, 0x58, 0x54, 0x42, the event trace buffer was written in big endian format. Otherwise, the event trace buffer was written in little endian format.

## Timer Valid Mask

The timer valid mask defines how many bits of the time-stamp in the actual event trace entries are valid. For example, if the time-stamp source has 16-bits, the value in this field should be 0xFFFF. A 32-bit time-stamp source would have a value of 0xFFFFFFFF. This value is defined by the **TX\_TRACE\_TIME\_MASK** constant in **tx\_port.h**.

## Trace Base Address

The trace buffer base address is the address the application specified as the start of the trace buffer in the ***tx\_trace\_enable*** call. This address is maintained for the sole use of the analysis tool to derive buffer-relative offsets for the various elements in the buffer. For example, the buffer relative offset of the current event in the trace buffer is calculated by simple subtraction of the base address from the current event address.

## Registry Start and End Pointers

The registry start pointer points to the address of the first object registry entry, while the registry end pointer points to the address immediately following the last register entry. These values are setup during the ***tx\_trace\_enable*** processing and are not changed throughout the duration of tracing.

## Registry Name Size

The registry name size defines maximum size in bytes for each object name in the registry entry and is defined by the symbol ***TX\_TRACE\_OBJECT\_REGISTRY\_NAME***. The default value is 32 and is defined in ***tx\_trace.h***. The object name corresponds to the name given by the application when the object was created. For example, the object registry name for a thread is the name supplied by the application to the ***tx\_thread\_create*** call.

## Buffer Start and End Pointers

The event trace buffer start pointer points to the address of the first trace entry, while the registry end pointer points to the address immediately following the last trace entry. These values are setup during the ***tx\_trace\_enable*** processing and are not changed throughout the duration of tracing.

## Current Buffer Pointer

The event trace buffer current pointer points to the address of the oldest trace entry. Since the trace entries are maintained in a circular list, the current buffer pointer is also represents the next trace entry to be written.

# Event Trace Object Registry

The event trace object registry contains *n* object registry entries that correspond to the objects created by the application. The main purpose of the object registry is for external analysis tools to correlate actual object names with the object addresses of the trace buffer entries. The number of registry entries is specified by the application in the *tx\_trace\_enable* call.

Each object register entry contains information about a specific ThreadX object previously created by the application. The following data structure defines each object registry entry:

```
typedef struct TX_TRACE_OBJECT_REGISTRY_ENTRY_STRUCT
{
    UCHAR          tx_trace_object_registry_entry_object_available;
    UCHAR          tx_trace_object_registry_entry_object_type;
    UCHAR          tx_trace_object_registry_entry_object_reserved1;
    UCHAR          tx_trace_object_registry_entry_object_reserved2;
    ULONG          tx_trace_thread_registry_entry_object_pointer;
    ULONG          tx_trace_object_registry_entry_object_parameter_1;
    ULONG          tx_trace_object_registry_entry_object_parameter_2;
    UCHAR          tx_trace_thread_registry_entry_object_name
                                [TX_TRACE_OBJECT_REGISTRY_NAME];
} TX_TRACE_OBJECT_REGISTRY_ENTRY;
```

## Object Available Flag

The object available flag is set to 1 if the object registry entry is available. Otherwise, if the value is not 1, the object registry entry is not available. Note

that the entry could still contain valid information even though it is available.

## Object Entry Type

The object entry type identifies the type of object in this entry. The following is a list of the valid object types:

Value	Object Type
0	Not Valid
1	Thread
2	Timer
3	Queue
4	Semaphore
5	Mutex
6	Event Flags Group
7	Block Pool
8	Byte Pool
9	Media
10	File
11	IP
12	Packet Pool
13	TCP Socket
14	UDP Socket
15-20	Reserved
21	USB Host Stack Device
22	USB Host Stack Interface
23	USB Host Endpoint
24	USB Host Class
25	USB Device
26	USB Device Interface
27	USB Device Endpoint
28	USB Device Class

## Object Pointer

The object pointer specifies the object address that is used for accessing the object using the ThreadX API.

## Object Reserved Fields

For all objects other than threads, these reserved fields should be 0. For threads, the priority of the thread at the time it is entered into the registry is placed in these two reserved fields.

## Object Parameters

The object parameters contain supplemental information about the object. The following describes the supplemental information for each ThreadX object:

Object Type	Parameter 1	Parameter 2
Thread	Stack Start	Stack Size
Timer	Initial Ticks	Reschedule Ticks
Queue	Queue Size	Message Size
Semaphore	Initial Instances	-
Mutex	Inheritance Flag	-
Event Flags Group	-	-
Block Pool	Total Blocks	Block Size
Byte Pool	Total Bytes	-
Media	Fat Cache Size	Sector Cache Size
File	-	-
IP	Stack Start	Stack Size
Packet Pool	Packet Size	Number of Packets
TCP Socket	IP address	Window Size
UDP Socket	IP address	RX Queue Max

## Object Name

The object name contains the name of the ThreadX object. The name is the name provided to ThreadX at the time the object was created. By default, the object

name has a maximum of 32 characters. Actual names greater than 32 characters are truncated.

## Event Trace Entries

The event trace entries are found in the bottom portion of the event trace buffer. The entries are maintained in a circular list, with the current entry pointer pointing to the oldest entry. The number of entries in the list is calculated by the **tx\_trace\_enable** call.

Each object register entry contains information about a specific ThreadX trace event. The following data structure defines each trace event entry:

```
typedef struct TX_TRACE_BUFFER_ENTRY_STRUCT
{
    ULONG          tx_trace_buffer_entry_thread_pointer;
    ULONG          tx_trace_buffer_entry_thread_priority;
    ULONG          tx_trace_buffer_entry_event_id;
    ULONG          tx_trace_buffer_entry_time_stamp;
    ULONG          tx_trace_buffer_entry_information_field_1;
    ULONG          tx_trace_buffer_entry_information_field_2;
    ULONG          tx_trace_buffer_entry_information_field_3;
    ULONG          tx_trace_buffer_entry_information_field_4;

} TX_TRACE_BUFFER_ENTRY;
```

### Thread Pointer

The thread pointer contains the address of the thread running at the time of this event. If the event occurred during initialization (no thread running), the value of this pointer is 0xF0F0F0F0. If the event occurred during an Interrupt Service Routine (ISR), the value of this pointer is 0xFFFFFFFF. If the entry has not yet been used, the value of this pointer is 0.

## Thread Priority

The thread priority field contains the thread priority and preemption-threshold of the thread that was running at the time of this event. If an interrupt context is present (thread pointer is 0xFFFFFFFF), the value of this field is not the priority but instead the value of `_tx_thread_current_ptr` at the time of the event. Otherwise, the value of this field is 0.

## Event ID

The event ID specifies the event that took place. Valid ThreadX trace event IDs range from 1 through 1024. Values starting at 1025 and above are reserved for user-specific events. Please refer to the `tx_trace.h` file for the complete definition of ThreadX event IDs.

## Information Fields (1-4)

The information fields contain additional information about the specific event. Please refer to the `tx_trace.h` file for the complete description of the information fields for each of the defined ThreadX event IDs.



# A

## ***Sample tx\_port.h***

This chapter displays a sample **tx\_port.h** file.

```
/*
 * Copyright (c) 1996-2006 by Express Logic Inc.
 *
 * This software is copyrighted by and is the sole property of Express
 * Logic, Inc. All rights, title, ownership, or other interests
 * in the software remain the property of Express Logic, Inc. This
 * software may only be used in accordance with the corresponding
 * license agreement. Any unauthorized use, duplication, transmission,
 * distribution, or disclosure of this software is expressly forbidden.
 *
 * This Copyright notice may not be removed or modified without prior
 * written consent of Express Logic, Inc.
 *
 * Express Logic, Inc. reserves the right to modify this software
 * without notice.
 *
 * Express Logic, Inc.          info@expresslogic.com
 * 11423 West Bernardo Court   http://www.expresslogic.com
 * San Diego, CA 92127
 */
*****
```

```
/*
 ** ThreadX Component
 ** Port Specific
 */
*****
```

```
/*
 * PORT SPECIFIC C INFORMATION
 * tx_port.h
 * AUTHOR
 * William E. Lamie, Express Logic, Inc.
 * DESCRIPTION
 * This file contains data type definitions that make the ThreadX
 * real-time kernel function identically on a variety of different
 * processor architectures. For example, the size or number of bits
 * in an "int" data type vary between microprocessor architectures and
 * even C compilers for the same microprocessor. ThreadX does not
 * directly use native C data types. Instead, ThreadX creates its
 * own special types that can be mapped to actual data types by this
 * file to guarantee consistency in the interface and functionality.
 * RELEASE HISTORY
 * DATE      NAME      DESCRIPTION
 * 12-12-2005 William E. Lamie  Initial ARM7 RealView
 *                               Support Version 5.0
 */
*****
```

```
#ifndef TX_PORT_H
#define TX_PORT_H
```

```
/* Determine if the optional ThreadX user define file should be used. */
#ifndef TX_INCLUDE_USER_DEFINE_FILE
```

```

/* Yes, include the user defines in tx_user.h. The defines in this file may
   alternately be defined on the command line. */

#include "tx_user.h"
#endif

/* Define compiler library include files. */

#include <stdlib.h>
#include <string.h>

/* Define ThreadX basic types for this port. */

#define VOID void
typedef char CHAR;
typedef unsigned char UCHAR;
typedef int INT;
typedef unsigned int UINT;
typedef long LONG;
typedef unsigned long ULONG;
typedef short SHORT;
typedef unsigned short USHORT;

/* Define the priority levels for ThreadX. Legal values range
   from 32 to 1024 and MUST be evenly divisible by 32. */

#ifndef TX_MAX_PRIORITIES
#define TX_MAX_PRIORITIES 32
#endif

/* Define the minimum stack for a ThreadX thread on this processor. If the size supplied during
   thread creation is less than this value, the thread create call will return an error. */

#ifndef TX_MINIMUM_STACK
#define TX_MINIMUM_STACK 200 /* Minimum stack size for this port */
#endif

/* Define the system timer thread's default stack size and priority. These are only applicable
   if TX_TIMER_PROCESS_IN_ISR is not defined. */

#ifndef TX_TIMER_THREAD_STACK_SIZE
#define TX_TIMER_THREAD_STACK_SIZE 1024 /* Default timer thread stack size */
#endif

#ifndef TX_TIMER_THREAD_PRIORITY
#define TX_TIMER_THREAD_PRIORITY 0 /* Default timer thread priority */
#endif

/* Define various constants for the ThreadX ARM port. */

#ifndef TX_ENABLE_FIQ_SUPPORT
#define TX_INT_DISABLE 0xC0 /* Disable IRQ & FIQ interrupts */
#else
#define TX_INT_DISABLE 0x80 /* Disable IRQ interrupts */
#endif
#define TX_INT_ENABLE 0x00 /* Enable IRQ interrupts */

/* Define the clock source for trace event entry time stamp. The following two item are port specific.

   For example, if the time source is at the address 0x0a800024 and is 16-bits in size, the clock
   source constants would be:

#define TX_TRACE_TIME_SOURCE *((ULONG *) 0x0a800024)
#define TX_TRACE_TIME_MASK 0x0000FFFFUL

*/
#ifndef TX_TRACE_TIME_SOURCE
#define TX_TRACE_TIME_SOURCE ++_tx_trace_simulated_time

```

```

#endif
#ifndef TX_TRACE_TIME_MASK
#define TX_TRACE_TIME_MASK          0xFFFFFFFFFUL
#endif

/* Define the port specific options for the _tx_build_options variable. This variable indicates
   how the ThreadX library was built. */

#ifdef TX_ENABLE_FIQ_SUPPORT
#define TX_FIQ_ENABLED               1
#else
#define TX_FIQ_ENABLED               0
#endif

#ifdef TX_ENABLE_IRQ_NESTING
#define TX_IRQ_NESTING_ENABLED      2
#else
#define TX_IRQ_NESTING_ENABLED      0
#endif

#ifdef TX_ENABLE_FIQ_NESTING
#define TX_FIQ_NESTING_ENABLED      4
#else
#define TX_FIQ_NESTING_ENABLED      0
#endif

#define TX_PORT_SPECIFIC_BUILD_OPTIONS    TX_FIQ_ENABLED | TX_IRQ_NESTING_ENABLED |
TX_FIQ_NESTING_ENABLED

/* Define the in-line initialization constant so that modules with in-line
   initialization capabilities can prevent their initialization from being
   a function call. */

#define TX_INLINE_INITIALIZATION

/* Determine whether or not stack checking is enabled. By default, ThreadX stack checking is
   disabled. When the following is defined, ThreadX thread stack checking is enabled. If stack
   checking is enabled (TX_ENABLE_STACK_CHECKING is defined), the TX_DISABLE_STACK_FILLING
   define is negated, thereby forcing the stack fill which is necessary for the stack checking
   logic. */

#ifdef TX_ENABLE_STACK_CHECKING
#undef TX_DISABLE_STACK_FILLING
#endif

/* Define the TX_THREAD control block extensions for this port. The main reason
   for the multiple macros is so that backward compatibility can be maintained with
   existing ThreadX kernel awareness modules. */

#define TX_THREAD_EXTENSION_0
#define TX_THREAD_EXTENSION_1
#define TX_THREAD_EXTENSION_2
#define TX_THREAD_EXTENSION_3

/* Define the port extensions of the remaining ThreadX objects. */

#define TX_BLOCK_POOL_EXTENSION
#define TX_BYTE_POOL_EXTENSION
#define TX_EVENT_FLAGS_GROUP_EXTENSION
#define TX_MUTEX_EXTENSION
#define TX_QUEUE_EXTENSION
#define TX_SEMAPHORE_EXTENSION
#define TX_TIMER_EXTENSION

```

```

/* Define the user extension field of the thread control block. Nothing
   additional is needed for this port so it is defined as white space. */

#ifndef TX_THREAD_USER_EXTENSION
#define TX_THREAD_USER_EXTENSION
#endif

/* Define the macros for processing extensions in tx_thread_create, tx_thread_delete,
   tx_thread_shell_entry, and tx_thread_terminate. */

#define TX_THREAD_CREATE_EXTENSION(thread_ptr)
#define TX_THREAD_DELETE_EXTENSION(thread_ptr)
#define TX_THREAD_COMPLETED_EXTENSION(thread_ptr)
#define TX_THREAD_TERMINATED_EXTENSION(thread_ptr)

/* Define the ThreadX object creation extensions for the remaining objects. */

#define TX_BLOCK_POOL_CREATE_EXTENSION(pool_ptr)
#define TX_BYTE_POOL_CREATE_EXTENSION(pool_ptr)
#define TX_EVENT_FLAGS_GROUP_CREATE_EXTENSION(group_ptr)
#define TX_MUTEX_CREATE_EXTENSION(mutex_ptr)
#define TX_QUEUE_CREATE_EXTENSION(queue_ptr)
#define TX_SEMAPHORE_CREATE_EXTENSION(semaphore_ptr)
#define TX_TIMER_CREATE_EXTENSION(timer_ptr)

/* Define the ThreadX object deletion extensions for the remaining objects. */

#define TX_BLOCK_POOL_DELETE_EXTENSION(pool_ptr)
#define TX_BYTE_POOL_DELETE_EXTENSION(pool_ptr)
#define TX_EVENT_FLAGS_GROUP_DELETE_EXTENSION(group_ptr)
#define TX_MUTEX_DELETE_EXTENSION(mutex_ptr)
#define TX_QUEUE_DELETE_EXTENSION(queue_ptr)
#define TX_SEMAPHORE_DELETE_EXTENSION(semaphore_ptr)
#define TX_TIMER_DELETE_EXTENSION(timer_ptr)

/* Define ThreadX interrupt lockout and restore macros for protection on
   access of critical kernel information. The restore interrupt macro must
   restore the interrupt posture of the running thread prior to the value
   present prior to the disable macro. In most cases, the save area macro
   is used to define a local function save area for the disable and restore
   macros. */

#ifndef __thumb

#define TX_INTERRUPT_SAVE_AREA
register unsigned int interrupt_save, temp;

#ifdef TX_ENABLE_FIQ_SUPPORT
#define TX_DISABLE
    __asm \
    { \
        MRS interrupt_save, CPSR; \
        ORR temp, interrupt_save, 0xC0; \
        MSR CPSR_c, temp \
    }
#else
#define TX_DISABLE
    __asm \
    { \
        MRS interrupt_save, CPSR; \
        ORR temp, interrupt_save, 0x80; \
        MSR CPSR_c, temp \
    }
#endif

#define TX_RESTORE
    __asm \
    { \
        MSR CPSR_c, interrupt_save \
    }

#endif

```

```

#else
unsigned int _tx_thread_interrupt_disable(void);
unsigned int _tx_thread_interrupt_restore(UINT old_posture);

#define TX_INTERRUPT_SAVE_AREA           unsigned int interrupt_save;
#define TX_DISABLE                      interrupt_save = _tx_thread_interrupt_disable();
#define TX_RESTORE                     _tx_thread_interrupt_restore(interrupt_save);
#endif

/* Define the interrupt lockout macros for each ThreadX object. */

#define TX_BLOCK_POOL_DISABLE          TX_DISABLE
#define TX_BYTE_POOL_DISABLE          TX_DISABLE
#define TX_EVENT_FLAGS_GROUP_DISABLE  TX_DISABLE
#define TX_MUTEX_DISABLE              TX_DISABLE
#define TX_QUEUE_DISABLE              TX_DISABLE
#define TX_SEMAPHORE_DISABLE          TX_DISABLE

/* Define the version ID of ThreadX. This may be utilized by the application. */

#ifndef TX_THREAD_INIT
CHAR           _tx_version_id[] =
                "Copyright (c) 1996-YYYY Express Logic Inc. * ThreadX ARM7/RVDS
Version GVVVV.5.0 SN: ZZZZ ";
#else
extern CHAR     _tx_version_id[];
#endif

#endif

```

# B

## ***tx\_trace.h File***

This chapter displays the **tx\_trace.h** file.

```
*****
/* Copyright (c) 1996-2009 by Express Logic Inc. */
/*
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 * Logic, Inc. All rights, title, ownership, or other interests
 * in the software remain the property of Express Logic, Inc. This
 * software may only be used in accordance with the corresponding
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 * distribution, or disclosure of this software is expressly forbidden.
 */
/*
 * This Copyright notice may not be removed or modified without prior
 * written consent of Express Logic, Inc.
 */
/*
 * Express Logic, Inc. reserves the right to modify this software
 * without notice.
 */
/*
 * Express Logic, Inc.          info@expresslogic.com
 * 11423 West Bernardo Court   http://www.expresslogic.com
 * San Diego, CA 92127
 */
*****
```

```
*****
/** ThreadX Component
 */
/** Trace
 */
*****
```

```
*****
/* COMPONENT DEFINITION           RELEASE
 */
/* tx_trace.h                   PORTABLE C
 */
/* AUTHOR
 */
/* William E. Lamie, Express Logic, Inc.
 */
/* DESCRIPTION
 */
/* This file defines the ThreadX trace component, including constants
 * and structure definitions as well as external references. It is
 * assumed that tx_api.h and tx_port.h have already been included.
 */
/* RELEASE HISTORY
 */
/* DATE           NAME            DESCRIPTION
 */
/* 12-12-2005    William E. Lamie  Initial Version 5.0
 */
/* 04-02-2007    William E. Lamie  Modified comment(s),
 *                                resulting in version 5.1
 */
/* 12-12-2008    William E. Lamie  Modified comment(s), added
 *                                new event definitions,
 *                                changed types to ensure the
 *                                trace has universal format,
 *                                optimized event macro, and
 *                                added filter logic and new
 *                                function prototypes,
 *                                resulting in version 5.2
 */
/* 07-04-2009    William E. Lamie  Modified comment(s), removed
 *                                FileX & Netx event IDs since
 *                                they are defined elsewhere,
 *                                and corrected priority
 *                                assignment in event trace,
 *                                resulting in version 5.3
 */
/* 12-12-2009    William E. Lamie  Modified comment(s), added
 *                                defines for default source,
 *                                and added logic to insert
 *                                the thread's preemption-
 *                                threshold along with its
 *                                priority, resulting in
 *                                version 5.4
 */
*****
```

```
/* Include necessary system files. */

#ifndef TX_TRACE_H
#define TX_TRACE_H

/* Determine if tracing is enabled. If not, simply define the in-line trace
 macros to whitespace. */

#ifndef TX_ENABLE_EVENT_TRACE
#define TX_TRACE_INITIALIZE
#define TX_TRACE_OBJECT_REGISTER(t,p,n,a,b)
#define TX_TRACE_OBJECT_UNREGISTER(c)
#define TX_TRACE_IN_LINE_INSERT(i,a,b,c,d,f)
#endif
```

```

/* Event tracing is enabled. */

/* Ensure that the thread component information is included. */

#include "tx_thread.h"

/* Define trace port-specific extension to white space if it isn't defined
already. */

#ifndef TX_TRACE_PORT_EXTENSION
#define TX_TRACE_PORT_EXTENSION
#endif

/* Define the default clock source for trace event entry time stamp. The following two item are port specific.
For example, if the time source is at the address 0x0a800024 and is 16-bits in size, the clock
source constants would be:

#define TX_TRACE_TIME_SOURCE          *((ULONG *) 0x0a800024)
#define TX_TRACE_TIME_MASK            0x0000FFFFUL

*/
#ifndef TX_TRACE_TIME_SOURCE
#define TX_TRACE_TIME_SOURCE          ++_tx_trace_simulated_time
#endif
#ifndef TX_TRACE_TIME_MASK
#define TX_TRACE_TIME_MASK            0xFFFFFFFFUL
#endif

/* Define the ID showing the event trace buffer is valid. */

#define TX_TRACE_VALID               0x54585442UL

/* ThreadX Trace Description. The ThreadX Trace feature is designed to capture
events in real-time in a circular event buffer. This buffer may be analyzed by other
tools. The high-level format of the Trace structure is:

[Trace Control Header           ]
[Trace Object Registry - Entry 0   ]
...
[Trace Object Registry - Entry "n"  ]
[Trace Buffer - Entry 0           ]
...
[Trace Buffer - Entry "n"         ]

*/
/* Trace Control Header. The Trace Control Header contains information that
defines the format of the Trace Object Registry as well as the location and
current entry of the Trace Buffer itself. The high-level format of the
Trace Control Header is:



| Entry                                 | Size | Description                                                                                                                                                                                                                                                                                     |
|---------------------------------------|------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| [Trace ID]                            | 4    | This 4-byte field contains the ThreadX Trace Identification. If the trace buffer is valid, the constants are 0x54585442 (TXTB). Since it is written as a 32-bit value, the endianness of this value is also used to determine if the event trace information is in little or big endian format. |
| [Timer Valid Mask]                    | 4    | Mask of valid bits in the 32-bit time stamp. This enables use of 32, 24, 16, or even 8-bit timers. If the time source is 32-bits, the mask is 0xFFFFFFFF. If the time source is 16-bits, the mask is 0x0000FFFF.                                                                                |
| [Trace Base Address]                  | 4    | The base address for all trace pointer. Subtracting the pointer and this address will yield the proper offset into the trace buffer.                                                                                                                                                            |
| [Trace Object Registry Start Pointer] | 4    | Pointer to the start of Trace Object Registry                                                                                                                                                                                                                                                   |
| [Reserved]                            | 2    | Reserved two bytes - should be 0x0000                                                                                                                                                                                                                                                           |
| [Trace Object Object Name Size]       | 2    | Number of bytes in object name                                                                                                                                                                                                                                                                  |
| [Trace Object Registry End Pointer]   | 4    | Pointer to the end of Trace Object Registry                                                                                                                                                                                                                                                     |
| [Trace Buffer Start Pointer]          | 4    | Pointer to the start of the Trace Buffer Area                                                                                                                                                                                                                                                   |
| [Trace Buffer End Pointer]            | 4    | Pointer to the end of the Trace Buffer Area                                                                                                                                                                                                                                                     |
| [Trace Buffer Current Pointer]        | 4    | Pointer to the oldest entry in the Trace Buffer. This entry will be overwritten on the next event and incremented to the next event (wrapping to the top if the buffer end pointer is exceeded).                                                                                                |
| [Reserved]                            | 4    | Reserved 4 bytes, should be 0xAFFFFFFF                                                                                                                                                                                                                                                          |
| [Reserved]                            | 4    | Reserved 4 bytes, should be 0xBFFFFFFF                                                                                                                                                                                                                                                          |
| [Reserved]                            | 4    | Reserved 4 bytes, should be 0xCFFFFFFF                                                                                                                                                                                                                                                          |



*/
/* Define the Trace Control Header. */

typedef struct TX_TRACE_CONTROL_HEADER_STRUCT
{
    ULONG          tx_trace_control_header_id;
    ULONG          tx_trace_control_header_timer_valid_mask;
    ULONG          tx_trace_control_header_trace_base_address;
    ULONG          tx_trace_control_header_object_registry_start_pointer;
    USHORT         tx_trace_control_header_reserved1;
}

```



```

#define TX_TRACE_ISR_EXIT
#define TX_TRACE_TIME_SLICE
#define TX_TRACE_RUNNING

/* Define the rest of the ThreadX system events. */

#define TX_TRACE_BLOCK_ALLOCATE
#define TX_TRACE_BLOCK_POOL_CREATE
#define TX_TRACE_BLOCK_POOL_DELETE
#define TX_TRACE_BLOCK_POOL_INFO_GET
#define TX_TRACE_BLOCK_POOL_PERFORMANCE_INFO_GET
#define TX_TRACE_BLOCK_POOL_PRIORITIZE
#define TX_TRACE_BLOCK_RELEASE
#define TX_TRACE_BYTE_ALLOCATE
#define TX_TRACE_BYTE_POOL_CREATE
#define TX_TRACE_BYTE_POOL_DELETE
#define TX_TRACE_BYTE_POOL_INFO_GET
#define TX_TRACE_BYTE_POOL_PERFORMANCE_INFO_GET
#define TX_TRACE_BYTE_POOL_PRIORITIZE
#define TX_TRACE_BYTE_RELEASE
#define TX_TRACE_EVENT_FLAGS_CREATE
#define TX_TRACE_EVENT_FLAGS_DELETE
#define TX_TRACE_EVENT_FLAGS_GET
#define TX_TRACE_EVENT_FLAGS_INFO_GET
#define TX_TRACE_EVENT_FLAGS_PERFORMANCE_INFO_GET
#define TX_TRACE_EVENT_FLAGS_SYSTEM_INFO_GET
#define TX_TRACE_INTERRUPT_SET_NOTIFY
#define TX_TRACE_INTERRUPT_CONTROL
#define TX_TRACE_MUTEX_CREATE
#define TX_TRACE_MUTEX_DELETE
#define TX_TRACE_MUTEX_GET
#define TX_TRACE_MUTEX_INFO_GET
#define TX_TRACE_MUTEX_PERFORMANCE_INFO_GET
#define TX_TRACE_MUTEX_SYSTEM_INFO_GET
#define TX_TRACE_MUTEX_PRIORITIZE
#define TX_TRACE_MUTEX_PUT
#define TX_TRACE_QUEUE_CREATE
#define TX_TRACE_QUEUE_DELETE
#define TX_TRACE_QUEUE_FLUSH
#define TX_TRACE_QUEUE_FRONT_SEND
#define TX_TRACE_QUEUE_INFO_GET
#define TX_TRACE_QUEUE_PERFORMANCE_INFO_GET
#define TX_TRACE_QUEUE_SYSTEM_INFO_GET
#define TX_TRACE_QUEUE_PRIORITIZE
#define TX_TRACE_QUEUE_RECEIVE
#define TX_TRACE_QUEUE_SEND
#define TX_TRACE_SEMAPHORE_SET_NOTIFY
#define TX_TRACE_SEMAPHORE_CEILING_PUT
#define TX_TRACE_SEMAPHORE_CREATE
#define TX_TRACE_SEMAPHORE_DELETE
#define TX_TRACE_SEMAPHORE_GET
#define TX_TRACE_SEMAPHORE_INFO_GET
#define TX_TRACE_SEMAPHORE_PERFORMANCE_INFO_GET
#define TX_TRACE_SEMAPHORE_SYSTEM_INFO_GET
#define TX_TRACE_SEMAPHORE_PRIORITIZE
#define TX_TRACE_SEMAPHORE_PUT
#define TX_TRACE_SEMAPHORE_PUT_NOTIFY
#define TX_TRACE_THREAD_CREATE
#define TX_TRACE_THREAD_DELETE
#define TX_TRACE_THREAD_ENTRY_EXIT_NOTIFY
#define TX_TRACE_THREAD_IDENTIFY
#define TX_TRACE_THREAD_INFO_GET
#define TX_TRACE_THREAD_PERFORMANCE_INFO_GET
#define TX_TRACE_THREAD_SYSTEM_INFO_GET
#define TX_TRACE_THREAD_PRIORITIZE
#define TX_TRACE_THREAD_PRIORITY_CHANGE
#define TX_TRACE_THREAD_PREEMPTION_CHANGE
#define TX_TRACE_THREAD_RELINQUISH
#define TX_TRACE_THREAD_RESET
#define TX_TRACE_THREAD_RESUME_API
#define TX_TRACE_THREAD_SLEEP
#define TX_TRACE_THREAD_STACK_ERROR_NOTIFY
#define TX_TRACE_THREAD_SUSPEND_API
#define TX_TRACE_THREAD_TERMINATE
#define TX_TRACE_THREAD_TIME_SLICE_CHANGE
#define TX_TRACE_THREAD_WAIT_ABORT
#define TX_TRACE_TIME_GET
#define TX_TRACE_TIME_SET
#define TX_TRACE_TIMER_ACTIVATE
#define TX_TRACE_TIMER_CHANGE
#define TX_TRACE_TIMER_CREATE
#define TX_TRACE_TIMER_DEACTIVATE
#define TX_TRACE_TIMER_DELETE
#define TX_TRACE_TIMER_INFO_GET
#define TX_TRACE_TIMER_PERFORMANCE_INFO_GET
#define TX_TRACE_TIMER_SYSTEM_INFO_GET

/* Define the an Trace Buffer Entry. */

typedef struct TX_TRACE_BUFFER_ENTRY_STRUCT
{
    ULONG      tx_trace_buffer_entry_thread_pointer;
    ULONG      tx_trace_buffer_entry_thread_priority;
    ULONG      tx_trace_buffer_entry_event_id;
    ULONG      tx_trace_buffer_entry_time;
    ULONG      tx_trace_buffer_entry_ticks;
    ULONG      tx_trace_buffer_entry_ticks2;
    ULONG      tx_trace_buffer_entry_ticks3;
    ULONG      tx_trace_buffer_entry_ticks4;
    ULONG      tx_trace_buffer_entry_ticks5;
    ULONG      tx_trace_buffer_entry_ticks6;
    ULONG      tx_trace_buffer_entry_ticks7;
    ULONG      tx_trace_buffer_entry_ticks8;
    ULONG      tx_trace_buffer_entry_ticks9;
    ULONG      tx_trace_buffer_entry_ticks10;
    ULONG      tx_trace_buffer_entry_ticks11;
    ULONG      tx_trace_buffer_entry_ticks12;
    ULONG      tx_trace_buffer_entry_ticks13;
    ULONG      tx_trace_buffer_entry_ticks14;
    ULONG      tx_trace_buffer_entry_ticks15;
    ULONG      tx_trace_buffer_entry_ticks16;
    ULONG      tx_trace_buffer_entry_ticks17;
    ULONG      tx_trace_buffer_entry_ticks18;
    ULONG      tx_trace_buffer_entry_ticks19;
    ULONG      tx_trace_buffer_entry_ticks20;
    ULONG      tx_trace_buffer_entry_ticks21;
    ULONG      tx_trace_buffer_entry_ticks22;
    ULONG      tx_trace_buffer_entry_ticks23;
    ULONG      tx_trace_buffer_entry_ticks24;
    ULONG      tx_trace_buffer_entry_ticks25;
    ULONG      tx_trace_buffer_entry_ticks26;
    ULONG      tx_trace_buffer_entry_ticks27;
    ULONG      tx_trace_buffer_entry_ticks28;
    ULONG      tx_trace_buffer_entry_ticks29;
    ULONG      tx_trace_buffer_entry_ticks30;
    ULONG      tx_trace_buffer_entry_ticks31;
    ULONG      tx_trace_buffer_entry_ticks32;
    ULONG      tx_trace_buffer_entry_ticks33;
    ULONG      tx_trace_buffer_entry_ticks34;
    ULONG      tx_trace_buffer_entry_ticks35;
    ULONG      tx_trace_buffer_entry_ticks36;
    ULONG      tx_trace_buffer_entry_ticks37;
    ULONG      tx_trace_buffer_entry_ticks38;
    ULONG      tx_trace_buffer_entry_ticks39;
    ULONG      tx_trace_buffer_entry_ticks40;
    ULONG      tx_trace_buffer_entry_ticks41;
    ULONG      tx_trace_buffer_entry_ticks42;
    ULONG      tx_trace_buffer_entry_ticks43;
    ULONG      tx_trace_buffer_entry_ticks44;
    ULONG      tx_trace_buffer_entry_ticks45;
    ULONG      tx_trace_buffer_entry_ticks46;
    ULONG      tx_trace_buffer_entry_ticks47;
    ULONG      tx_trace_buffer_entry_ticks48;
    ULONG      tx_trace_buffer_entry_ticks49;
    ULONG      tx_trace_buffer_entry_ticks50;
    ULONG      tx_trace_buffer_entry_ticks51;
    ULONG      tx_trace_buffer_entry_ticks52;
    ULONG      tx_trace_buffer_entry_ticks53;
    ULONG      tx_trace_buffer_entry_ticks54;
    ULONG      tx_trace_buffer_entry_ticks55;
    ULONG      tx_trace_buffer_entry_ticks56;
    ULONG      tx_trace_buffer_entry_ticks57;
    ULONG      tx_trace_buffer_entry_ticks58;
    ULONG      tx_trace_buffer_entry_ticks59;
    ULONG      tx_trace_buffer_entry_ticks60;
    ULONG      tx_trace_buffer_entry_ticks61;
    ULONG      tx_trace_buffer_entry_ticks62;
    ULONG      tx_trace_buffer_entry_ticks63;
    ULONG      tx_trace_buffer_entry_ticks64;
    ULONG      tx_trace_buffer_entry_ticks65;
    ULONG      tx_trace_buffer_entry_ticks66;
    ULONG      tx_trace_buffer_entry_ticks67;
    ULONG      tx_trace_buffer_entry_ticks68;
    ULONG      tx_trace_buffer_entry_ticks69;
    ULONG      tx_trace_buffer_entry_ticks70;
    ULONG      tx_trace_buffer_entry_ticks71;
    ULONG      tx_trace_buffer_entry_ticks72;
    ULONG      tx_trace_buffer_entry_ticks73;
    ULONG      tx_trace_buffer_entry_ticks74;
    ULONG      tx_trace_buffer_entry_ticks75;
    ULONG      tx_trace_buffer_entry_ticks76;
    ULONG      tx_trace_buffer_entry_ticks77;
    ULONG      tx_trace_buffer_entry_ticks78;
    ULONG      tx_trace_buffer_entry_ticks79;
    ULONG      tx_trace_buffer_entry_ticks80;
    ULONG      tx_trace_buffer_entry_ticks81;
    ULONG      tx_trace_buffer_entry_ticks82;
    ULONG      tx_trace_buffer_entry_ticks83;
    ULONG      tx_trace_buffer_entry_ticks84;
    ULONG      tx_trace_buffer_entry_ticks85;
    ULONG      tx_trace_buffer_entry_ticks86;
    ULONG      tx_trace_buffer_entry_ticks87;
    ULONG      tx_trace_buffer_entry_ticks88;
    ULONG      tx_trace_buffer_entry_ticks89;
    ULONG      tx_trace_buffer_entry_ticks90;
    ULONG      tx_trace_buffer_entry_ticks91;
    ULONG      tx_trace_buffer_entry_ticks92;
    ULONG      tx_trace_buffer_entry_ticks93;
    ULONG      tx_trace_buffer_entry_ticks94;
    ULONG      tx_trace_buffer_entry_ticks95;
    ULONG      tx_trace_buffer_entry_ticks96;
    ULONG      tx_trace_buffer_entry_ticks97;
    ULONG      tx_trace_buffer_entry_ticks98;
    ULONG      tx_trace_buffer_entry_ticks99;
    ULONG      tx_trace_buffer_entry_ticks100;
    ULONG      tx_trace_buffer_entry_ticks101;
    ULONG      tx_trace_buffer_entry_ticks102;
    ULONG      tx_trace_buffer_entry_ticks103;
    ULONG      tx_trace_buffer_entry_ticks104;
    ULONG      tx_trace_buffer_entry_ticks105;
    ULONG      tx_trace_buffer_entry_ticks106;
    ULONG      tx_trace_buffer_entry_ticks107;
    ULONG      tx_trace_buffer_entry_ticks108;
    ULONG      tx_trace_buffer_entry_ticks109;
    ULONG      tx_trace_buffer_entry_ticks110;
    ULONG      tx_trace_buffer_entry_ticks111;
    ULONG      tx_trace_buffer_entry_ticks112;
    ULONG      tx_trace_buffer_entry_ticks113;
    ULONG      tx_trace_buffer_entry_ticks114;
    ULONG      tx_trace_buffer_entry_ticks115;
    ULONG      tx_trace_buffer_entry_ticks116;
    ULONG      tx_trace_buffer_entry_ticks117;
    ULONG      tx_trace_buffer_entry_ticks118;
    ULONG      tx_trace_buffer_entry_ticks119;
    ULONG      tx_trace_buffer_entry_ticks120;
    ULONG      tx_trace_buffer_entry_ticks121;
    ULONG      tx_trace_buffer_entry_ticks122;
    ULONG      tx_trace_buffer_entry_ticks123;
    ULONG      tx_trace_buffer_entry_ticks124;
    ULONG      tx_trace_buffer_entry_ticks125;
    ULONG      tx_trace_buffer_entry_ticks126;
    ULONG      tx_trace_buffer_entry_ticks127;
    ULONG      tx_trace_buffer_entry_ticks128;
    ULONG      tx_trace_buffer_entry_ticks129;
}

```

```

    ULONG          tx_trace_buffer_entry_time_stamp;
    ULONG          tx_trace_buffer_entry_information_field_1;
    ULONG          tx_trace_buffer_entry_information_field_2;
    ULONG          tx_trace_buffer_entry_information_field_3;
    ULONG          tx_trace_buffer_entry_information_field_4;

) TX_TRACE_BUFFER_ENTRY;

/* Trace management component data declarations follow. */

/* Determine if the initialization function of this component is including
   this file. If so, make the data definitions really happen. Otherwise,
   make them extern so other functions in the component can access them. */

#ifndef TX_TRACE_INIT
#define TRACE_DECLARE
#else
#define TRACE_DECLARE extern
#endif

/* Define the pointer to the start of the trace buffer control structure. */
TRACE_DECLARE TX_TRACE_CONTROL_HEADER      *_tx_trace_control_header_ptr;

/* Define the pointer to the start of the trace object registry area in the trace buffer. */
TRACE_DECLARE TX_TRACE_OBJECT_REGISTRY_ENTRY *_tx_trace_registry_start_ptr;

/* Define the pointer to the end of the trace object registry area in the trace buffer. */
TRACE_DECLARE TX_TRACE_OBJECT_REGISTRY_ENTRY *_tx_trace_registry_end_ptr;

/* Define the pointer to the starting entry of the actual trace event area of the trace buffer. */
TRACE_DECLARE TX_TRACE_BUFFER_ENTRY         *_tx_trace_buffer_start_ptr;

/* Define the pointer to the ending entry of the actual trace event area of the trace buffer. */
TRACE_DECLARE TX_TRACE_BUFFER_ENTRY         *_tx_trace_buffer_end_ptr;

/* Define the pointer to the current entry of the actual trace event area of the trace buffer. */
TRACE_DECLARE TX_TRACE_BUFFER_ENTRY         *_tx_trace_buffer_current_ptr;

/* Define the trace event enable bits, where each bit represents a type of event that can be enabled
   or disabled dynamically by the application. */
TRACE_DECLARE ULONG                         _tx_trace_event_enable_bits;

/* Define a counter that is used in environments that don't have a timer source. This counter
   is incremented on each use giving each event a unique timestamp. */
TRACE_DECLARE ULONG                         _tx_trace_simulated_time;

/* Define the function pointer used to call the application when the trace buffer wraps. If NULL,
   the application has not registered a callback function. */
TRACE_DECLARE VOID                          (*_tx_trace_full_notify_function)(VOID *);

/* Define the event trace macros that are expanded in-line when event tracing is enabled. */

#define TX_TRACE_INITIALIZE                  _tx_trace_initialize();
#define TX_TRACE_OBJECT_REGISTER(t,p,n,a,b)  _tx_trace_object_register(t, (VOID *) p, (CHAR *) n, (ULONG) a, (ULONG) b);
#define TX_TRACE_OBJECT_UNREGISTER(o)        _tx_trace_object_unregister((VOID *) o);
#define TX_TRACE_IN_LINE_INSERT(i,a,b,c,d,e) \
( \
    TX_TRACE_BUFFER_ENTRY     *trace_event_ptr; \
    ULONG                     trace_system_state; \
    ULONG                     trace_priority; \
    TX_THREAD                 *trace_thread_ptr; \
    trace_event_ptr = _tx_trace_buffer_current_ptr; \
    if ((trace_event_ptr) && (_tx_trace_event_enable_bits & ((ULONG) (e)))) \
    { \
        TX_TRACE_PORT_EXTENSION \
        trace_system_state = (ULONG) _tx_thread_system_state; \
        trace_thread_ptr = _tx_thread_current_ptr; \
    } \
    if (trace_system_state == 0) \
    { \
        trace_priority = trace_thread_ptr -> tx_thread_priority; \
        trace_priority = trace_priority | 0x80000000 | (trace_thread_ptr -> tx_thread_preempt_threshold << 16); \
    } \
    else if (trace_system_state < 0xF0F0F0F0UL) \
    { \
        trace_priority = (ULONG) trace_thread_ptr; \
        trace_thread_ptr = (TX_THREAD *) 0xFFFFFFFFFUL; \
    } \
)

```

```

    } \
else \
{
    trace_thread_ptr = (TX_THREAD *) 0xF0F0F0F0UL;
    trace_priority = 0; \
} \
trace_event_ptr -> tx_trace_buffer_entry_thread_pointer = (ULONG) trace_thread_ptr; \
trace_event_ptr -> tx_trace_buffer_entry_thread_priority = (ULONG) trace_priority; \
trace_event_ptr -> tx_trace_buffer_entry_event_id = (ULONG) (i); \
trace_event_ptr -> tx_trace_buffer_entry_time_stamp = (ULONG) TX_TRACE_TIME_SOURCE; \
trace_event_ptr -> tx_trace_buffer_entry_information_field_1 = (ULONG) (a); \
trace_event_ptr -> tx_trace_buffer_entry_information_field_2 = (ULONG) (b); \
trace_event_ptr -> tx_trace_buffer_entry_information_field_3 = (ULONG) (c); \
trace_event_ptr -> tx_trace_buffer_entry_information_field_4 = (ULONG) (d); \
trace_event_ptr++;
if (trace_event_ptr >= _tx_trace_buffer_end_ptr) \
{
    trace_event_ptr = _tx_trace_buffer_start_ptr; \
    _tx_trace_buffer_current_ptr = trace_event_ptr; \
    _tx_trace_control_header_ptr -> tx_trace_control_header_buffer_current_pointer = (ULONG) trace_event_ptr; \
    if (_tx_trace_full_notify_function) \
        (_tx_trace_full_notify_function)((VOID *) _tx_trace_control_header_ptr); \
} \
else \
{
    _tx_trace_buffer_current_ptr = trace_event_ptr; \
    _tx_trace_control_header_ptr -> tx_trace_control_header_buffer_current_pointer = (ULONG) trace_event_ptr; \
} \
}
}

#endif

/* Define function prototypes of the trace component. */

UINT _tx_trace_enable(VOID *trace_buffer_start, ULONG trace_buffer_size, ULONG registry_entries);
UINT _tx_trace_event_filter(ULONG event_filter_bits);
UINT _tx_trace_object_filter(ULONG event_filter_bits);
UINT _tx_trace_disable(VOID);
VOID _tx_trace_initialize(VOID);
UINT _tx_trace_interrupt_control(UINT new_posture);
VOID _tx_trace_isr_enter_insert(ULONG isr_id);
VOID _tx_trace_isr_exit_insert(ULONG isr_id);
VOID _tx_trace_object_register(UCHAR object_type, VOID *object_ptr, CHAR *object_name, ULONG parameter_1, ULONG parameter_2);
VOID _tx_trace_object_unregister(VOID *object_ptr);
UINT _tx_trace_buffer_full_notify(VOID (*full_buffer_callback)(VOID *));
UINT _tx_trace_user_event_insert(ULONG event_id, ULONG info_field_1, ULONG info_field_2, ULONG info_field_3, ULONG info_field_4);

#endif

```



# C

## **DOS Command Line Utilities**

There are three DOS command line utilities found in the TraceX installation under the **Utilities** sub-directory. They are described on the following page.

- ea2tracex.exe
- hex2tracex.exe
- mot2tracex.exe

The utilities supplied are listed below::

<b>Utility</b>	<b>Purpose</b>	<b>Command Line Definitions</b>
<b>ea2tracex.exe</b>	<p>Converts the trace file generated by ThreadX in association with the GHS tools to the TraceX trace file format.</p> <p>Note: ThreadX for GHS tools produces a different trace format than ThreadX for non-GHS tools, which is why this conversion utility is needed.</p>	> <b>ea2tracex</b> original_file converted_file <cr>
<b>hex2tracex.exe</b>	Converts a trace file generated by ThreadX but dumped from the development tool in Intel HEX format to the binary TraceX trace file format. Note that TraceX V5 and above can open HEX files without converting them.	> <b>hex2tracex</b> hex_file converted_file <cr>
<b>mot2tracex.exe</b>	Converts a trace file generated by ThreadX but dumped from the development tool in Motorola S-Record format to the binary TraceX trace file format. Note that TraceX V5 and above can open S-Record files without converting them.	> <b>mot2tracex</b> mot_file converted_file <cr>

# D

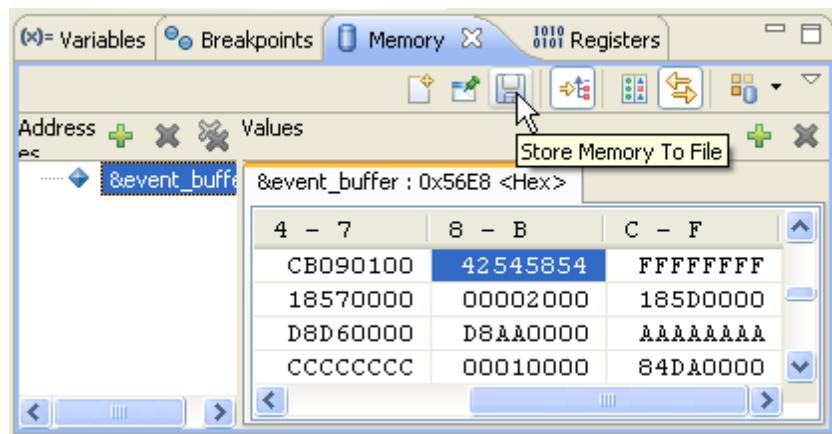
## *Dumping the Trace Buffer*

Dumping the trace buffer created by ThreadX to a file on the host computer is done via commands and/or utilities provided by the specific development tool being used. This appendix contains examples of dumping a trace buffer to a host file in some of the more popular development tools used with ThreadX.

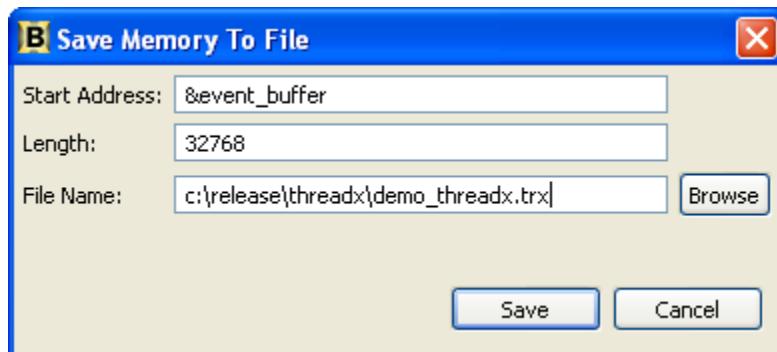
- BenchX Tools 258
- RealView Tools 259
- IAR Tools 259
- CodeWarrior Tools 260
- MPLAB Tools 261
- GHS Tools 267
- Renesas HEW 268

## BenchX Tools

The trace buffer can be dumped to a host file easily with the BenchX tools by selecting the **Store Memory To File** button within the **Memory View**, as shown below:



At this point, specify the trace buffer address, size, destination file name (including path), and select the **Save** button as shown below. This will create the binary trace file for viewing within TraceX.



## RealView Tools

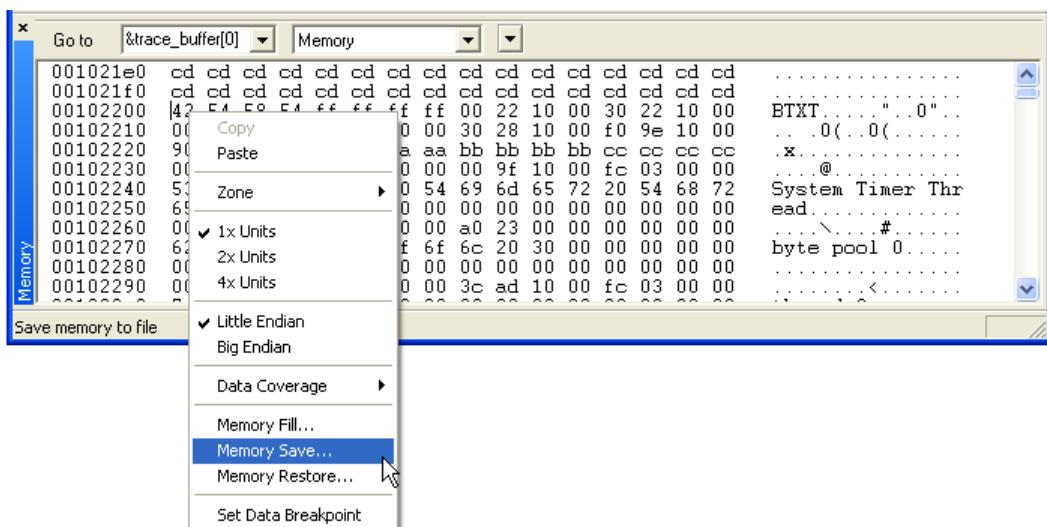
The trace buffer can be dumped to a host file easily with the ARM RealView tools by entering the following command at the command line prompt in RealView:

```
> WRITEFILE,raw trace_file.trx=0x6860..0xE560
```

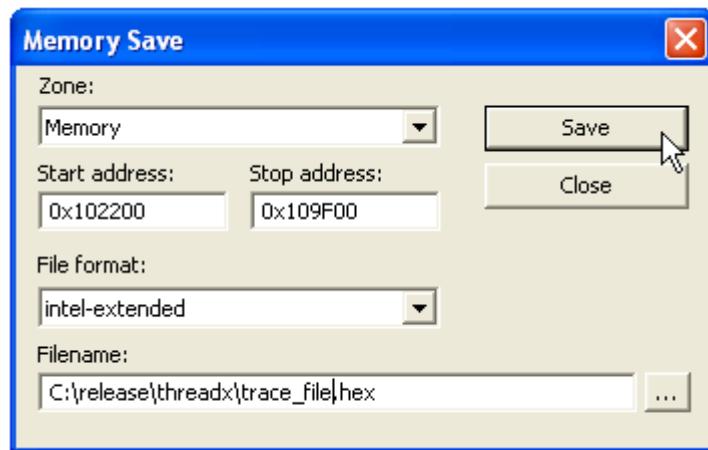
Upon completion, the file **trace\_file.trx** will contain the trace buffer that is located starting at the address 0x6860 and goes up to address 0xE560. This file is ready for viewing by TraceX.

## IAR Tools

The trace buffer can be dumped to a host file very easily with the IAR tools by simply right clicking in the memory view and selecting the **Memory Save...** option, as shown below.



This results in the **Memory Save** dialog to be displayed. Enter the starting and ending address and the trace file name, then select the **Save** button. In the example shown below, the IAR tools save the specified trace buffer into Intel HEX records in the file **trace\_file.hex**.



At this point, we have the trace buffer saved in the **trace\_file.hex** file on the host and is ready for viewing with TraceX.

## CodeWarrior Tools

The trace buffer can be dumped to a host file easily with the CodeWarrior tools by entering the **save** command in the Command Window. The following example **save** command assumes the trace buffer starts at 0x102200 and ends at 0x109F00:

```
> save -b p:0x102200..0x109F00 trace_file.trx -a 32bit
```

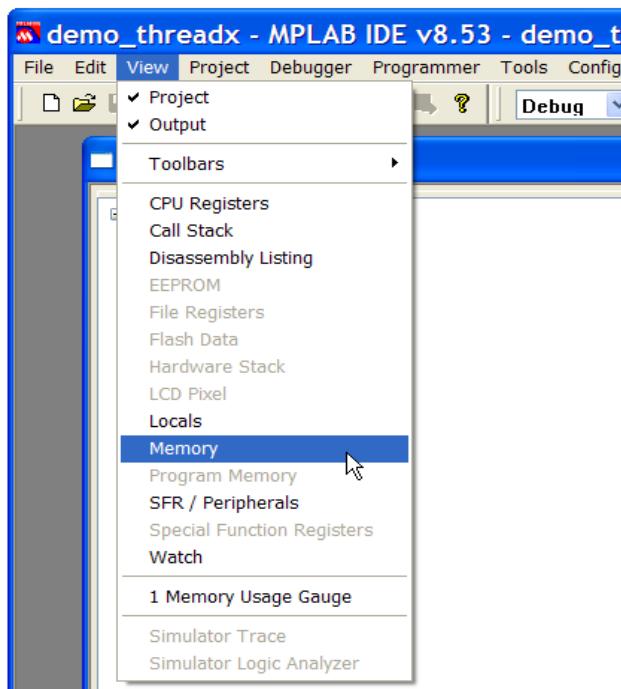
This results in the trace buffer being saved in the file **trace\_file.trx** on the host.

## MPLAB Tools

MPLAB can create a TraceX-compatible trace file through its Export Table utility, which allows the export of any range of memory to a host file. To use this utility to create a trace file for TraceX, proceed as follows:

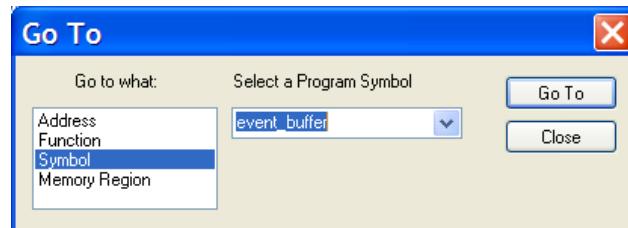
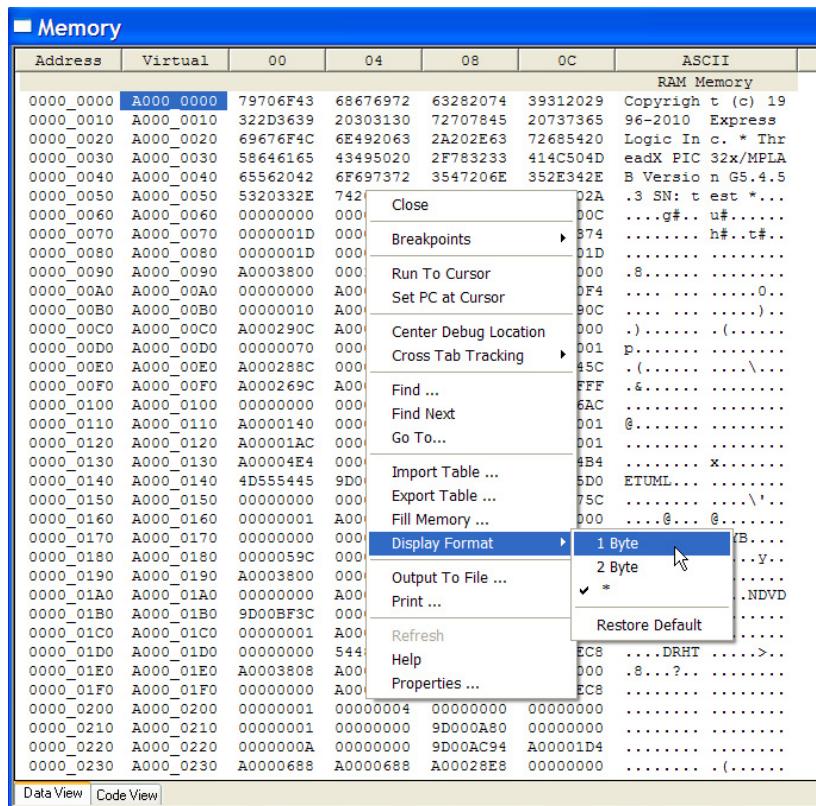
### Step 1

Open a memory window by selecting View → Memory.



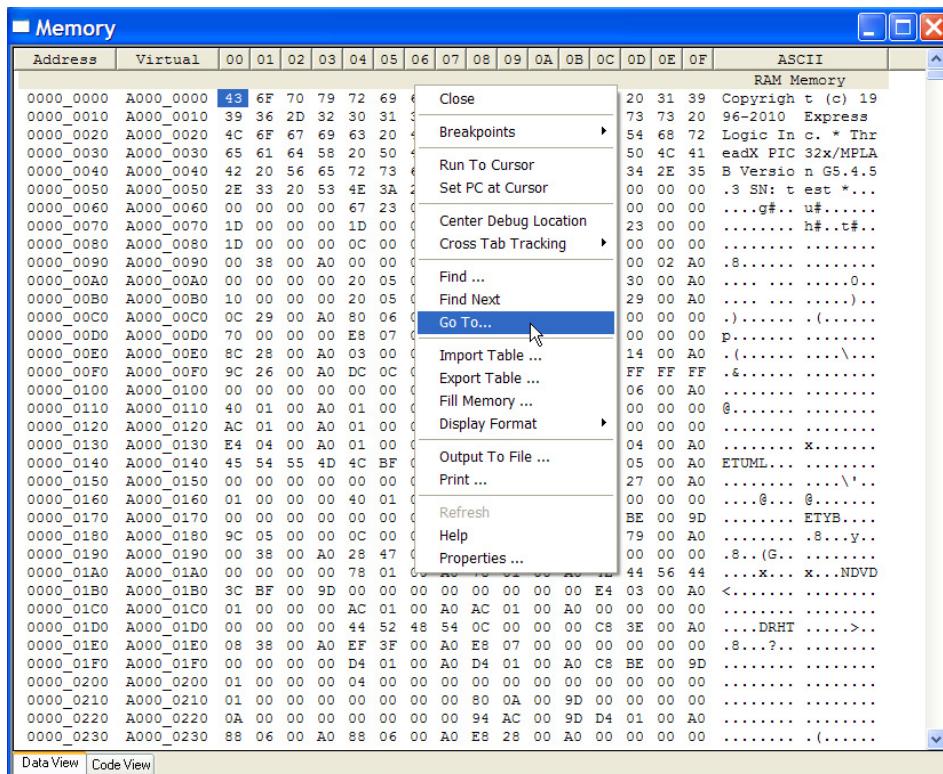
**Step 2**

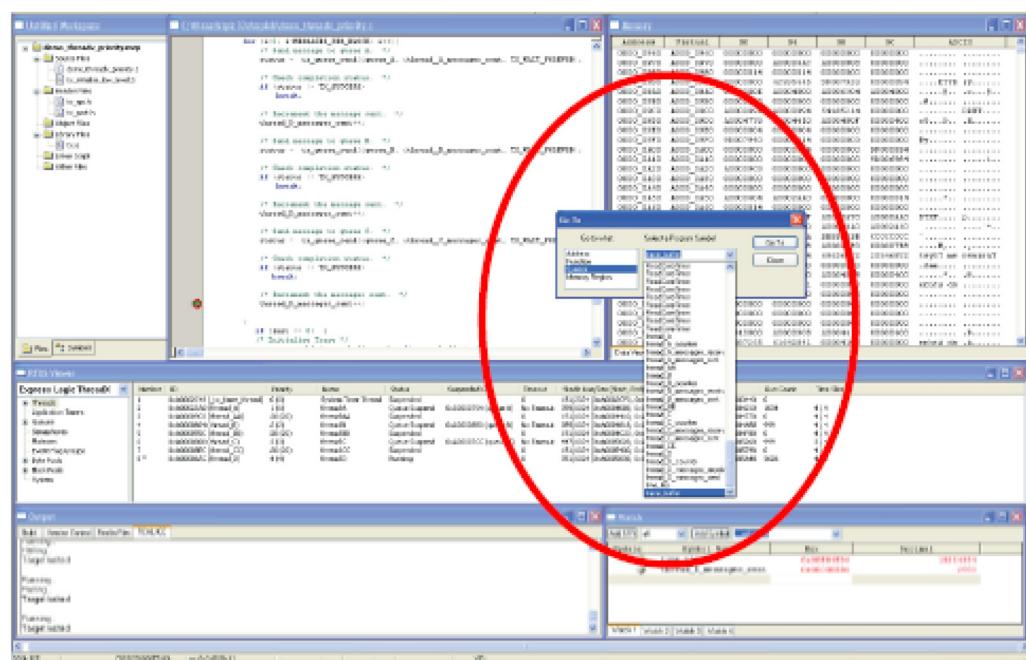
Right-click within the **Memory View** to display a list of options. Specify **Display Format -> 1 Byte** to select byte display..



### Step 3

Right-click again within the **Memory View** Window and select **Go To**, which opens a dialog box that enables you to specify the address of the event buffer. This example shows **event\_buffer** being displayed.





**Step 4**

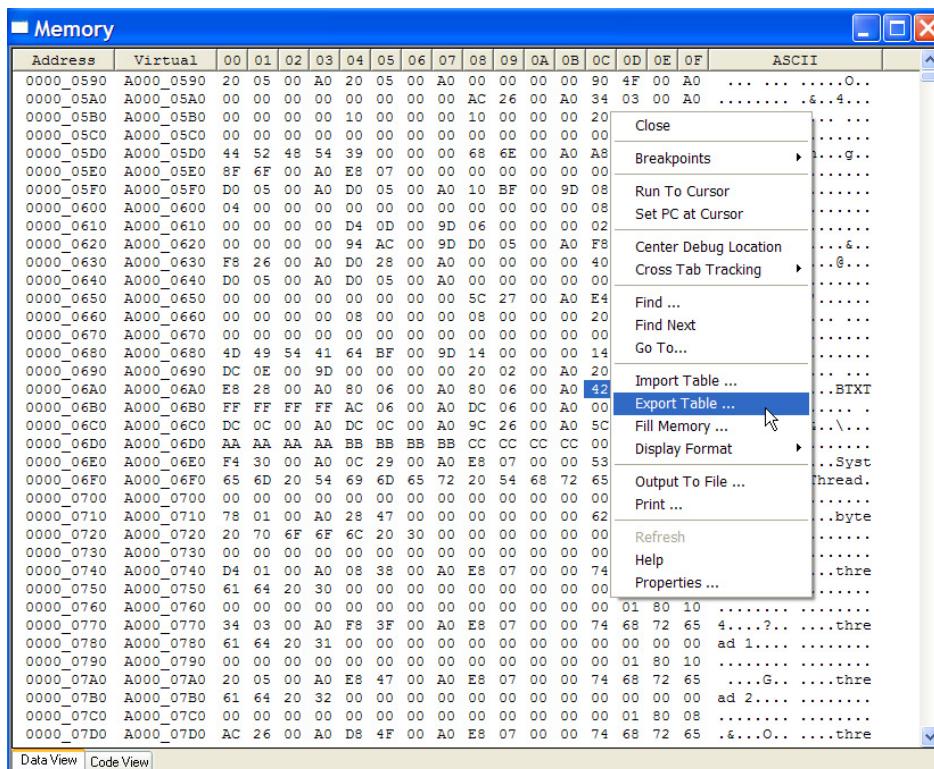
This highlights the contents of the first location of the trace buffer, which is always the string BTXT....

Address	Virtual	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	ASCII
0000_0590	A000_0590	20	05	00	A0	20	05	00	A0	00	00	00	90	4F	00	A0	.....	.....O..
0000_05A0	A000_05A0	00	00	00	00	00	00	00	AC	26	00	A0	34	03	00	A0	.....	.....4..
0000_05B0	A000_05B0	00	00	00	00	10	00	00	00	10	00	00	20	00	00	00	00	.....
0000_05C0	A000_05C0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....
0000_05D0	A000_05D0	44	52	48	54	39	00	00	00	68	6E	00	A0	A8	67	00	A0	DRHT9... hn...g..
0000_05E0	A000_05E0	8F	6F	00	A0	E8	07	00	00	00	00	00	00	00	00	00	00	.....
0000_05F0	A000_05F0	D0	05	00	A0	D0	05	00	A0	10	BF	00	9D	08	00	00	00	.....
0000_0600	A000_0600	04	00	00	00	00	00	00	00	00	00	00	08	00	00	00	00	.....
0000_0610	A000_0610	00	00	00	00	D4	0D	00	9D	06	00	00	02	00	00	00	00	.....
0000_0620	A000_0620	00	00	00	00	94	AC	00	9D	D0	05	00	A0	F8	26	00	A0	.....
0000_0630	A000_0630	F8	26	00	A0	D0	28	00	A0	00	00	00	40	01	00	A0	.....	.....6..
0000_0640	A000_0640	D0	05	00	A0	D0	05	00	A0	00	00	00	00	00	00	00	00	.....
0000_0650	A000_0650	00	00	00	00	00	00	00	00	5C	27	00	A0	E4	03	00	A0	.....
0000_0660	A000_0660	00	00	00	08	00	00	00	08	00	00	00	20	00	00	00	00	.....
0000_0670	A000_0670	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....
0000_0680	A000_0680	4D	49	54	41	64	BF	00	9D	14	00	00	14	00	00	00	MITAd...	.....
0000_0690	A000_0690	DC	0E	00	9D	00	00	00	20	02	00	A0	20	02	00	A0	.....	.....
0000_06A0	A000_06A0	E8	28	00	A0	80	06	00	A0	80	06	00	A0	42	54	58	54	.(.....BTXT
0000_06B0	A000_06B0	FF	FF	FF	AC	06	00	A0	DC	06	00	A0	00	00	20	00	00	.....
0000_06C0	A000_06C0	DC	0C	00	A0	DC	0C	00	A0	9C	26	00	A0	5C	14	00	A0	.....
0000_06D0	A000_06D0	AA	AA	AA	BB	BB	BB	BB	CC	CC	CC	00	01	80	00	00	00	.....
0000_06E0	A000_06E0	F4	30	00	A0	0C	29	00	A0	E8	07	00	00	53	79	73	74	.0...)...Syst
0000_06F0	A000_06F0	65	6D	20	54	69	6D	65	72	20	54	68	72	65	61	64	00	em Timer Thread.
0000_0700	A000_0700	00	00	00	00	00	00	00	00	00	00	00	00	00	08	00	00	.....
0000_0710	A000_0710	78	01	00	A0	28	47	00	00	00	00	00	62	79	74	65	x...(G.. byte	.....
0000_0720	A000_0720	20	70	6F	6F	6C	20	30	00	00	00	00	00	00	00	00	00	pool 0.
0000_0730	A000_0730	00	00	00	00	00	00	00	00	00	00	00	00	01	80	01	00	.....
0000_0740	A000_0740	D4	01	00	A0	08	38	00	A0	E8	07	00	00	74	68	72	65	.....8.. thre
0000_0750	A000_0750	61	64	20	30	00	00	00	00	00	00	00	00	00	00	00	00	ad 0....
0000_0760	A000_0760	00	00	00	00	00	00	00	00	00	00	00	00	01	80	10	00	.....
0000_0770	A000_0770	34	03	00	A0	F8	3F	00	A0	E8	07	00	00	74	68	72	65	4....?.. thre
0000_0780	A000_0780	61	64	20	31	00	00	00	00	00	00	00	00	00	00	00	00	ad 1....
0000_0790	A000_0790	00	00	00	00	00	00	00	00	00	00	00	00	01	80	10	00	.....
0000_07A0	A000_07A0	20	05	00	A0	E8	47	00	A0	E8	07	00	00	74	68	72	65	....G.. thre
0000_07B0	A000_07B0	61	64	20	32	00	00	00	00	00	00	00	00	00	00	00	00	ad 2....
0000_07C0	A000_07C0	00	00	00	00	00	00	00	00	00	00	00	00	01	80	08	00	.....
0000_07D0	A000_07D0	AC	26	00	A0	D8	4F	00	A0	E8	07	00	00	74	68	72	65	....O.. thre

Data View | Code View

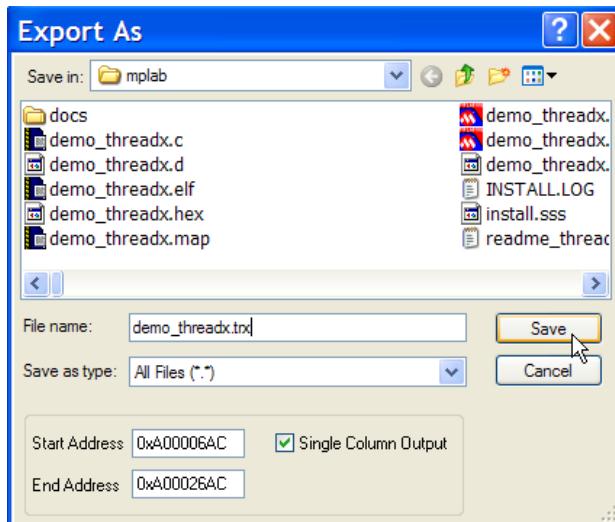
**Step 5**

Now, right-click again to bring up the options menu, and select **Export Table**.

**Step 6**

This brings up the **Export Table** dialog, as shown. Specify the range of addresses to export. For an 8K trace buffer, as is the case in this example, specify the range 0xA00006AC to 0xA00026AC, and enter a

name for the host file to be created (demo\_threadx.trx in this example).



## Step 7

A file named **demo\_threadx.trx** will be created on the host, and this file can be opened by TraceX.

# GHS Tools

The trace buffer can be dumped to a host file easily with the GHS tools by entering the following command at the command line prompt in the debug command window:

```
memdump raw c:\release\threadx\demo_threadx.trx event_buffer 32768
```

Upon completion, the file **demo\_threadx.trx** will contain the trace buffer that is located in the event\_buffer with a size of 32,768 bytes and is ready for viewing by TraceX.

## Renesas HEW

The trace buffer can be dumped to a host file easily with the Renesas HEW tools by following the three steps (and sub-steps) below:

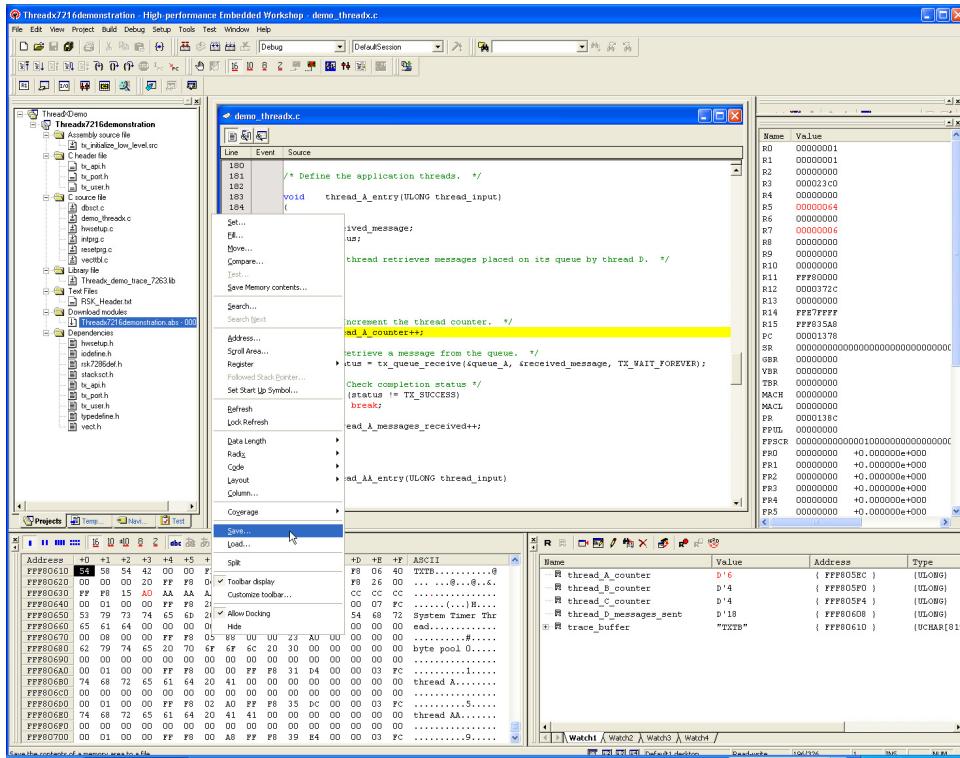
### Step 1

Open Memory Window.

Address	+0	+1	+2	+3	+4	+5	+6	+7	+8	+9	+A	+B	+C	+D	+E	+F	ASCII
FFF80610	54	58	54	42	00	00	FF	FF	FF	F8	06	10	FF	F8	06	40	TXTB.....@
FFF80620	00	00	00	20	FF	F8	0C	40	FF	F8	0C	40	FF	F8	26	00	....@...@..&
FFF80630	FF	F8	15	A0	AA	AA	AA	BB	BB	BB	CC	CC	CC	CC	CC	CC	.....
FFF80640	00	01	00	00	FF	F8	28	94	FF	F8	29	48	00	00	07	FC	.....(...)H....
FFF80650	53	79	73	74	65	6D	20	54	69	6D	65	72	20	54	68	72	System Timer Thr
FFF80660	65	61	64	00	00	00	00	00	00	00	00	00	00	00	00	00	ead.....
FFF80670	00	08	00	00	FF	F8	05	88	00	00	23	A0	00	00	00	00	.....#....
FFF80680	62	79	74	65	20	70	6F	6F	6C	20	30	00	00	00	00	00	byte pool 0....
FFF80690	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....
FFF806A0	00	01	00	00	FF	F8	00	00	FF	F8	31	D4	00	00	03	FC	.....1....
FFF806B0	74	68	72	65	61	64	20	41	00	00	00	00	00	00	00	00	thread A.....
FFF806C0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....
FFF806D0	00	01	00	00	FF	F8	02	A0	FF	F8	35	DC	00	00	03	FC	.....5....
FFF806E0	74	68	72	65	61	64	20	41	00	00	00	00	00	00	00	00	thread AA.....
FFF806F0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....
FFF80700	00	01	00	00	FF	F8	00	A8	FF	F8	39	E4	00	00	03	FC	.....9....

## Step 2

Place cursor within memory window and right click.

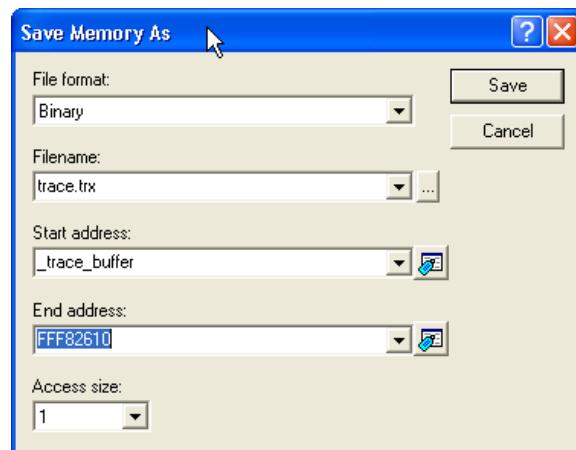


## Step 3

Select Save, then in the Save Memory As window do the following:

- Select File format: Binary.
- Specify Filename: As Desired
- Specify Start address: trace\_buffer
- Specify End address: (trace\_buffer+size)
- Specify Access size: 1

- Click Save



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Renesas Electronics Corporation