uart_core

parameter CLOCK_FREQ

parameter BAUD_RATE

uart_tx

parameter CLOCK_FREQ

parameter BAUD_RATE

data_in 8

data_in_valid

clk

rst

data_in_ready

serial_out

D Q

clk

rst

serial_out

data_in 8

data_in_valid

data_out_ready

serial_in

clk

rst

uart_rx

parameter CLOCK_FREQ

parameter BAUD_RATE

serial_in

data_out_ready

clk

rst

data_out_valid

data_out 8

serial_in

D Q

clk

rst

data_in_ready

data_out 8

data_out_valid

serial_out

Title:	uart_core.v
Project:	AMA-RISCV
Drawn by:	Aleksandar Lilic
Version:	1.0.0
Date:	2021-06-19	Page:	1 out of 1