

D

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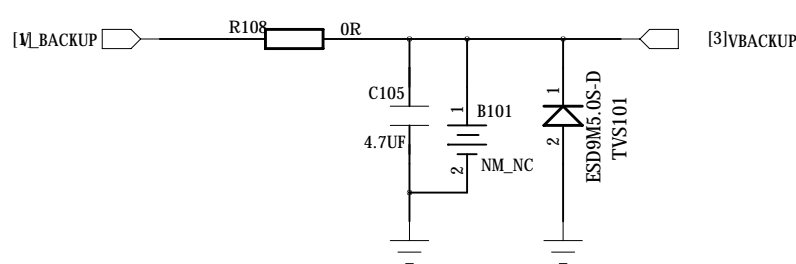
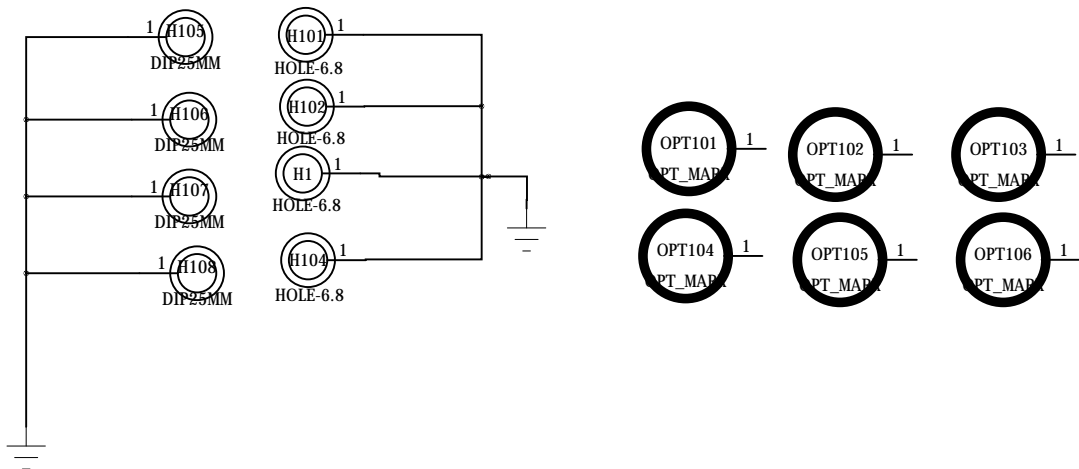
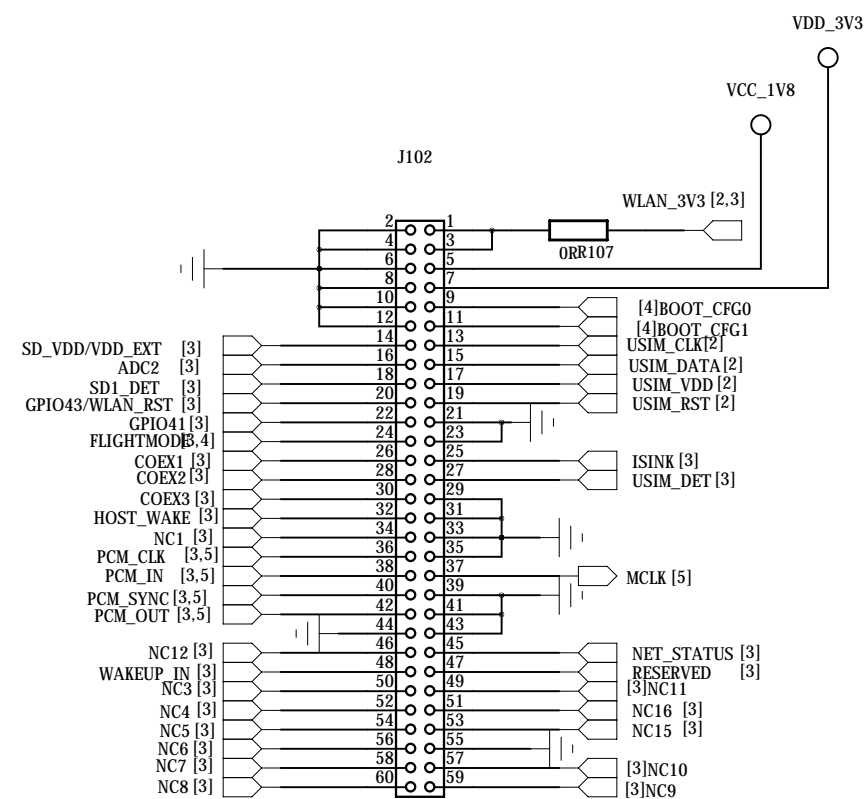
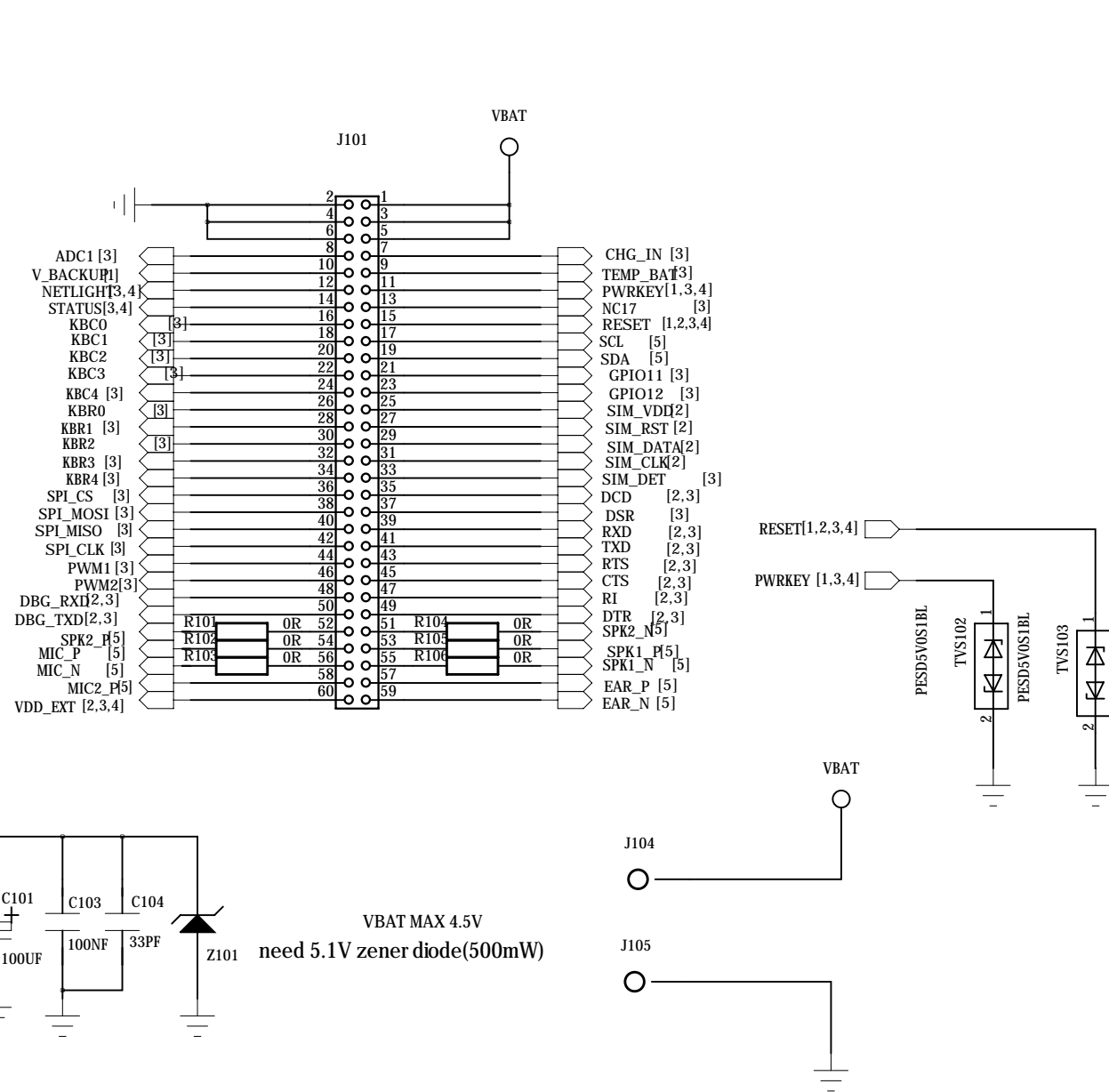
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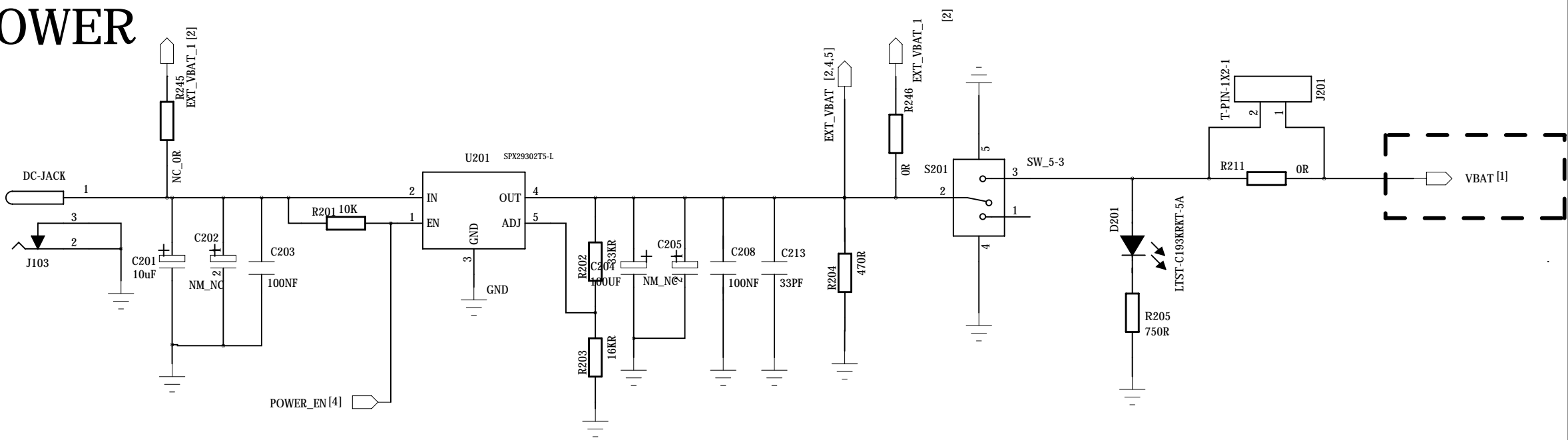
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1

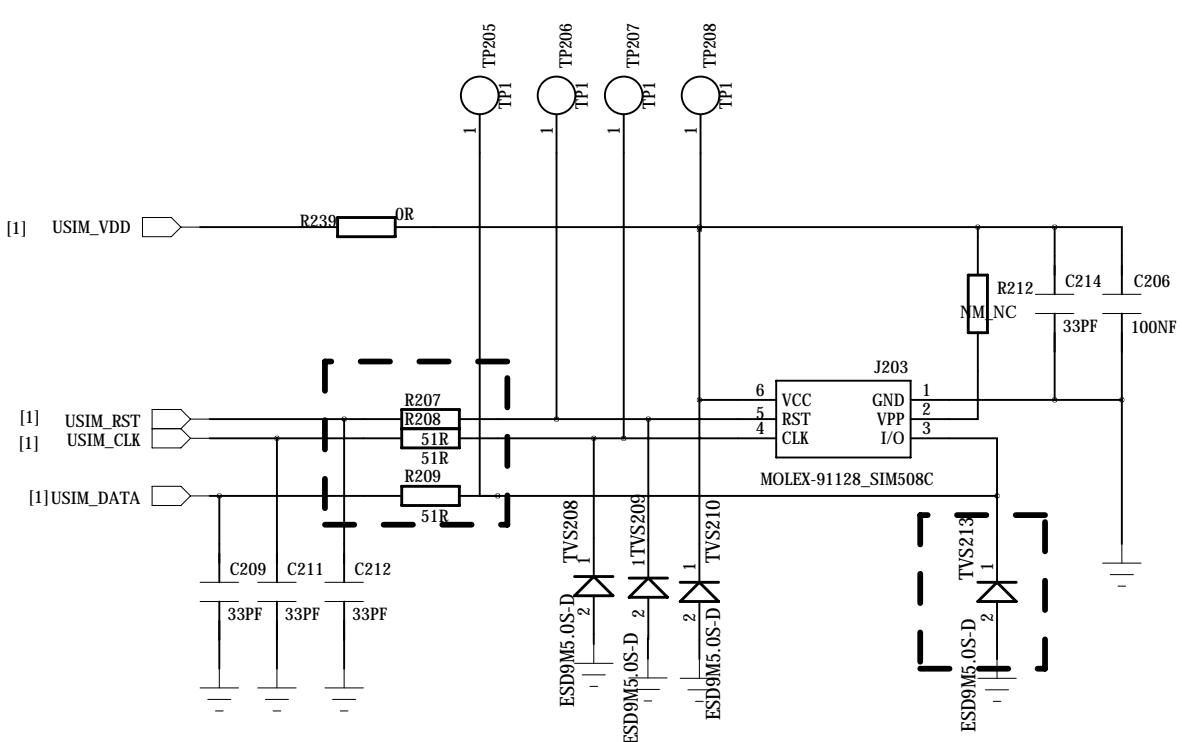


SIM Technology		
DRAWN BY <NAME HERE>	PORJECT W57-EVB	TITLE
CHECKED BY <NAME HERE>	SIZE A2	VER V1.01
SHEET 1 of 5		2016-10-28

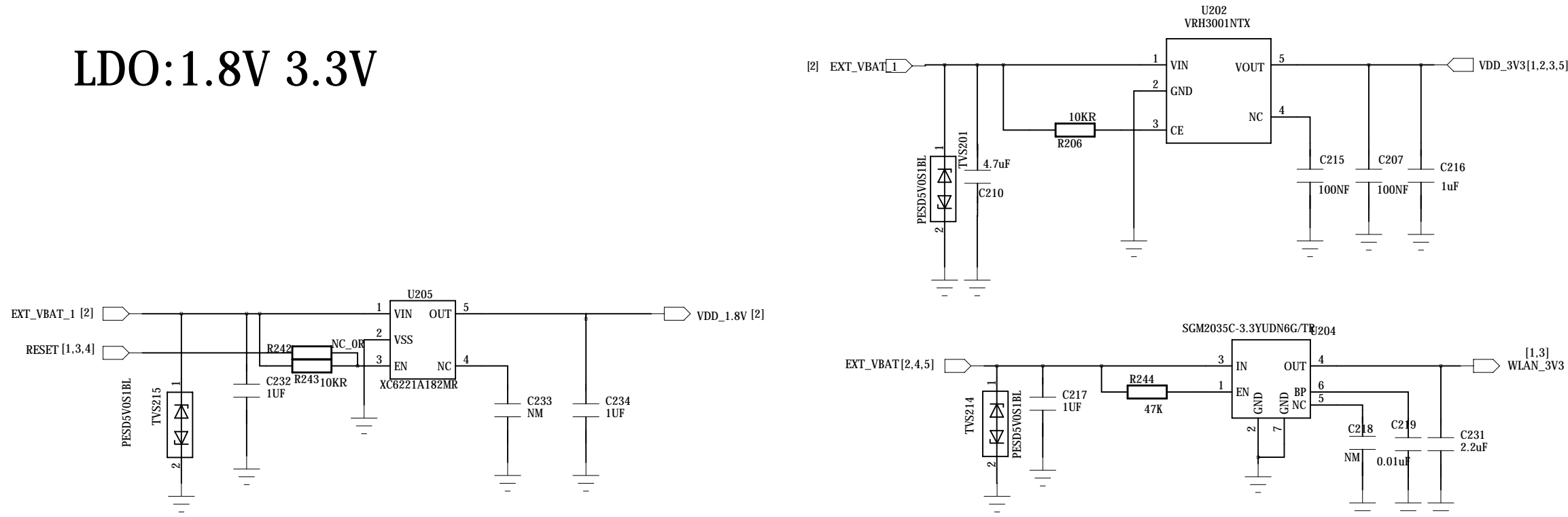
POWER



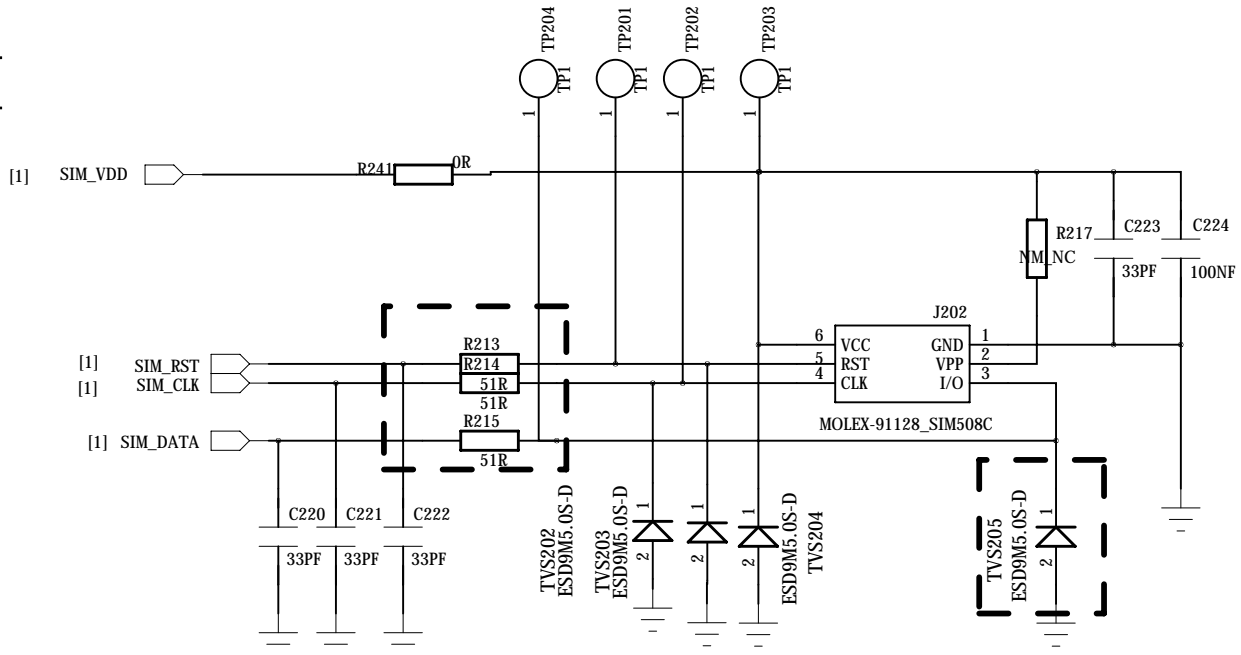
Sub SIM



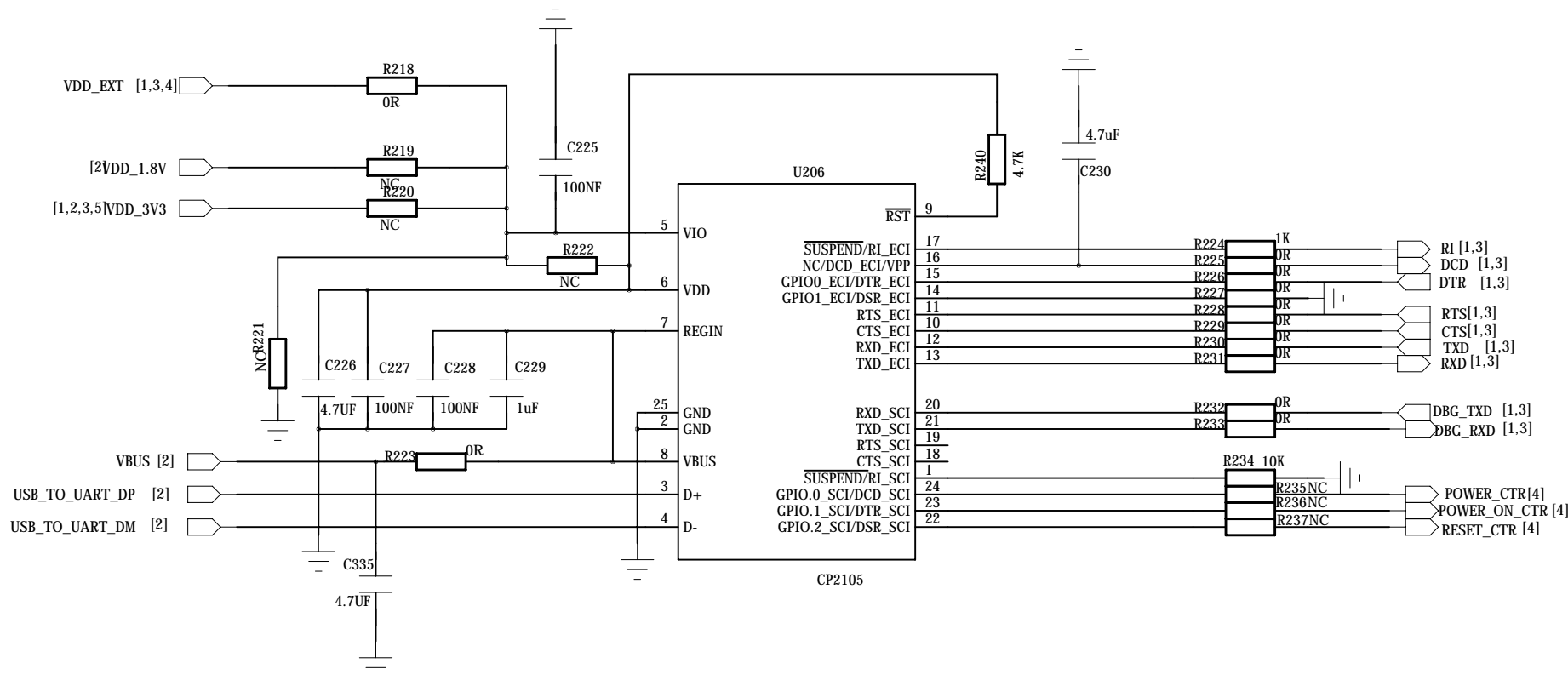
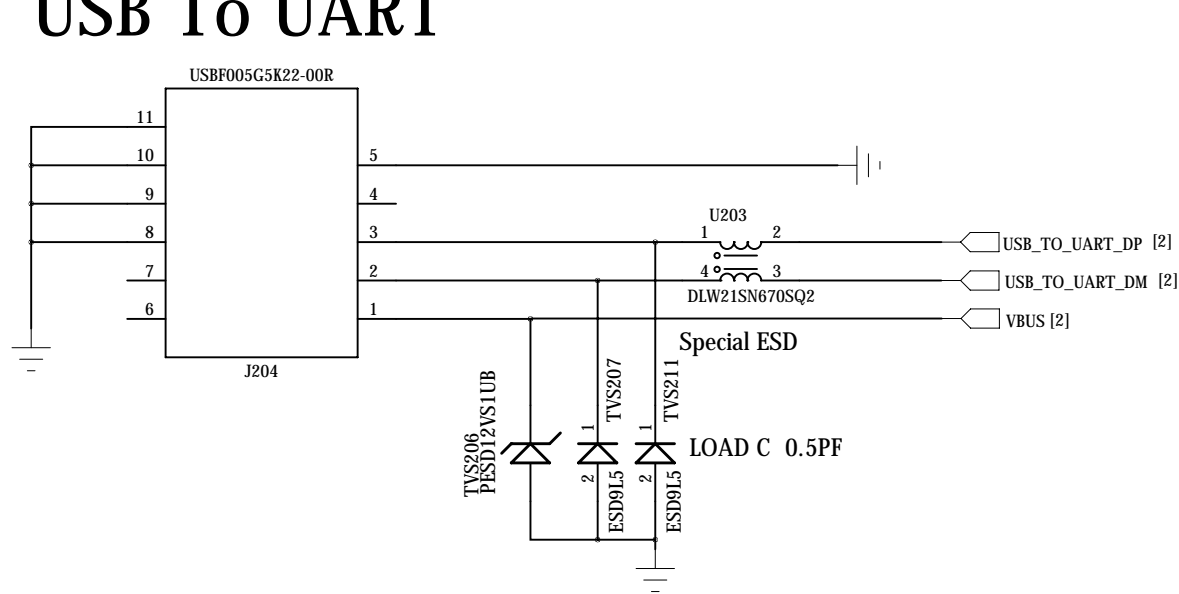
LDO:1.8V 3.3V



Main SIM



USB To UART

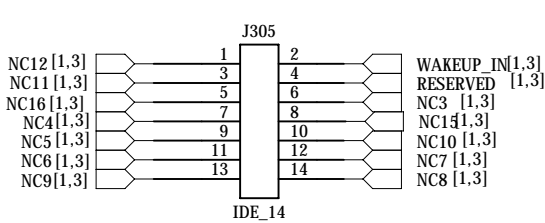
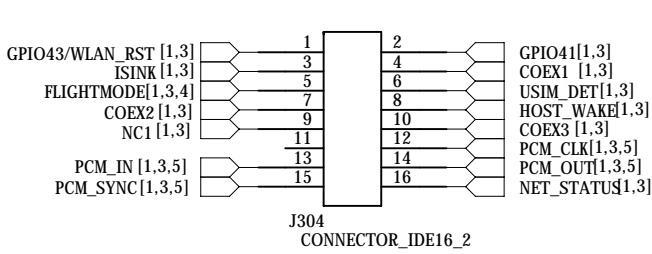
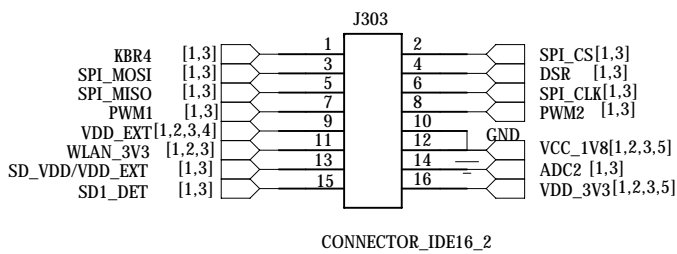
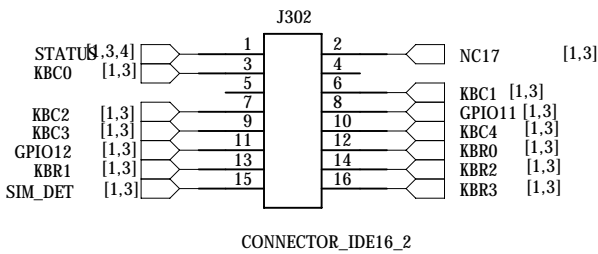
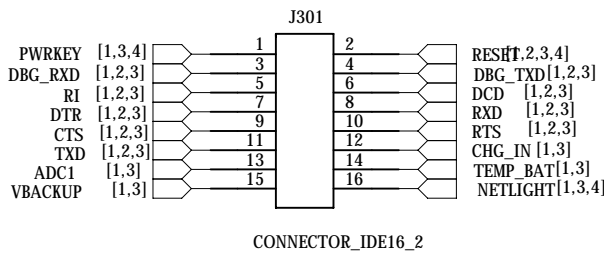


[2] VDD_1.8V OR R238 VCC_1V8 [1,3,5]

SIM Technology

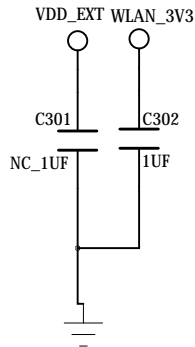
DRAWN BY <NAME HERE>	PORJECT W57-EVB	TITLE P2_POWER_INTERFACE
CHECKED BY <NAME HERE>	SIZE A2	VER V1.01
SHEET 2 of 5		2016-10-28

D



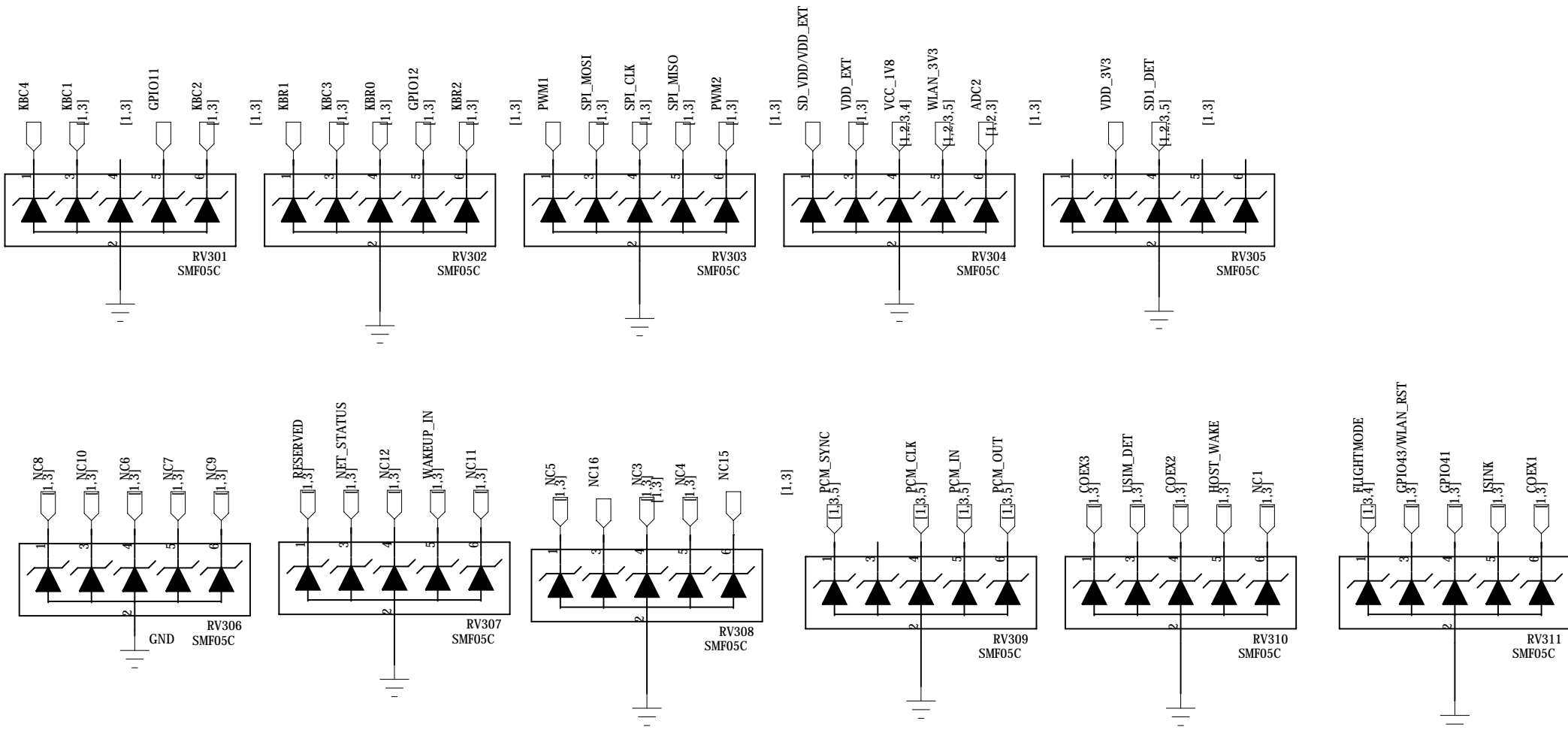
D

C



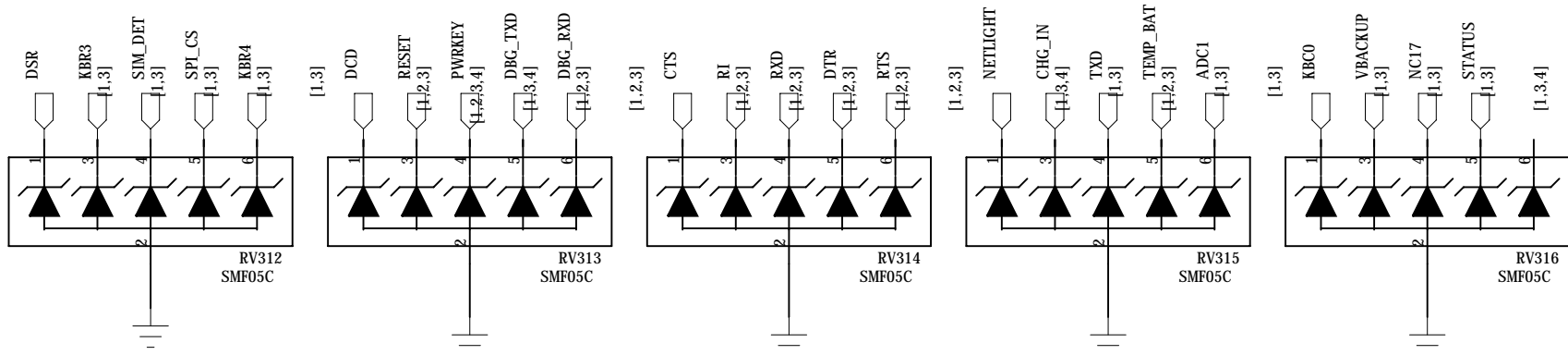
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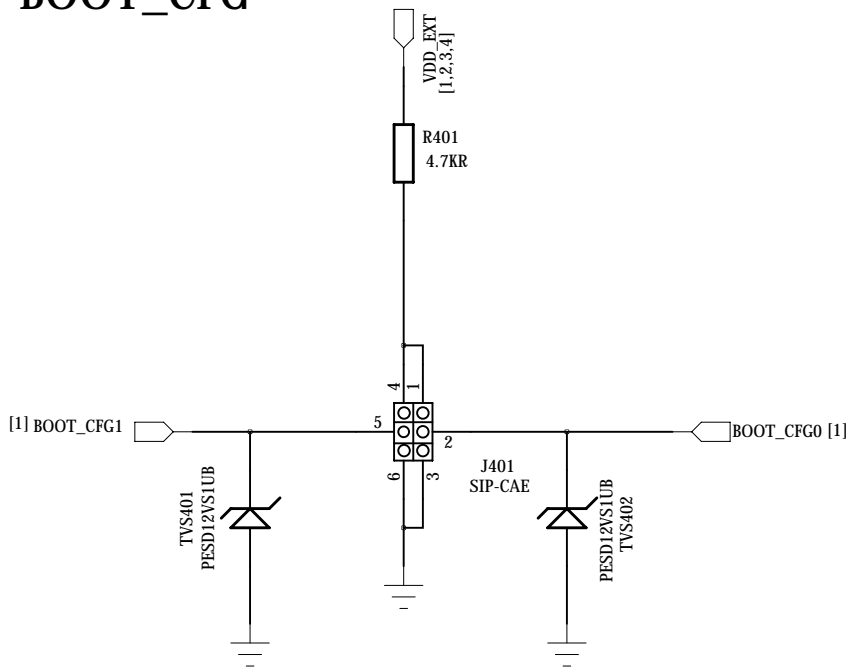


SIM Technology		
DRAWN BY <NAME HERE>	PORJECT W57-EVB	TITLE P3_TESTPOINT
CHECKED BY <NAME HERE>	SIZE A2	VER V1.01
SHEET 3 of 5		2016-10-28

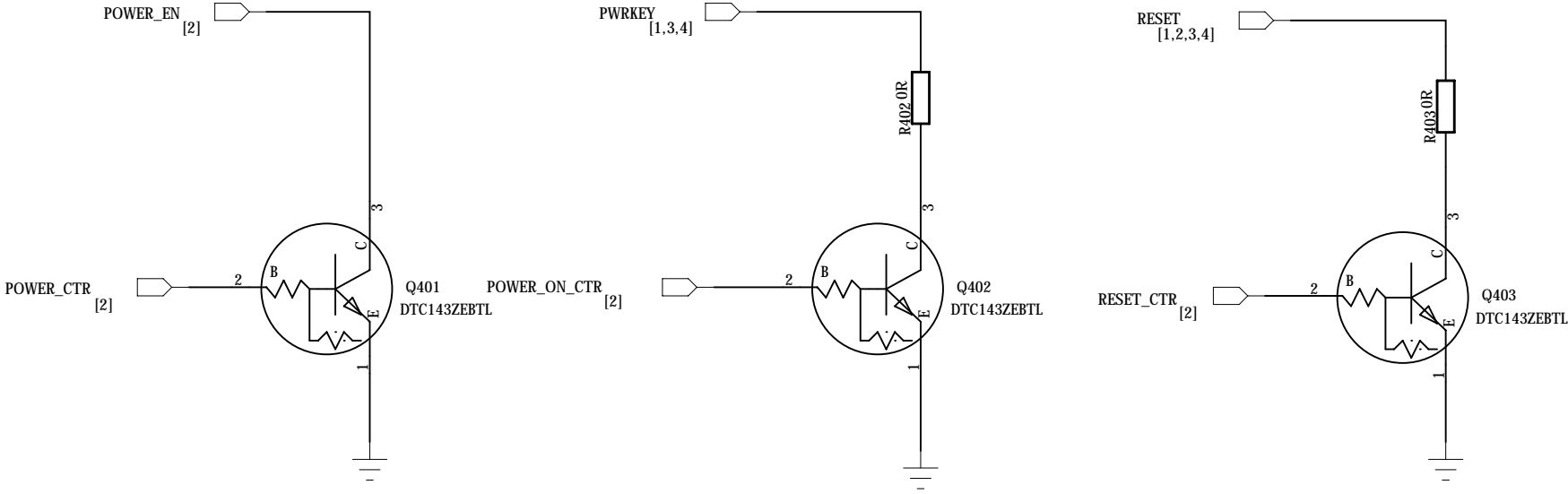
A

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BOOT_CFG

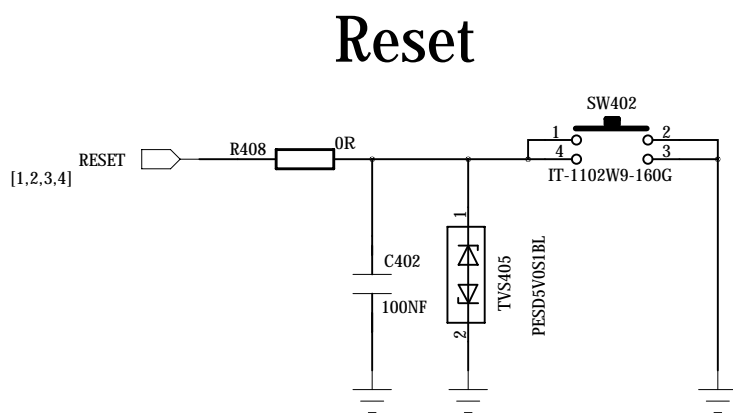
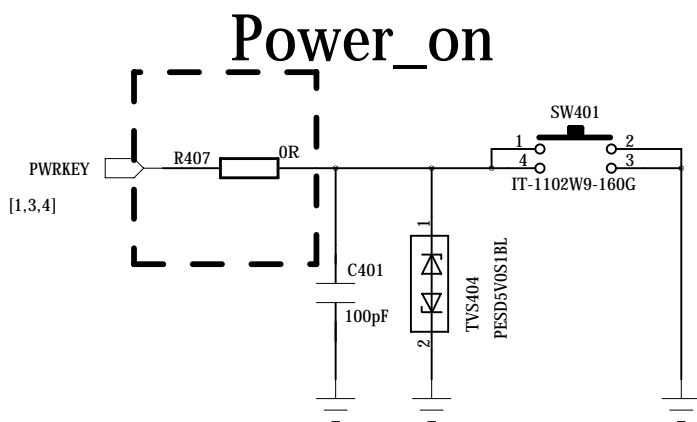
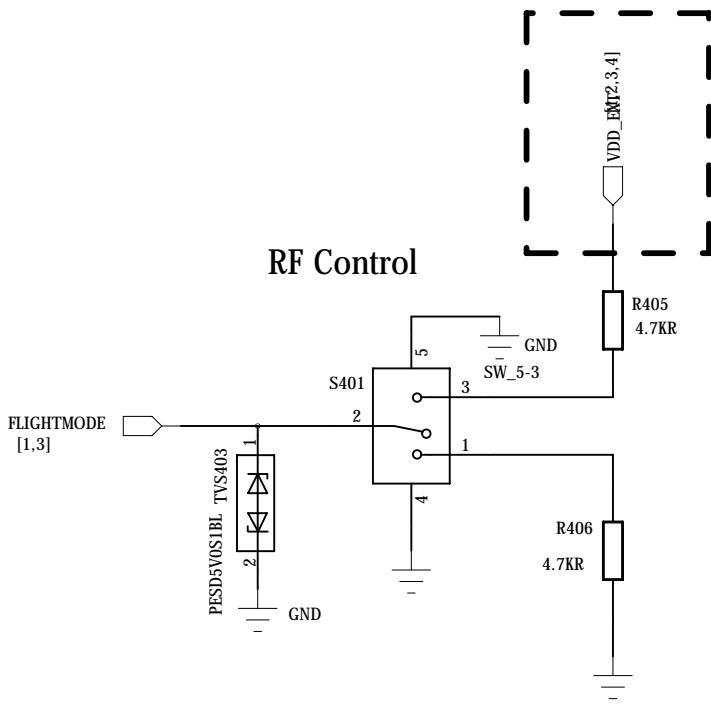
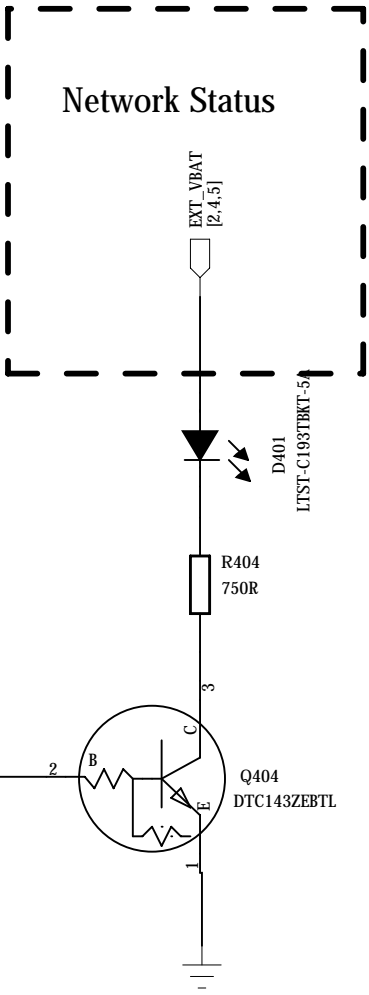
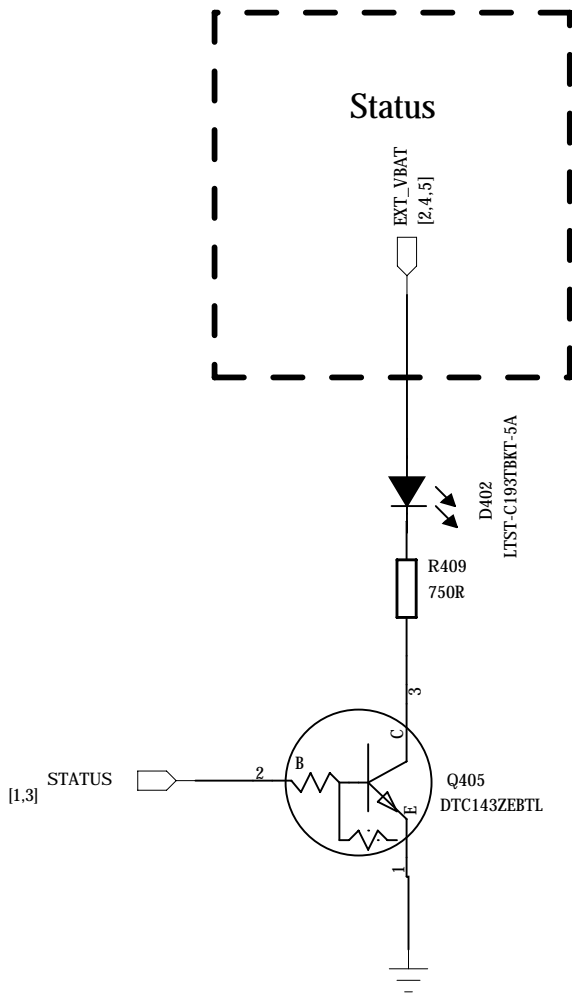


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SIM Technology

DRAWN BY <NAME HERE>	PORJECT W57-EVB	TITLE P4_UART_DEBUG
CHECKED BY <NAME HERE>	SIZE A2	VER V1.01
SHEET 4 of 5		2016-10-28

6

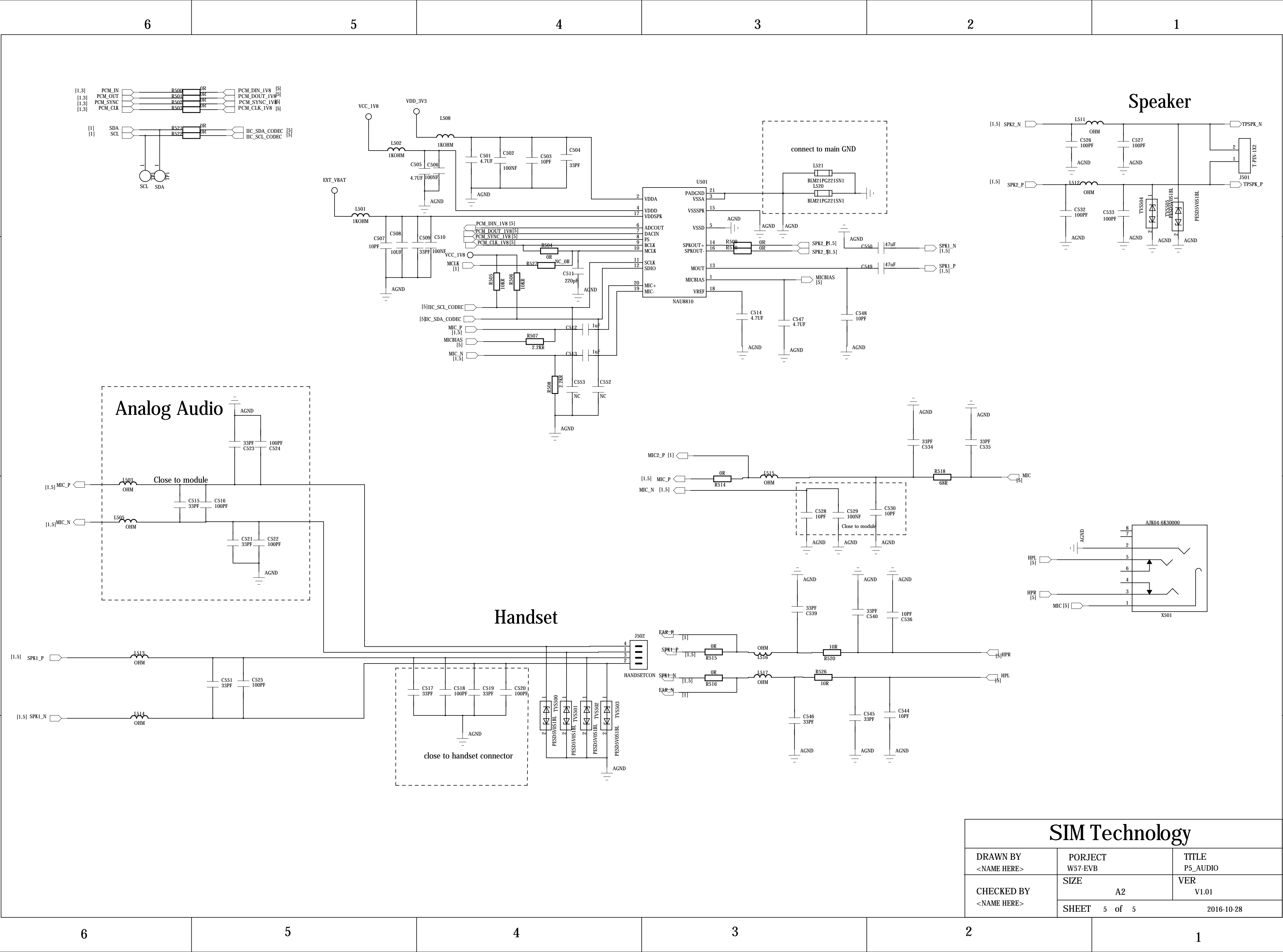
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1



SIM Technology		
DRAWN BY <NAME HERE>	PORJECT W57-EVB	TITLE P5_AUDIO
CHECKED BY <NAME HERE>	SIZE A2	VER V1.01
	SHEET 5 of 5	2016-10-28