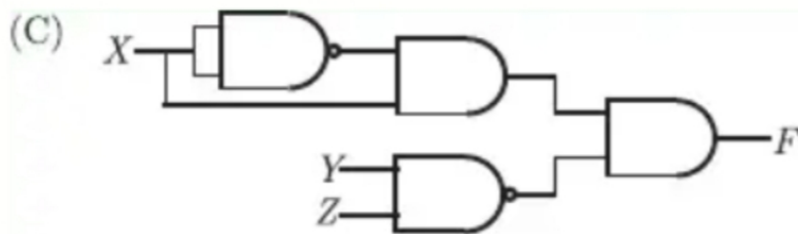
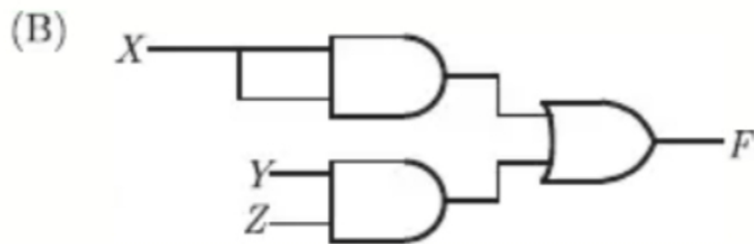
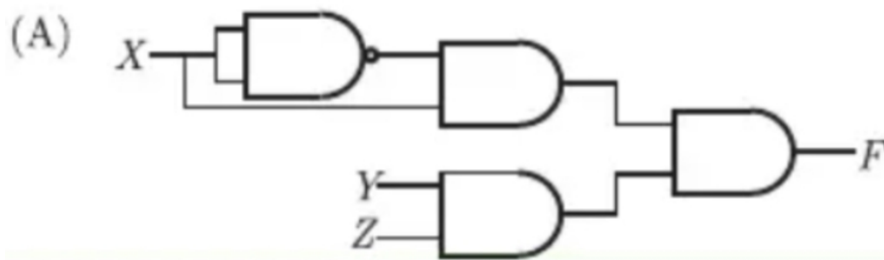
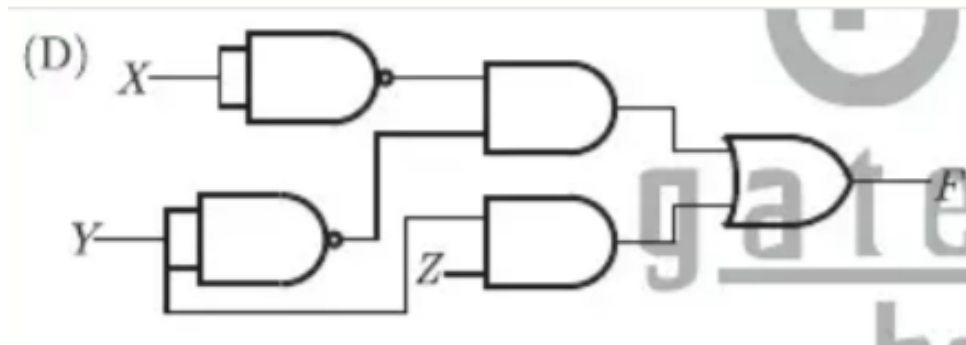


GATE QUESTION EEE 2010 Q53

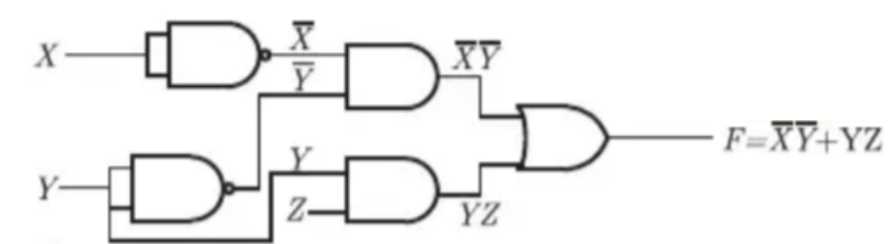
Q53: Which of the following circuits is a realization of the above function F ?





Correct option: (D)

Explanation: Option (D) implements the function:



$$F = \overline{X}Y + YZ$$