Stream cipher (based on AES S-box)

Referent

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Project

Design and implement an AES S-box based stream cipher supporting both encryption and decryption. The encryption algorithm of cipher is performed by XORing each character of the plaintext (an 8-bit ASCII coded character) with an 8-bit value obtained by substituting an 8-bit counter value with the S-box transformation of the AES algorithm. The 8-bit counter value (Counter Block, CB) has to be initialized with the value of an 8-bit symmetric key, K. The encryption law of the stream cipher can be expressed as it follows:

$$C[i] = P[i] \oplus S(CB[i])$$

where

$C[i]$ is the 8-bit ASCII code of the ith character of ciphertext.
$P[i]$ is the 8-bit ASCII code of the ith character of plaintext.
$CB[i]$ is the 8-bit value of the ith counter (counter block), for $i = 0,1,2,...$, and it can be represented by the formula $CB[i] = K + i \bmod 256$, being K the 8-bit symmetric (encryption/decryption) key. Note mod is the modulo operation.
$S(\ )$ is the S-box transformation of AES algorithm, that works over a byte.
$\oplus$ is the XOR operator.

Following figure represents the encryption scheme of the stream cipher.
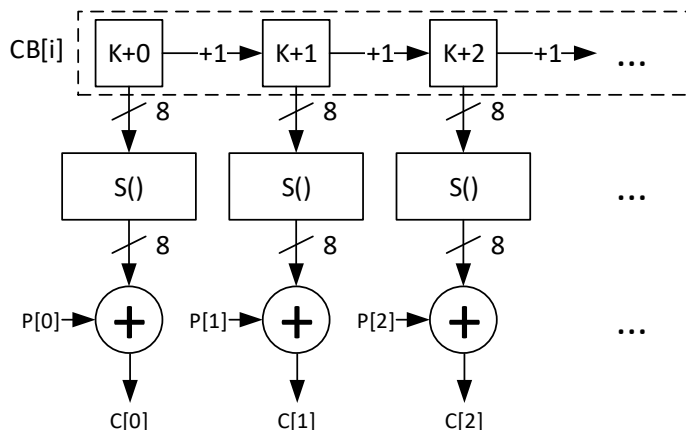

Additional design specifications

- The stream cipher shall encrypt one plaintext/ciphertext character per clock cycle;
- The stream cipher shall have an asynchronous active-low reset port;
- The input data character (plaintext or ciphertext) can be any 8-bit ASCII character;
- The stream cipher shall feature an input port which has to be asserted when providing the input data (plaintext or ciphertext) character (*din_valid* port): 1'b1, when input character is valid and stable, 1'b0, otherwise; the following waveform is expected at input interface of stream cipher

clk

din_valid

| din[7:0] | XX | D[0] | XX | D[1] | D[2] | XX |
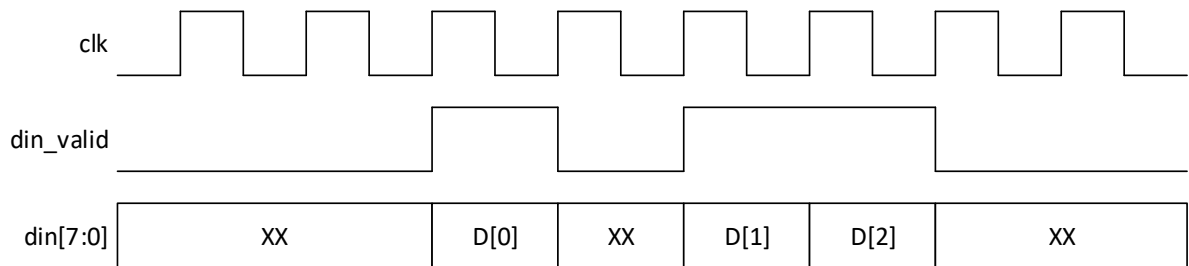
- The stream cipher shall feature an output port which is asserted when the generated output character (ciphertext or plaintext) is available at the corresponding output port (*dout_ready* port): 1'b1, when output character is valid and stable, 1'b0, otherwise; this flag shall be kept to logic 1 at most for one clock cycle; the following waveform is expected at the output interface of stream cipher

clk

dout_ready

| dout[7:0] | XX | Q[0] | XX | Q[1] | Q[2] | XX |
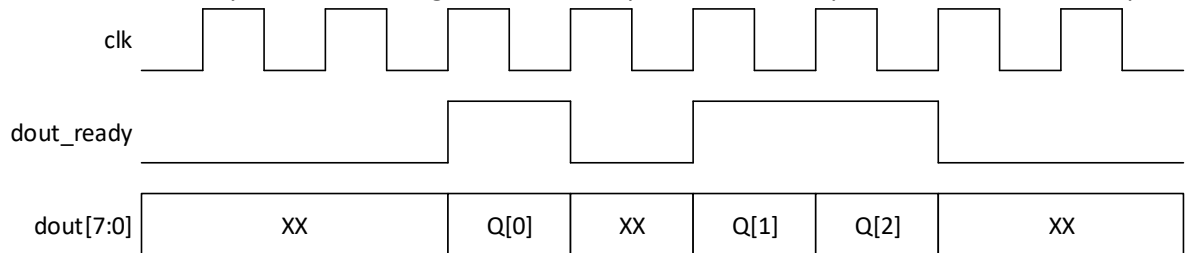
- For each arbitrary length plaintext/ciphertext message, the counter block shall start from initialization value K (the 8-bit key)

Hints

- The AES S-box function is largely documented online: implement the LUT version (for faster developing).
- Mind that before encrypting/decrypting the first character of a plaintext/ciphertext message the counter block shall be initialized with the key value, and that the counter block shall not be reinitialized with another key value (or the same) until the encryption/decryption of the whole message has been performed.
- Develop a testbench that encrypts two plaintext messages (from file or from string): for each plaintext message P, store the corresponding ciphertext C into proper object (file or string), then decrypt C (using the stream cipher) and store the corresponding plaintext P' into proper object (file or string); compare P' with P and check they match (character by character or using string methods). At least one of the two plaintext messages should be greater than 256 characters (around 300, for instance).
  For decryption focus on following XOR property:
  $$S \oplus S = 0$$
  thus, if $C = P \oplus S$, then $C \oplus S = (P \oplus S) \oplus S = P \oplus S \oplus S = P \oplus (S \oplus S) = P$.