

[illegible]

The circuit diagram shows a 4-bit parallel adder. It consists of two 74181 ICs (labeled 1 and 2) and one 74180 IC (labeled 3). The 74181 ICs are configured as 4-bit parallel adders. The 74180 IC is configured as a 4-bit majority gate. The inputs to the 74181 ICs are the 4-bit numbers A and B. The outputs of the 74181 ICs are the 4-bit sum S and the 4-bit carry-out C. The 74180 IC is used to generate the carry-in for the 74181 ICs. The circuit is powered by a 5V supply and a 1.5mF capacitor.

