

# NUCLEO64 STM32G4

MB1367

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## Legend

General comment such as function title, configuration, ...

[Text to be added to silkscreen.](#)

[Warning text.](#)

▲ Notes to generate the  
board layout.

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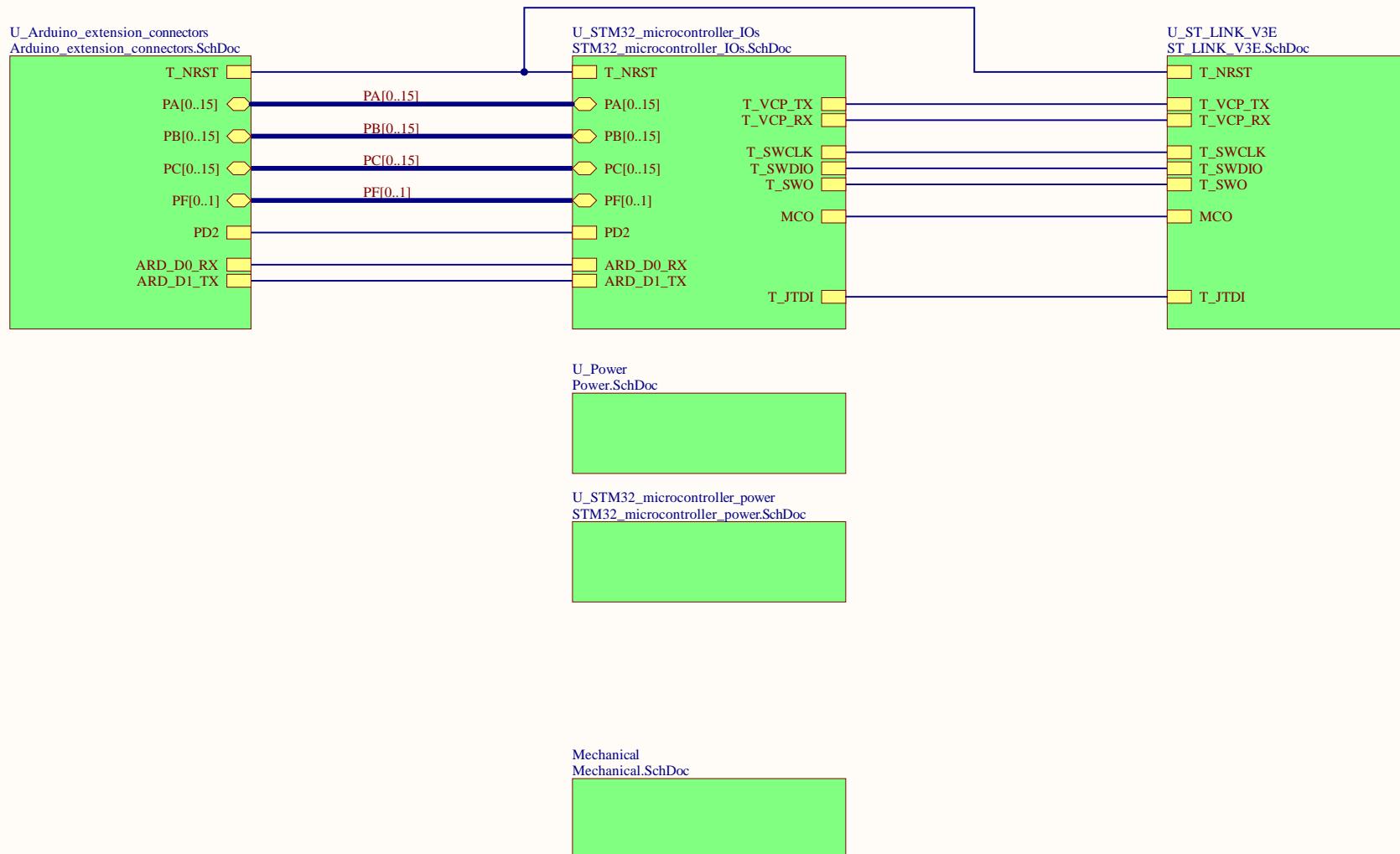
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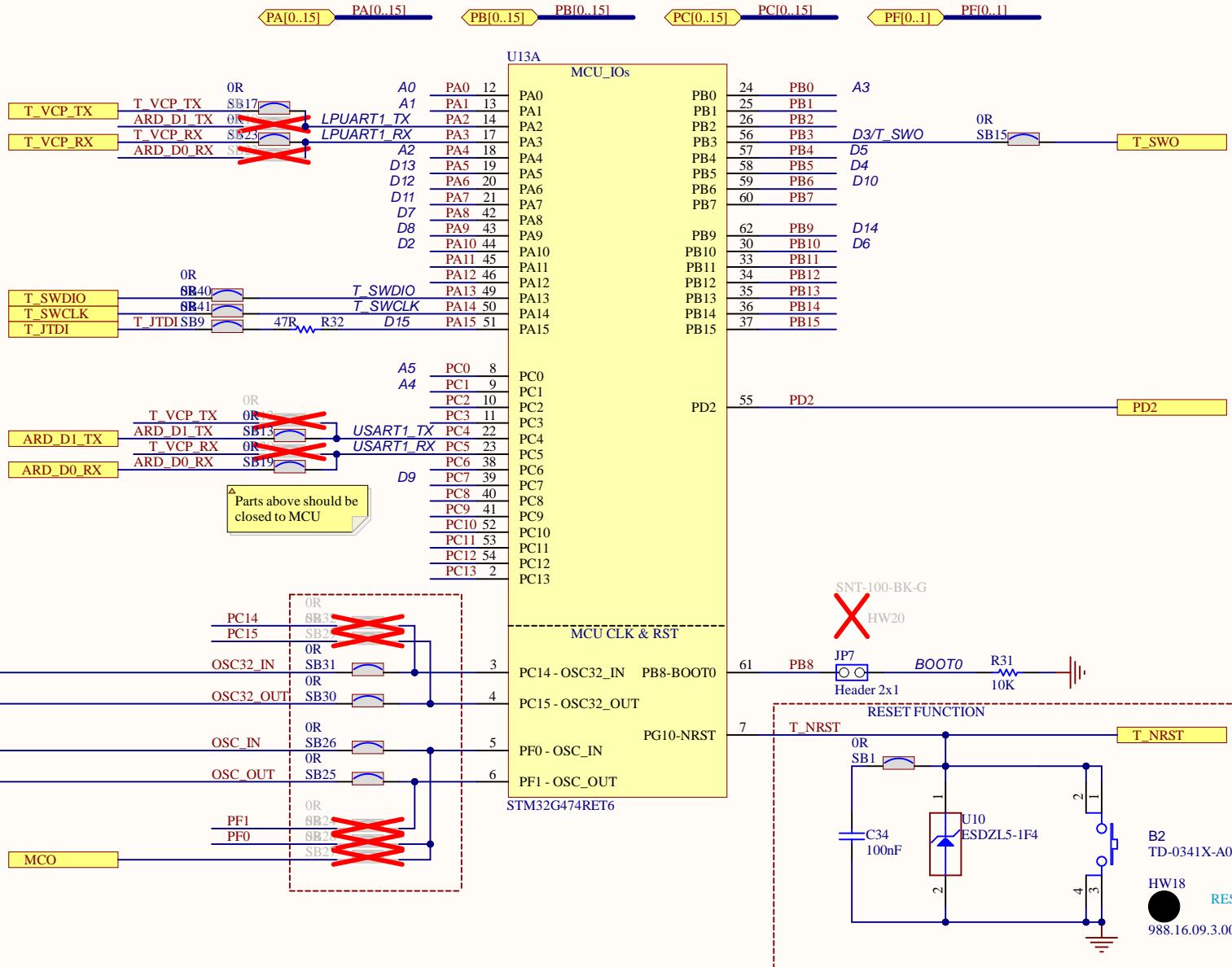
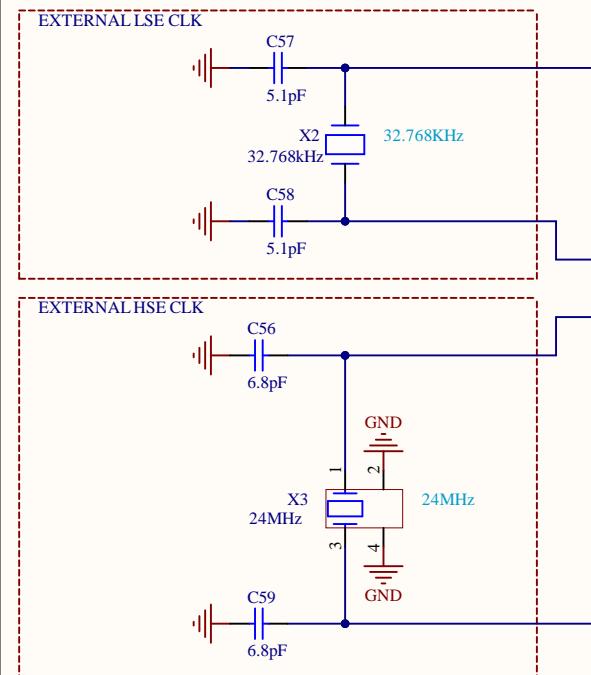
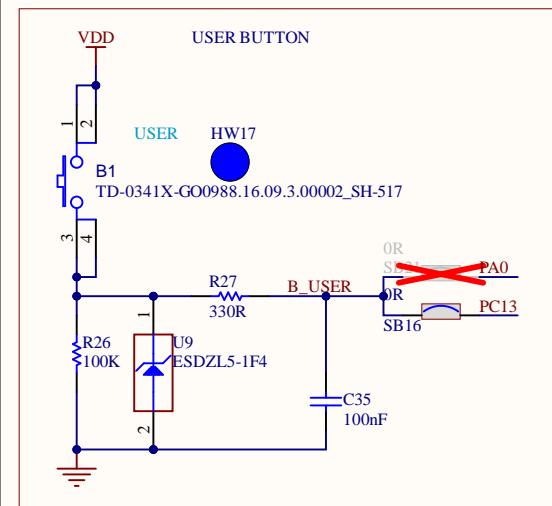
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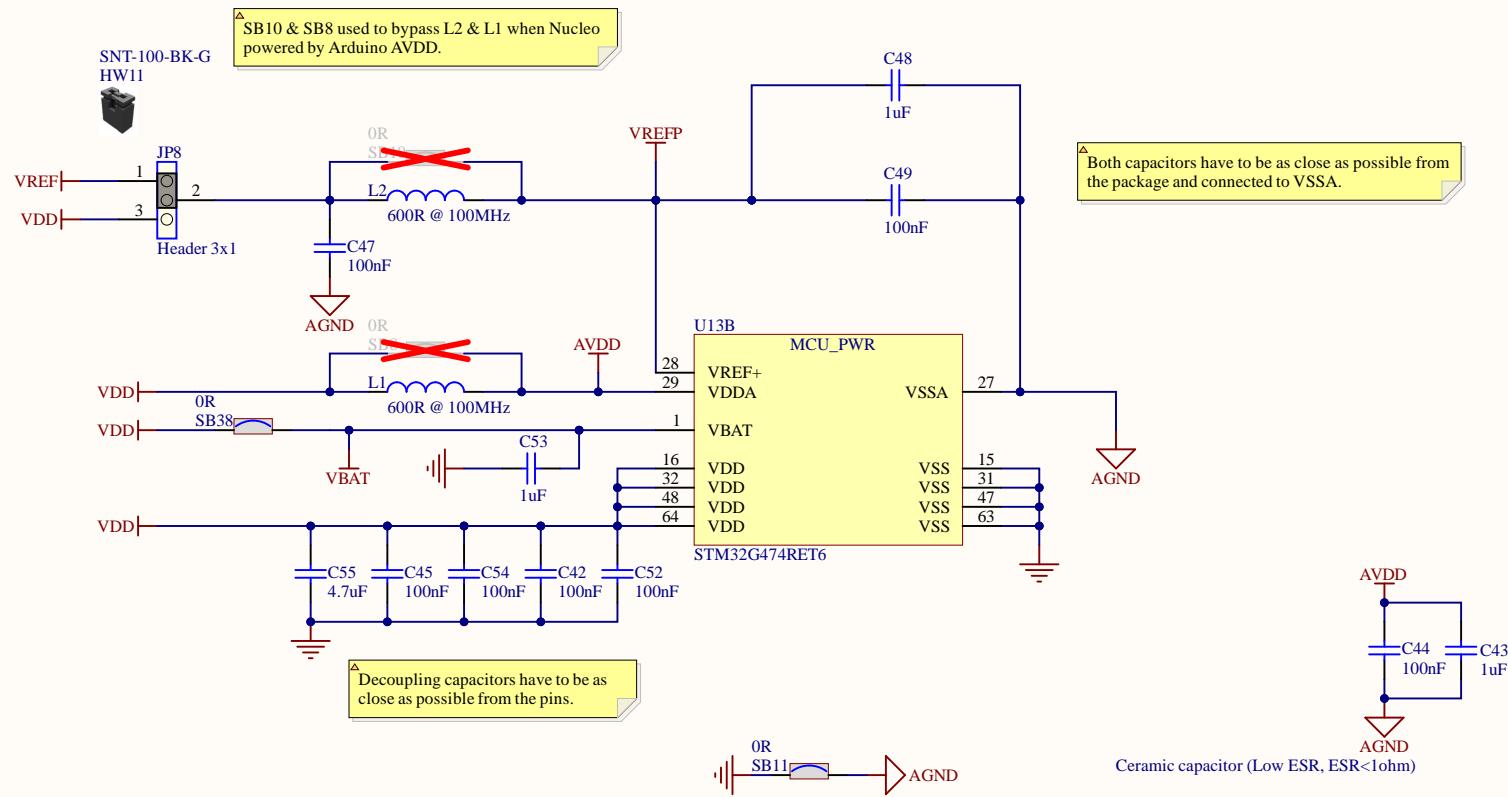
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Title: <a href="#">Project overview</a>		
Project: <a href="#">NUCLEO64 STM32G4</a>		
Variant: <a href="#">G474RE</a>		
Revision: <a href="#">D-01</a>	Reference: <a href="#">MB1367</a>	
Size: <a href="#">A4</a>	Date: <a href="#">26-SEP-25</a>	Sheet: <a href="#">1 of 8</a>

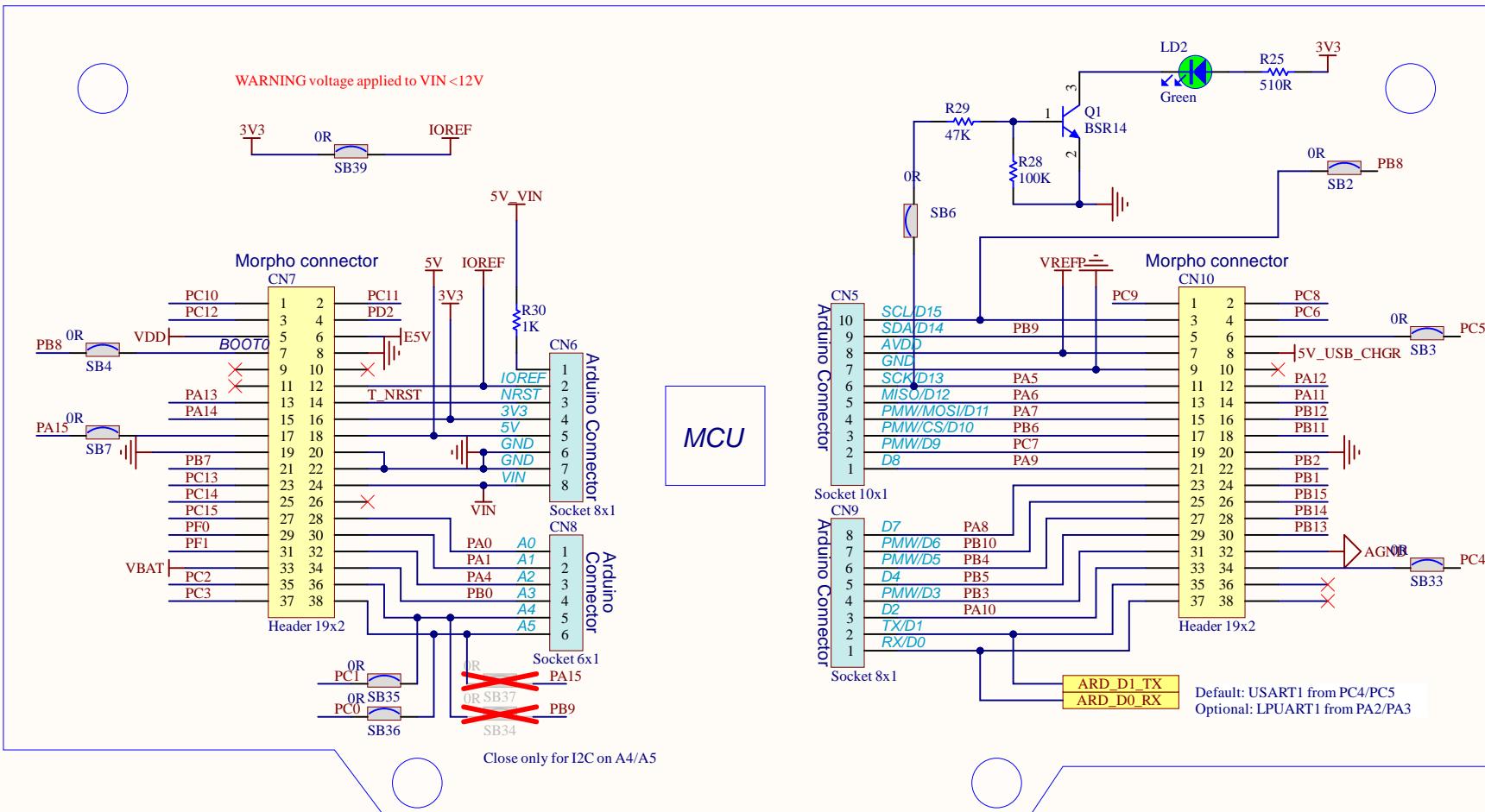




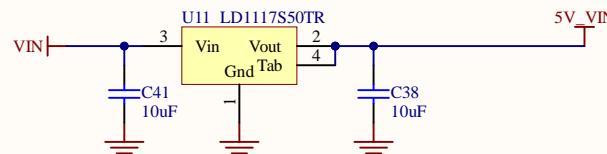




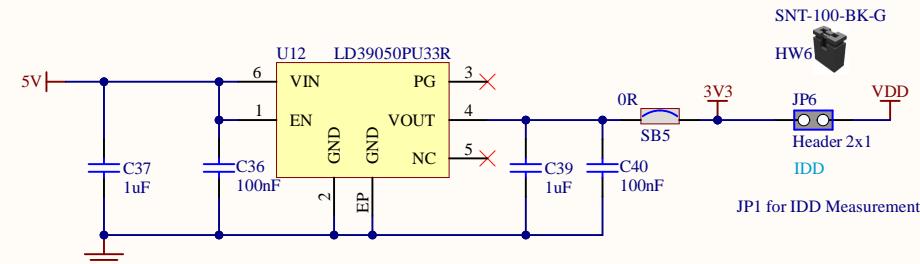
PA[0..15]	PA[0..15]
PB[0..15]	PB[0..15]
PC[0..15]	PC[0..15]
PD2	PD2
T_NRST	T_NRST
PF[0..1]	PF[0..1]



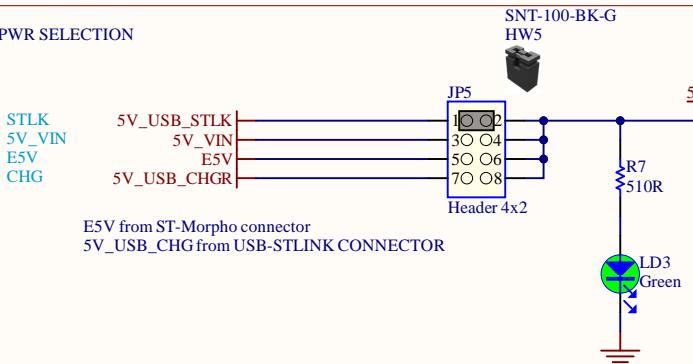
VIN / 5V PWR



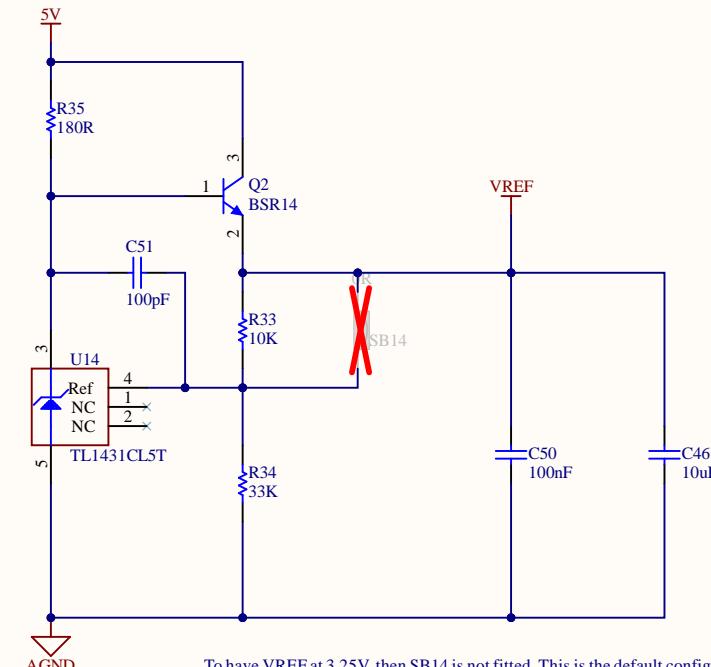
VDD PWI



## 5V PWR SELECTION

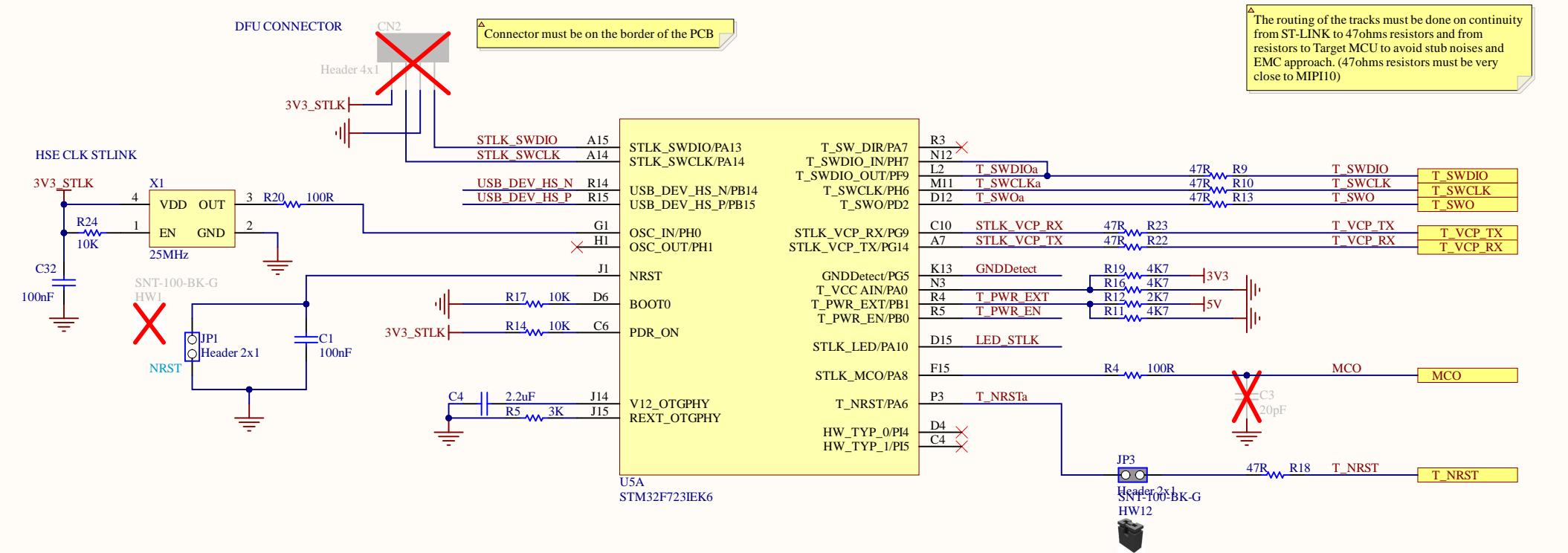


VREF PW

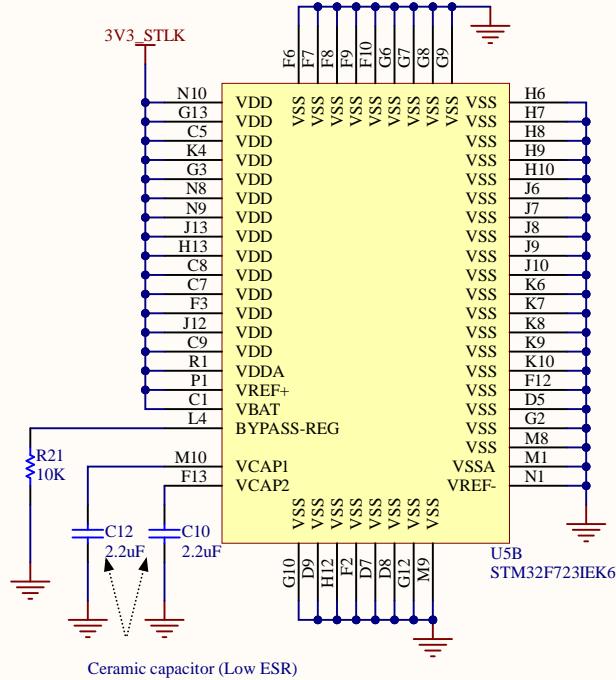


To have VREF at 3.25V, then SB14 is not fitted. This is the default configuration.  
To have VREF at 2.5V, then SB14 is fitted and R34 is not fitted

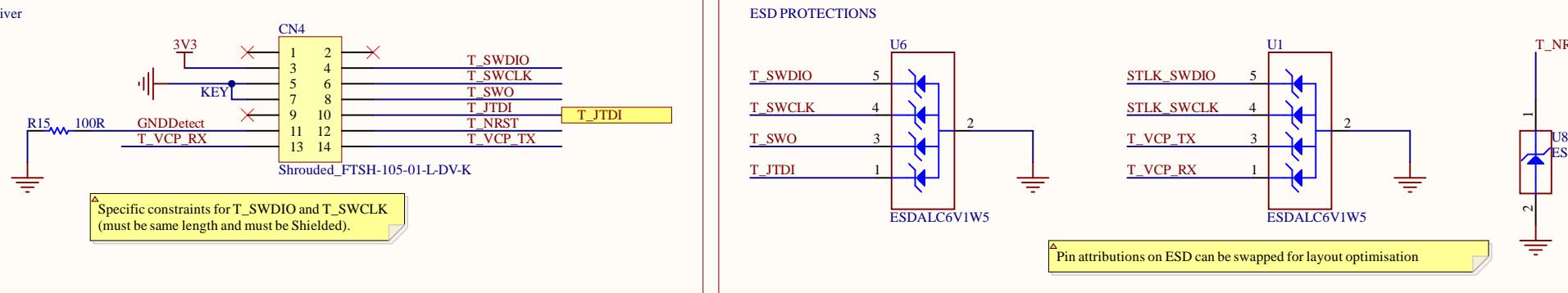
## STLINK MCU



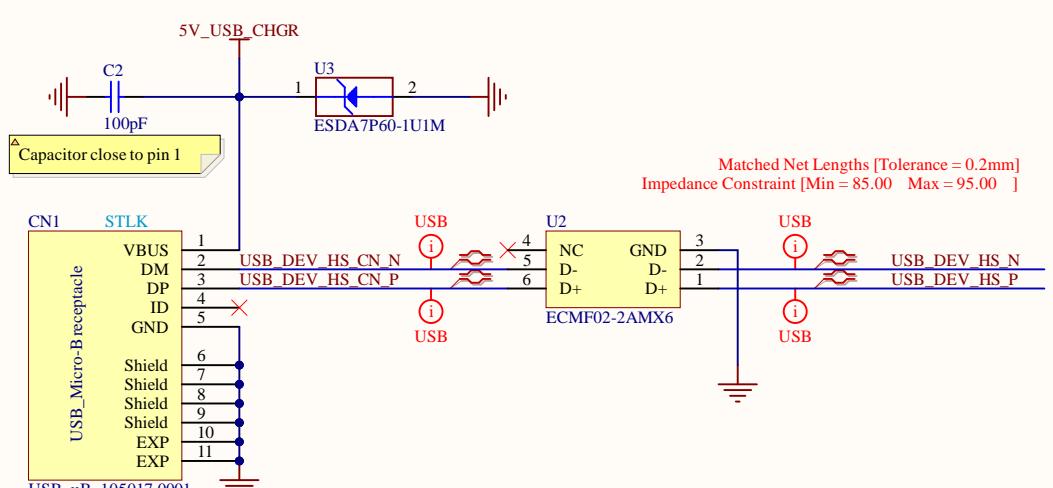
## ST-LINK POWER



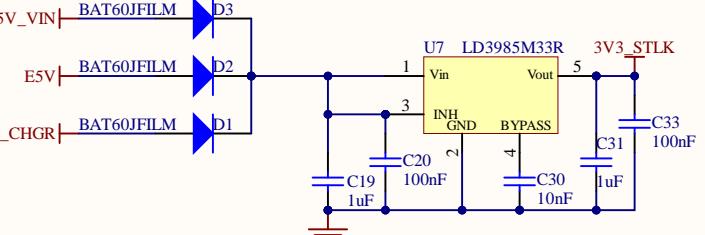
## STDCL4 Receiver



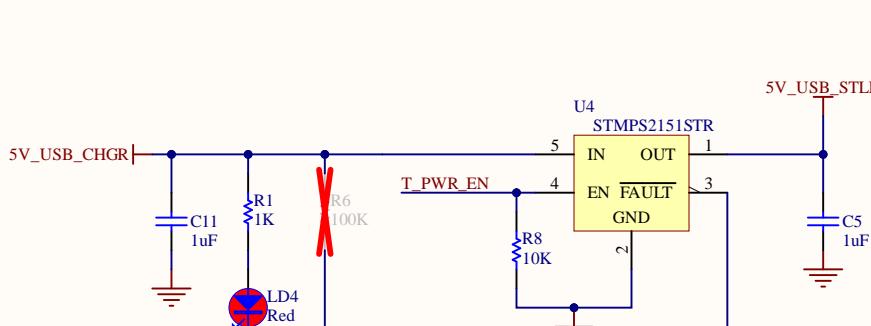
## ST-LINK USB CONNECTOR



## ST-LINK POWER 3V3 / 150mA



## 5V ST-LINK PROTECTION



## GND



HW101  
BOARD REF  
N/A

HW102  
BOARD CPN  
NUCLEO-G4xxRx

HW108  
ES  
N/A

HW100  
PCB  
N/A

HW103  
LOGO NUCLEO  
N/A

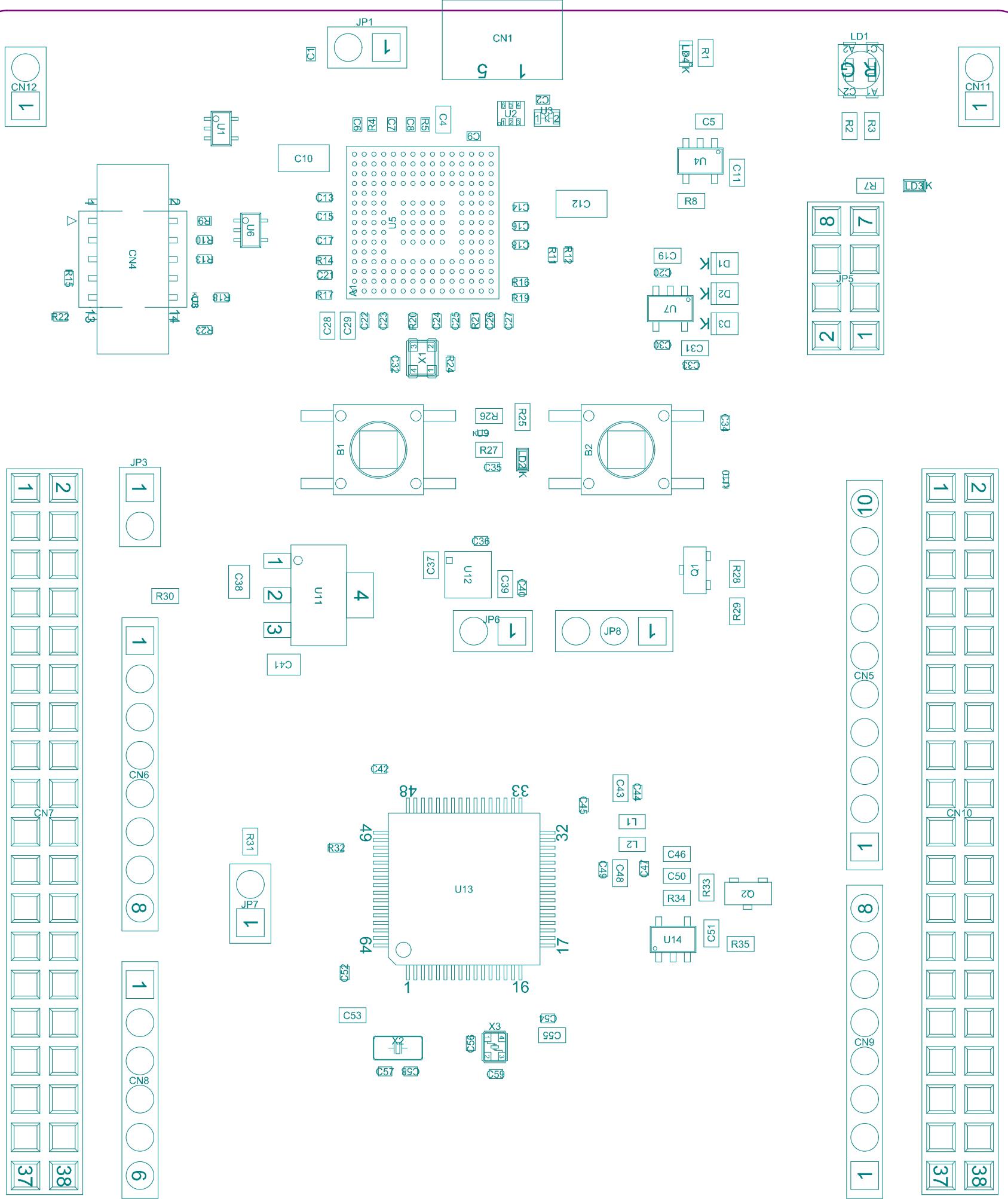
HW106  
N/A

HW104  
N/A

HW107  
N/A

HW105  
N/A





SB1

SB2

SB3

SB6

SB5

SB4

SB40

688S

SB41

SB9  
7BS

SB15

61BS

SB38

SB30  
SB31

SB13

SB17

SB25  
SB26

SB19

SB23

SB11

SB33

SB35

SB36



