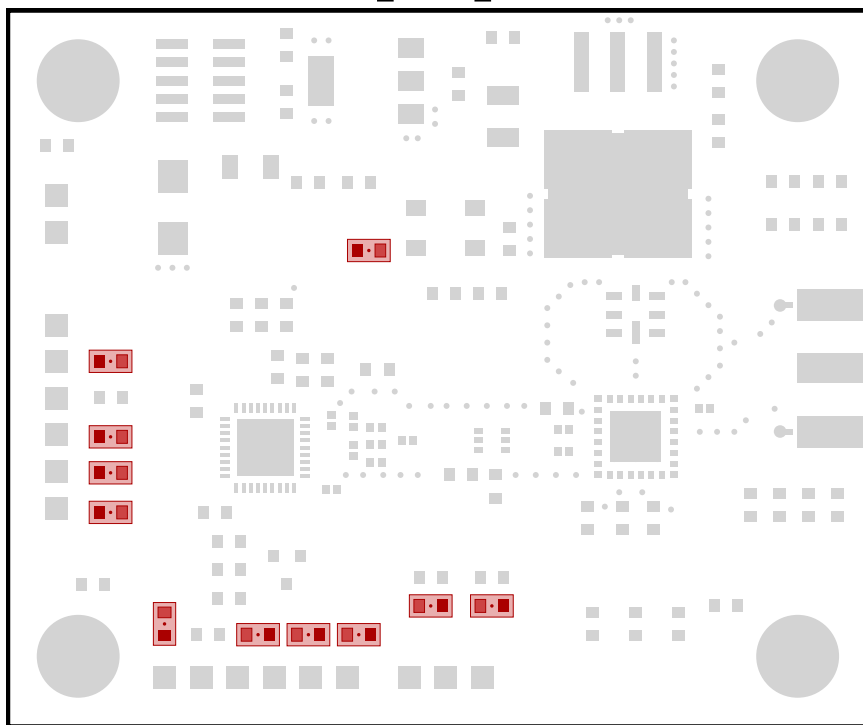


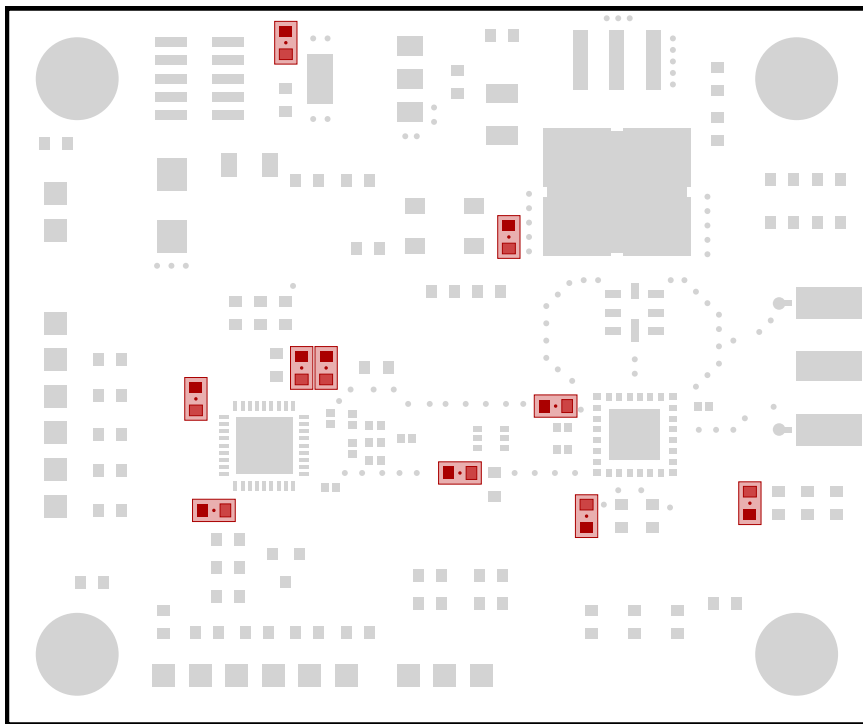
[illegible]

11x 100, R\_0603\_1608Metric



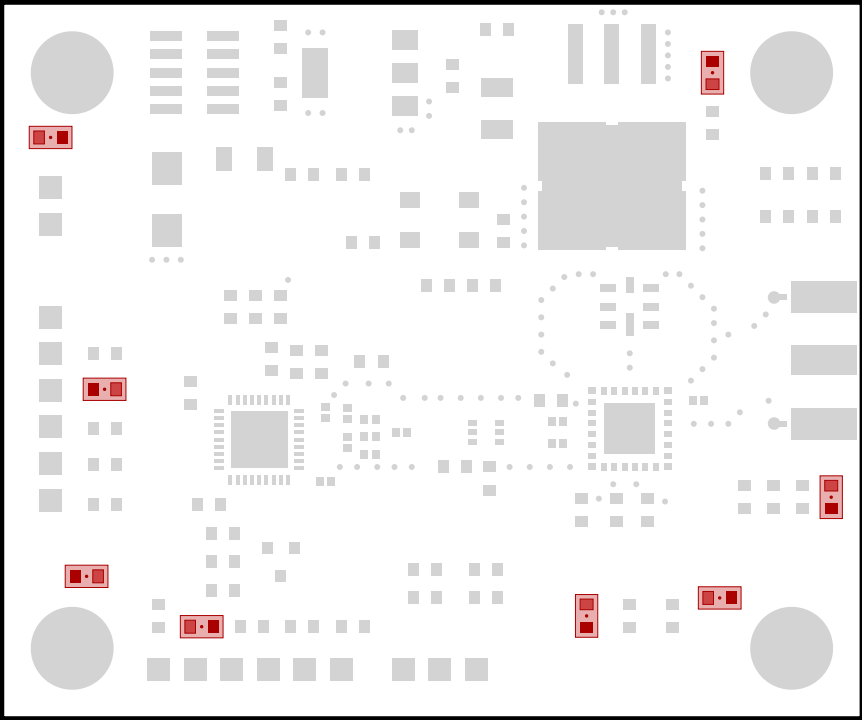
R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R17

10x 0.1u, C\_0603\_1608Metric



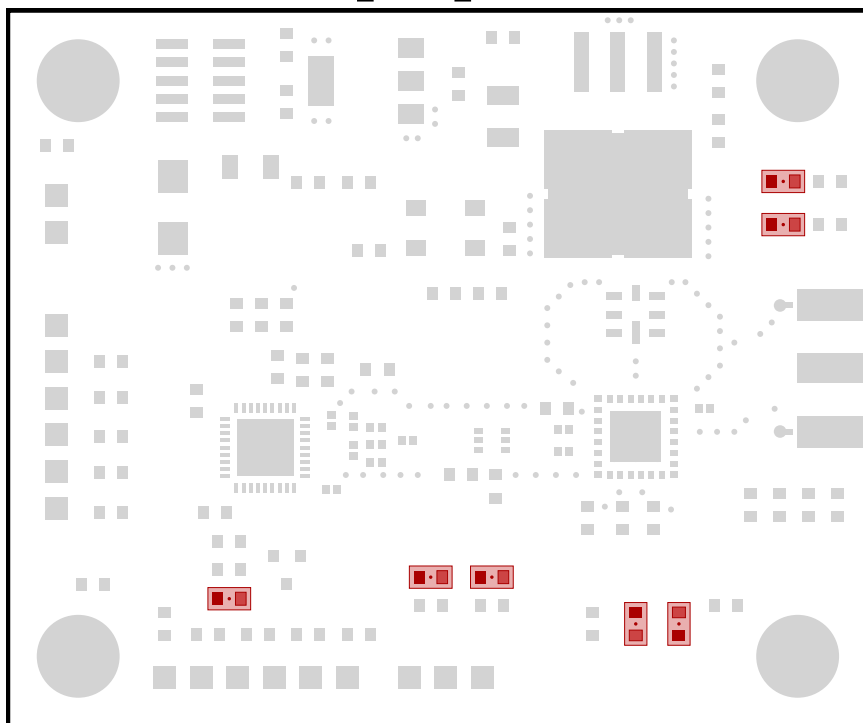
C1, C3, C5, C6, C8, C10, C15, C26, C27, C31

8x DNI, R\_0603\_1608Metric



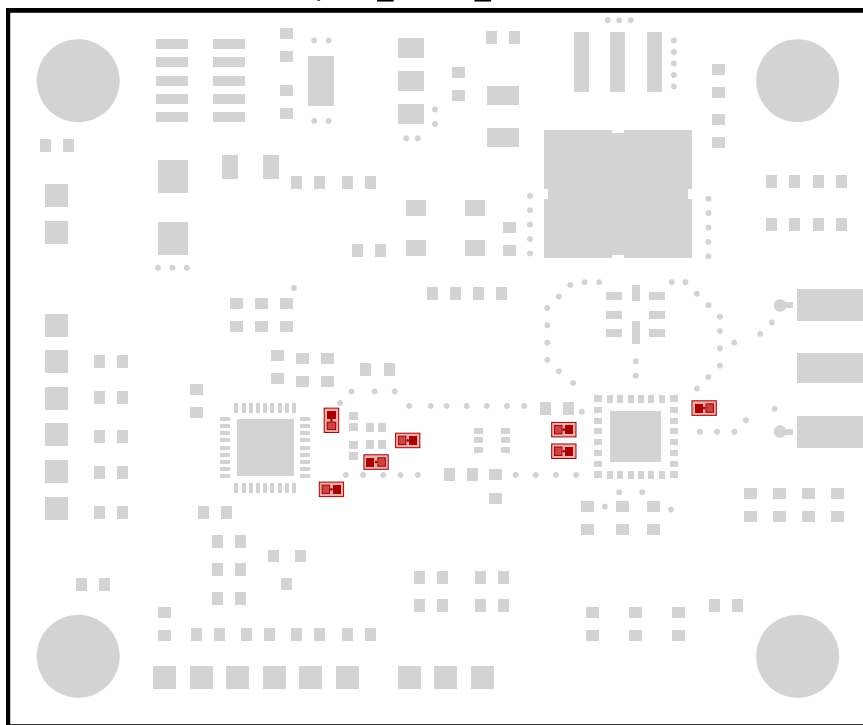
R15, R19, R20, R25, R29, R30, R31, R32

7x 1K, R\_0603\_1608Metric



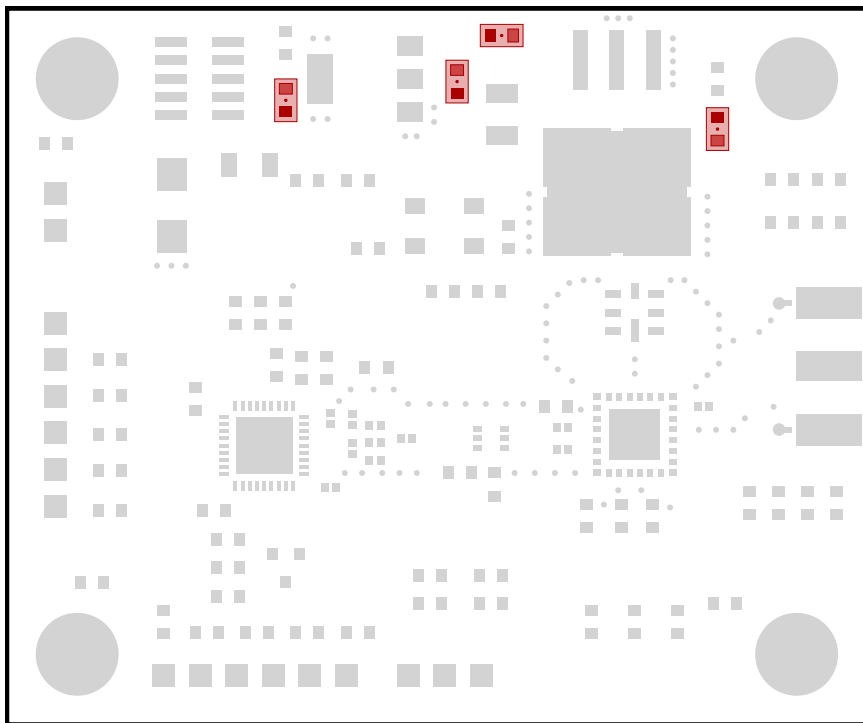
R1, R2, R3, R4, R18, R26, R28

7x 220p, C\_0402\_1005Metric



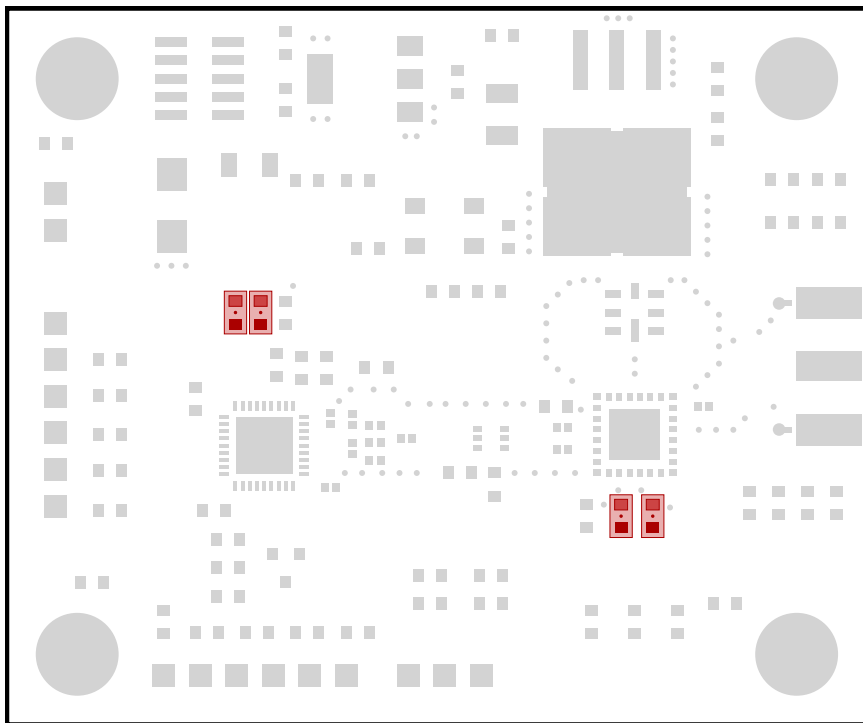
C2, C7, C21, C25, C38, C39, C45

4x 10u, C\_0603\_1608Metric



C4, C13, C18, C20

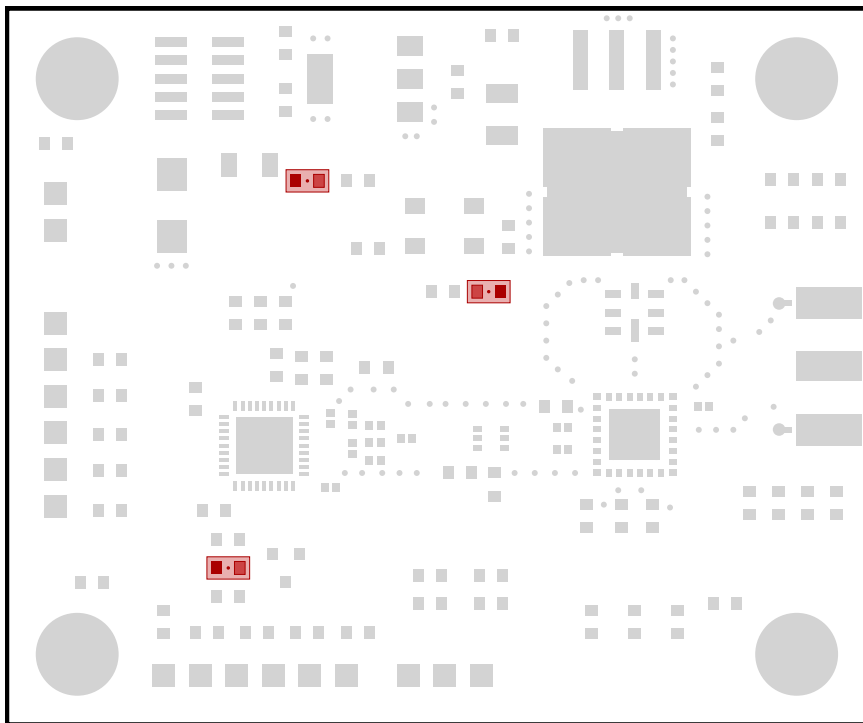
4x 1u, C\_0603\_1608Metric



C9, C44, C46, C47

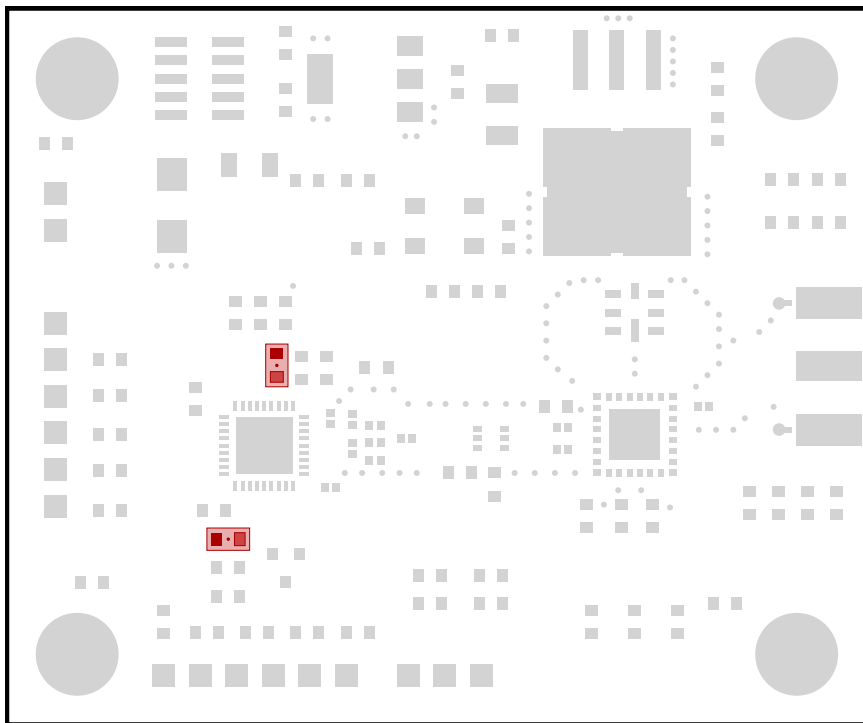


3x 33K, R\_0603\_1608Metric



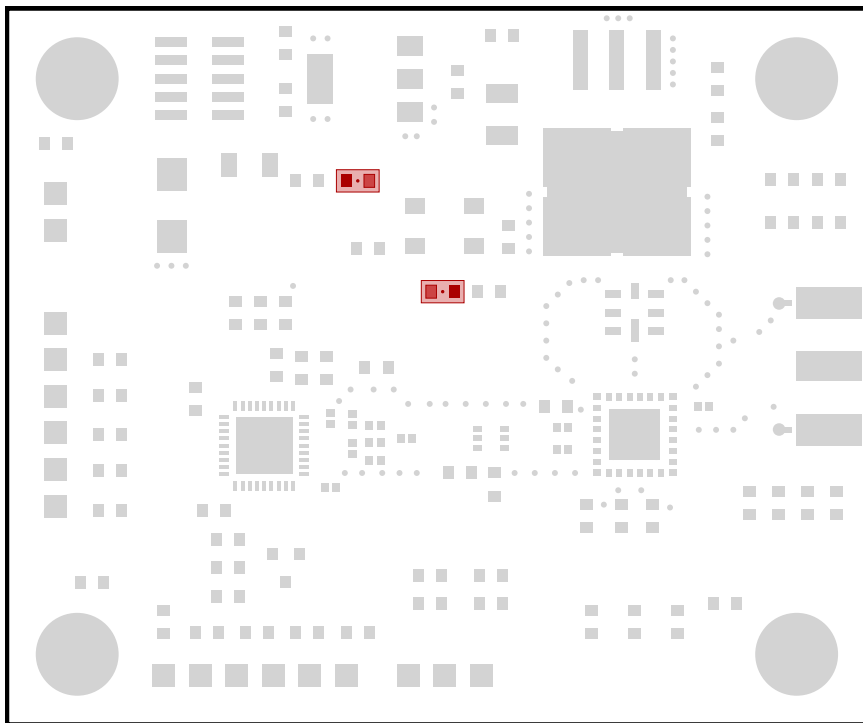
R21, R23, R27

2x 1n, C\_0603\_1608Metric



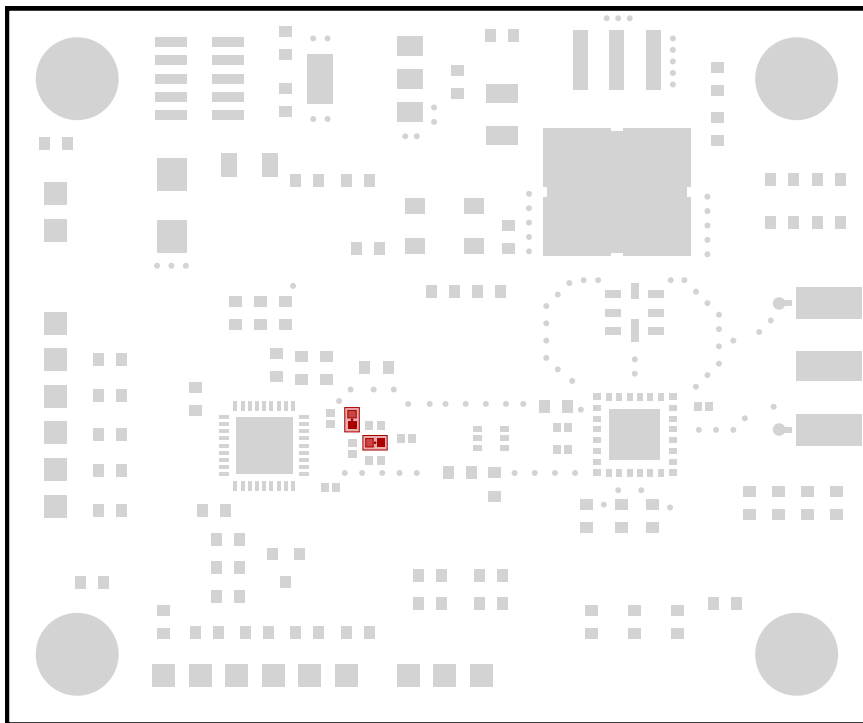
C19, C22

2x 10K, R\_0603\_1608Metric



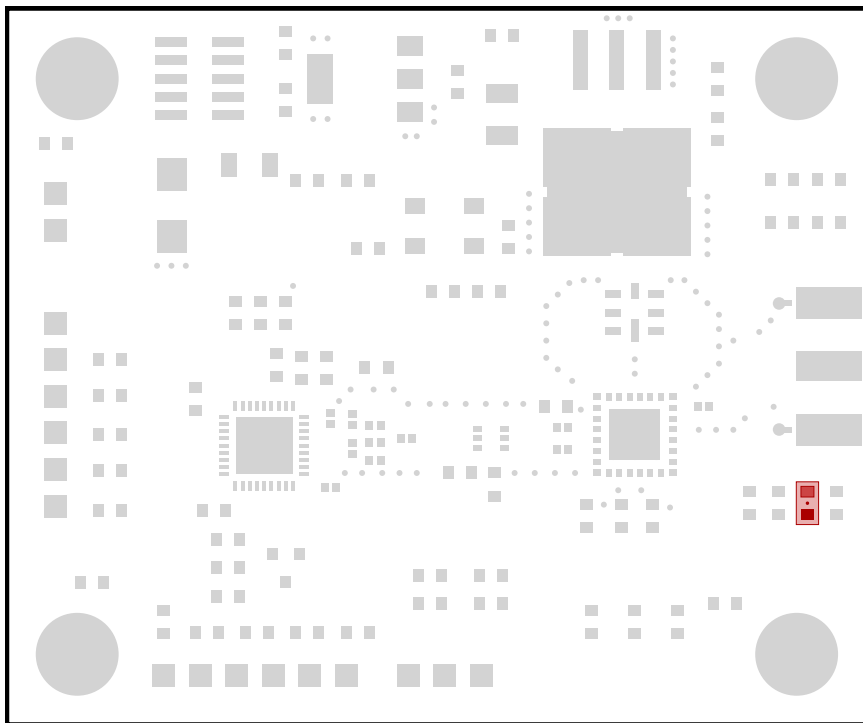
R22, R24

2x 3.9p, C\_0402\_1005Metric



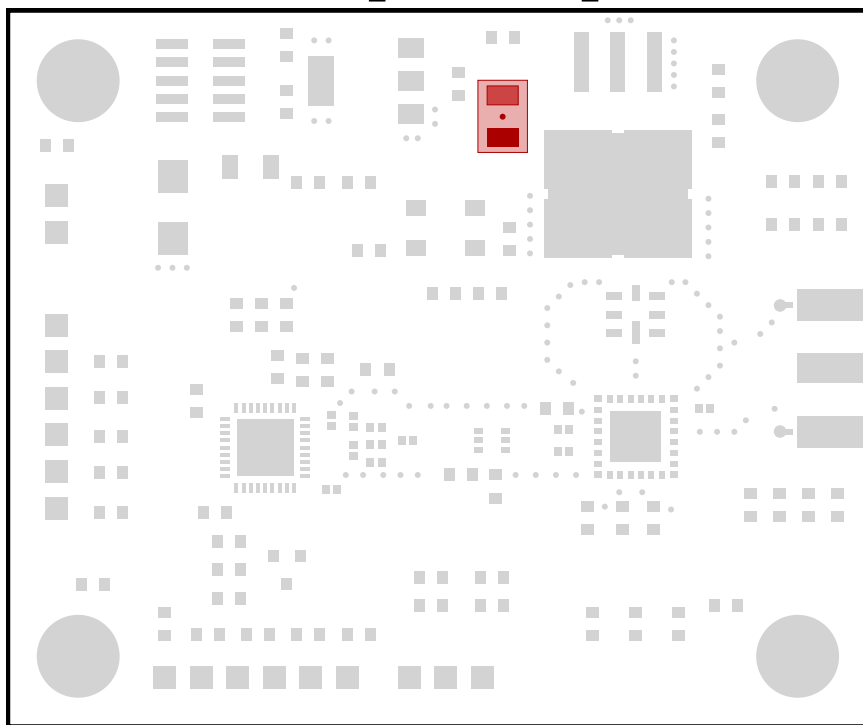
C23, C24

1x 5K6, R\_0603\_1608Metric



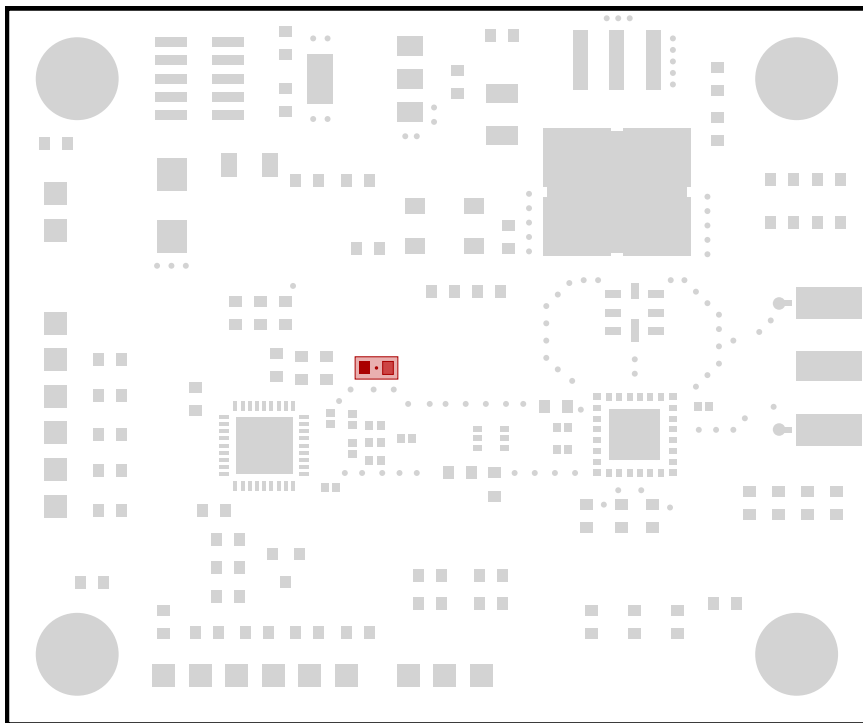
R36

1x 100u, CP\_EIA-3528-15\_AVX-H



C14

1x 56K, R\_0603\_1608Metric



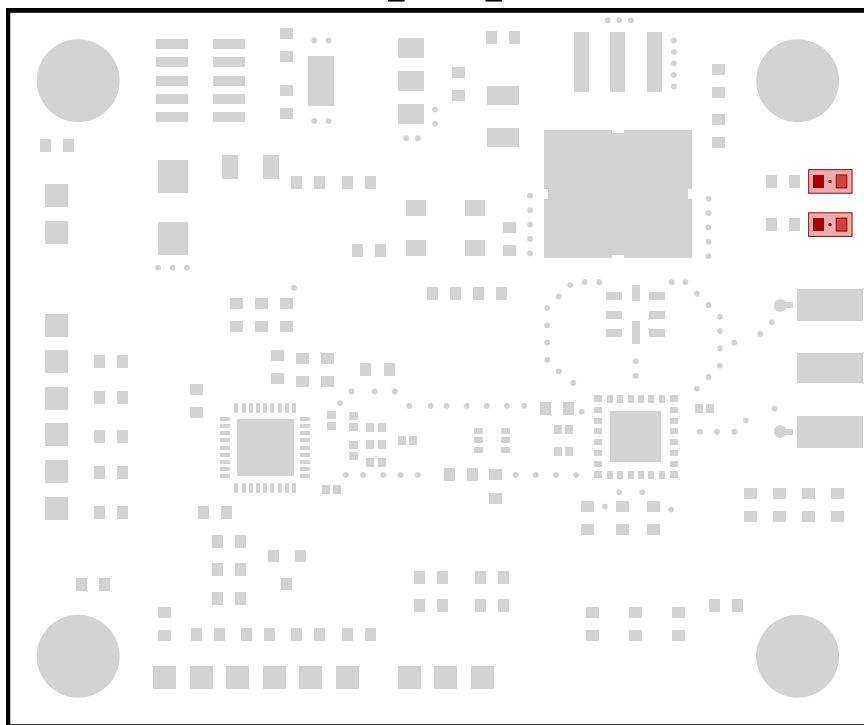
R16

The diagram is a complex, abstract black and white technical drawing or stylized map. It is enclosed within a thick black rectangular border. The central area is dominated by a large, irregular shape composed of a grid of small squares, resembling a circuit board or a map of a city. This central shape is surrounded by various other geometric elements: four large circles are positioned at the corners; several smaller rectangles and lines are scattered throughout; and a prominent, large, solid black rectangle is located in the upper right quadrant. The overall composition is highly detailed and symmetrical, suggesting a technical or architectural plan.

R37

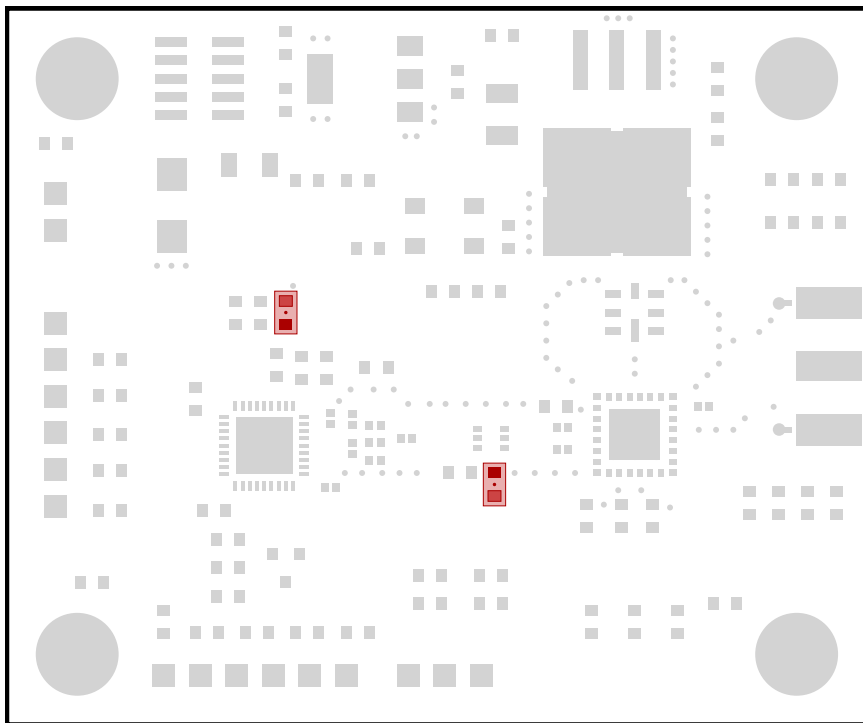


2x LED, LED\_0603\_1608Metric



D1, D2

2x Z=1k @ 100M, L\_0603\_1608Metric

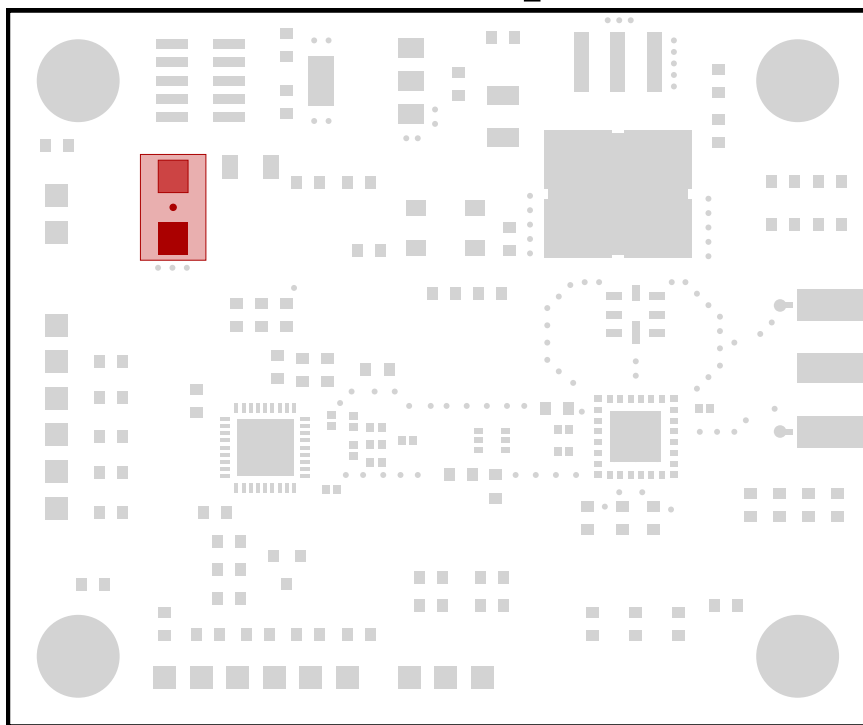


L3, L8

A complex, abstract black and white diagram, possibly a technical or architectural plan. It features a central grid of squares, surrounded by various geometric shapes like circles, rectangles, and lines, suggesting a technical or architectural plan. The diagram is highly detailed and symmetrical, with a central focus on a grid of squares. The overall layout is intricate, with many small elements and lines connecting different parts of the diagram. The central grid is composed of a 4x4 arrangement of squares, with additional squares and lines extending outwards in various directions. The diagram is framed by a thick black border, and the background is white. The overall impression is one of a highly detailed and complex technical drawing.

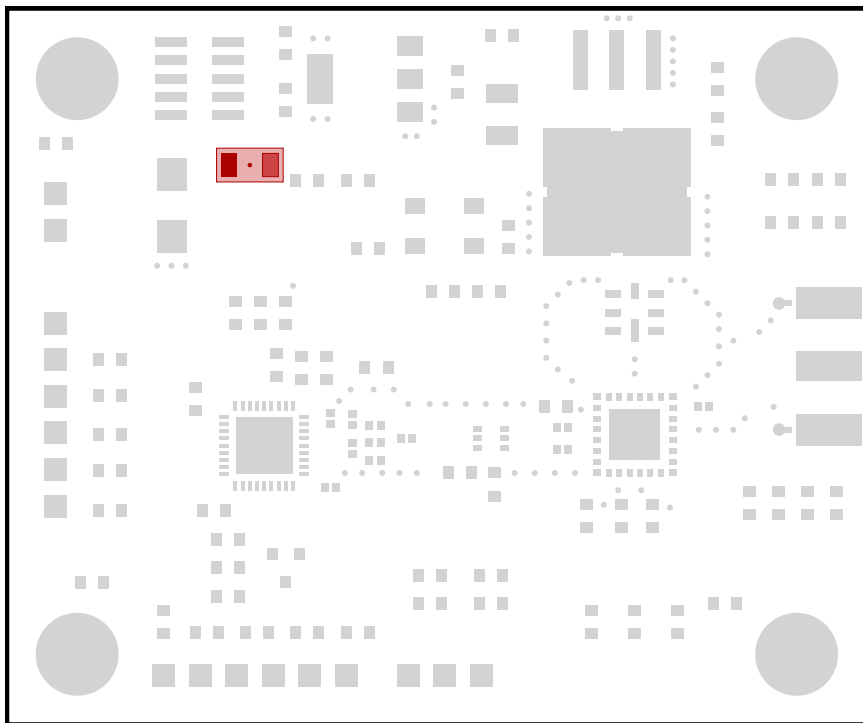
L5, L6

1x STPS340U, D\_SMB



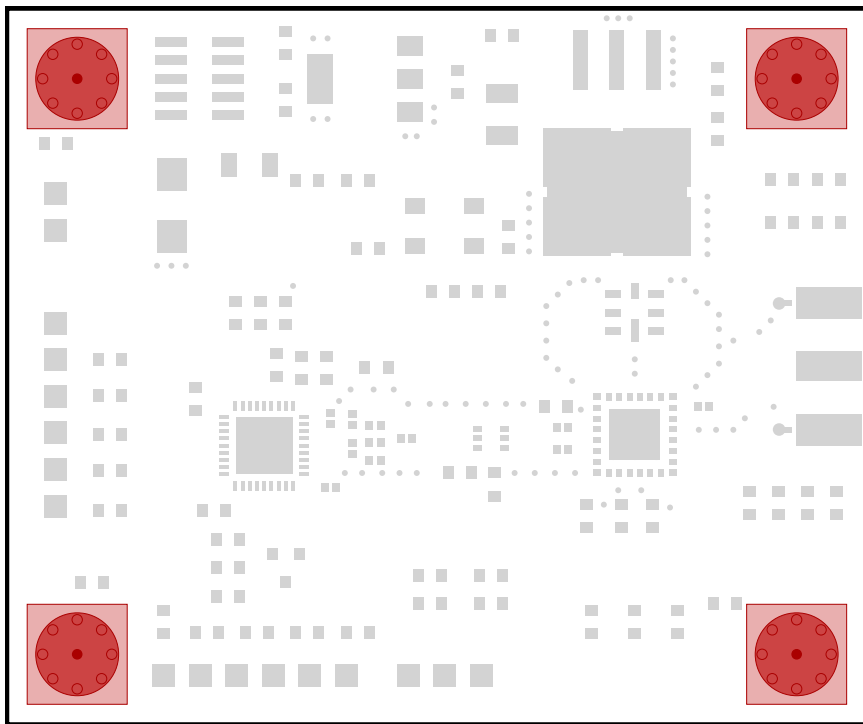
D3

1x Z=500 @ 100M, L\_1206\_3216Metric



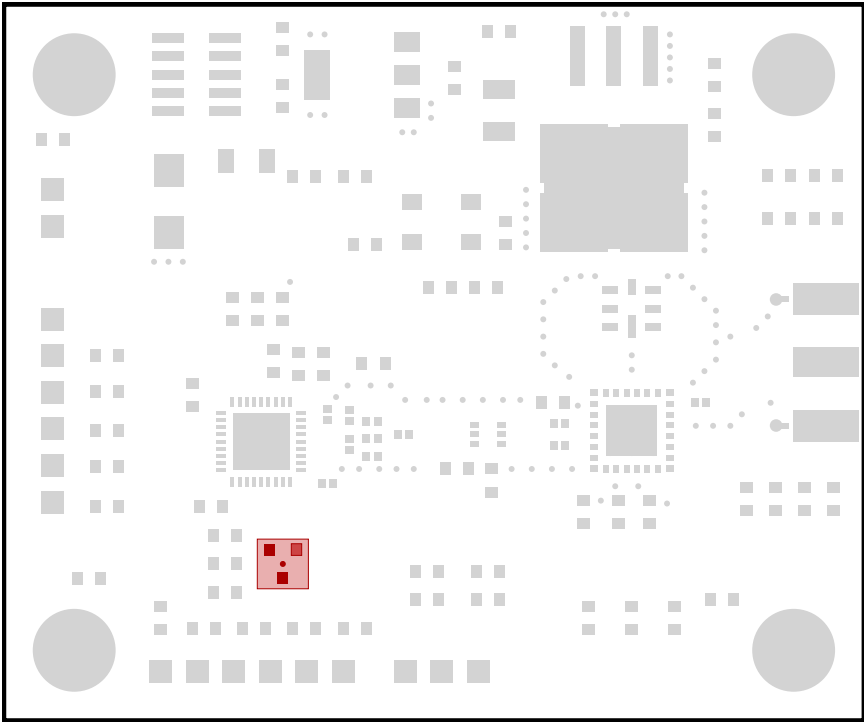
L2

4x M3\_Hole, MountingHole\_3.2mm\_M3\_Pad\_Via



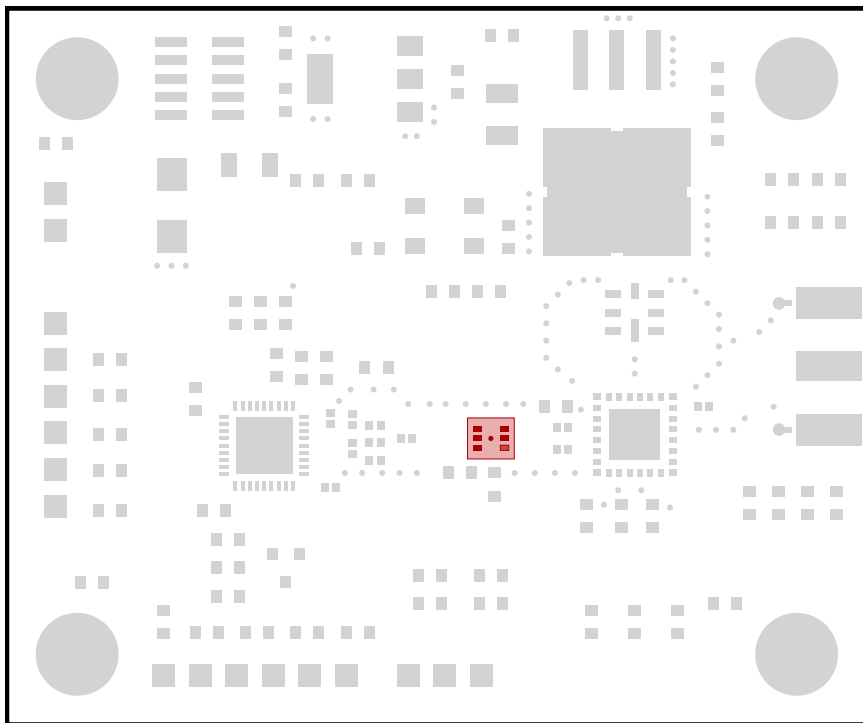
MK1, MK2, MK3, MK4

1x TLV803S, SOT-23



U5

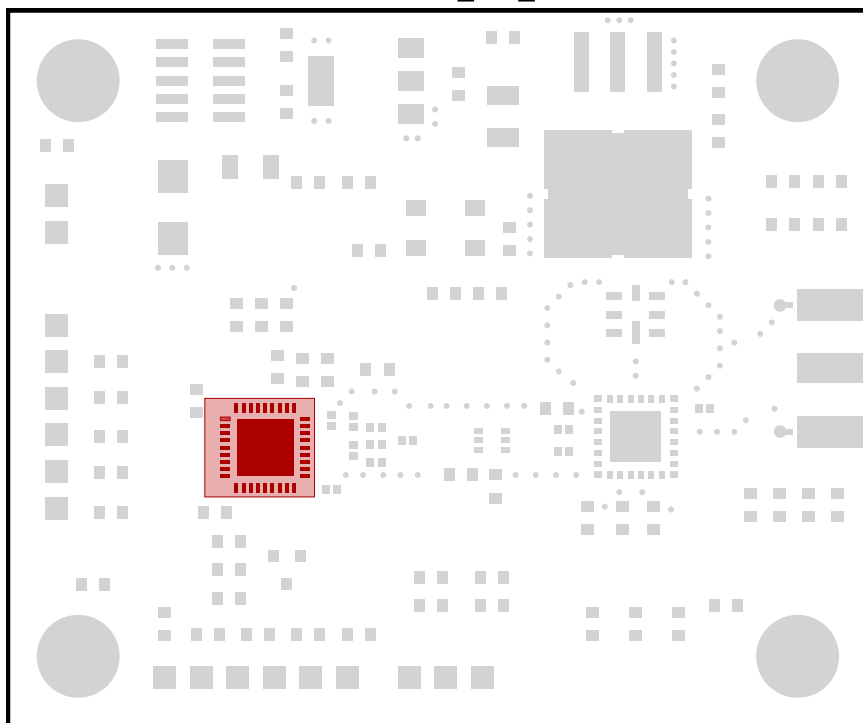
1x PE4259, SOT-363\_SC-70-6



U6

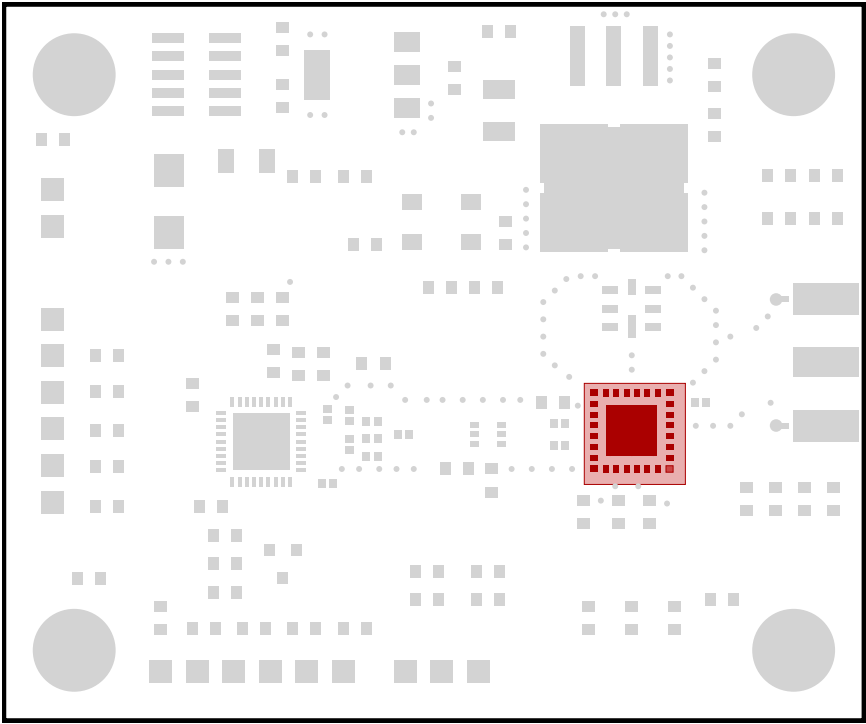


1x CC1110, TI\_CC\_QFN36



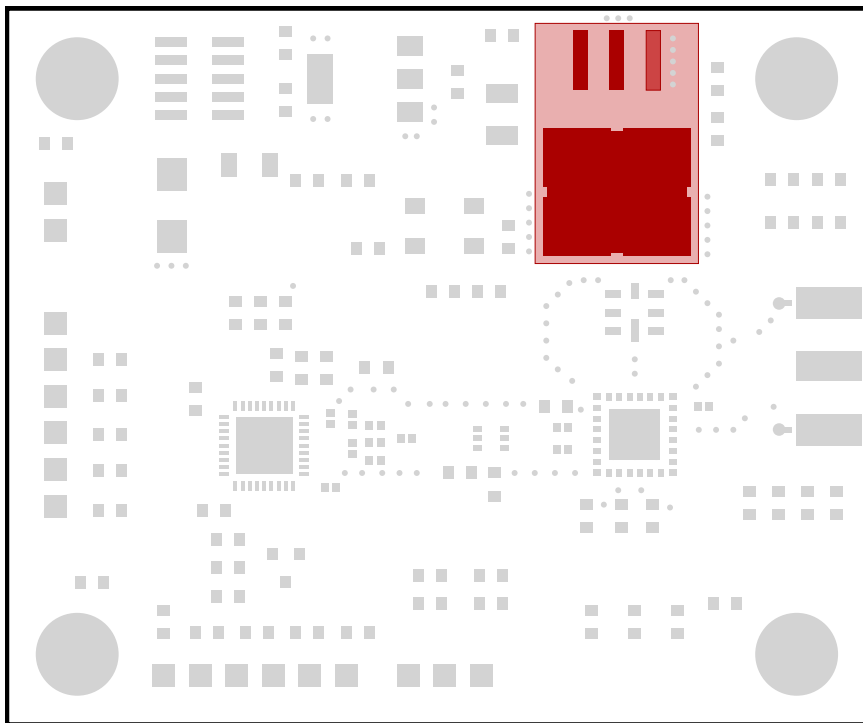
U2

1x RFFM6403, Qorvo\_LGA\_28\_ThermalVias



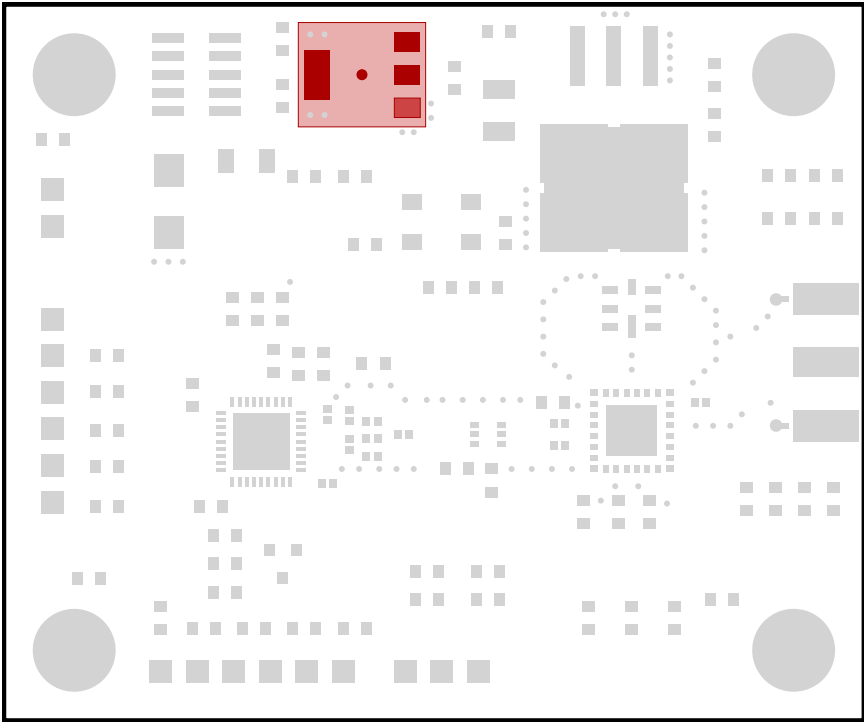
U9

1x LT1086-3.6, TO-263-3\_TabPin2



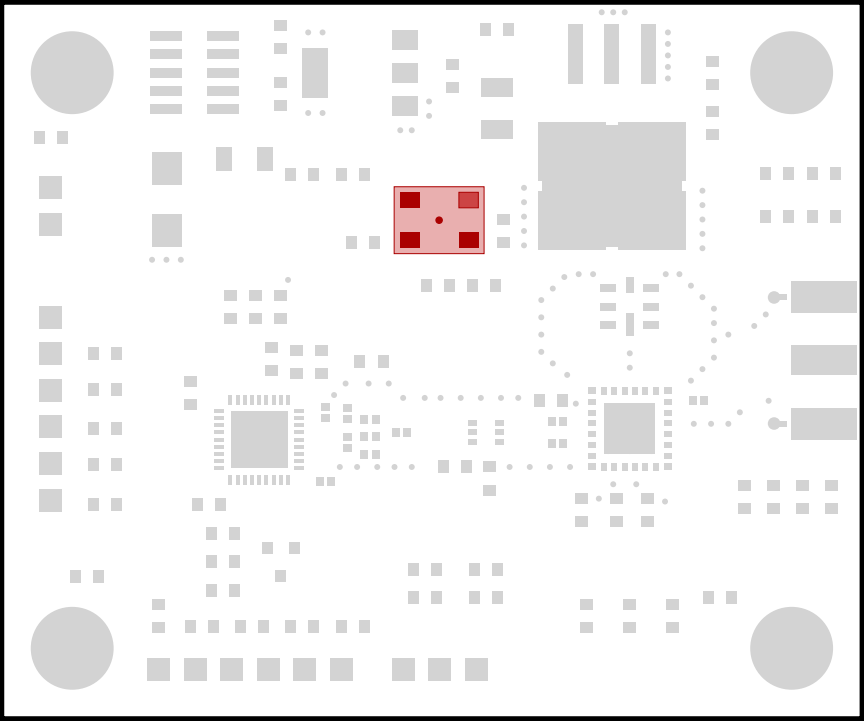
U3

1x LM1117-3.3, SOT-223-3\_TabPin2



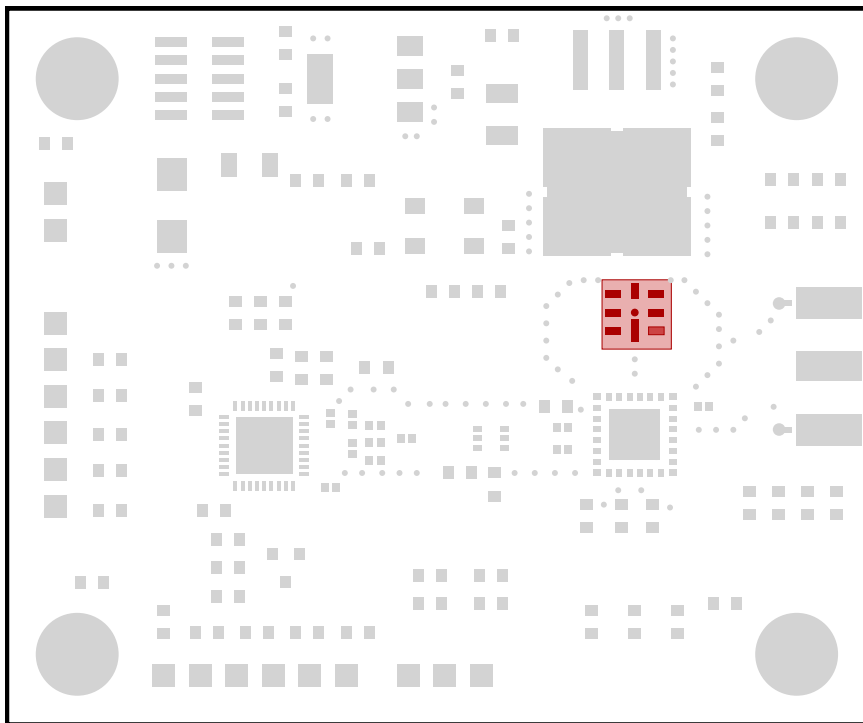
U1

1x FOX924 27MHz,  
Oscillator\_SMD\_Fordahl\_DFAS15-4Pin\_5.0x3.2mm



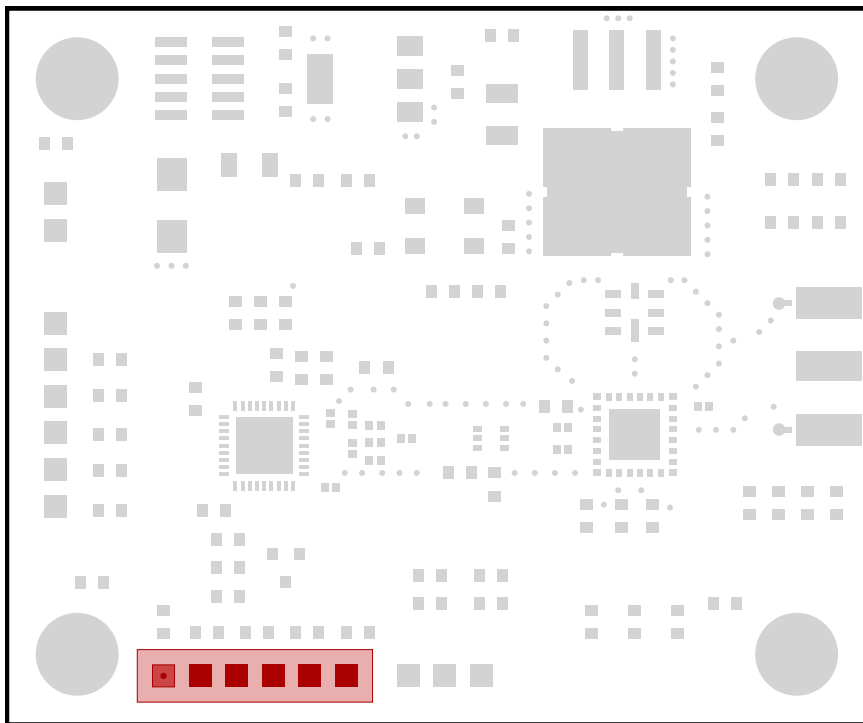
U4

1x STA1120A, SAW-8\_3.8mm



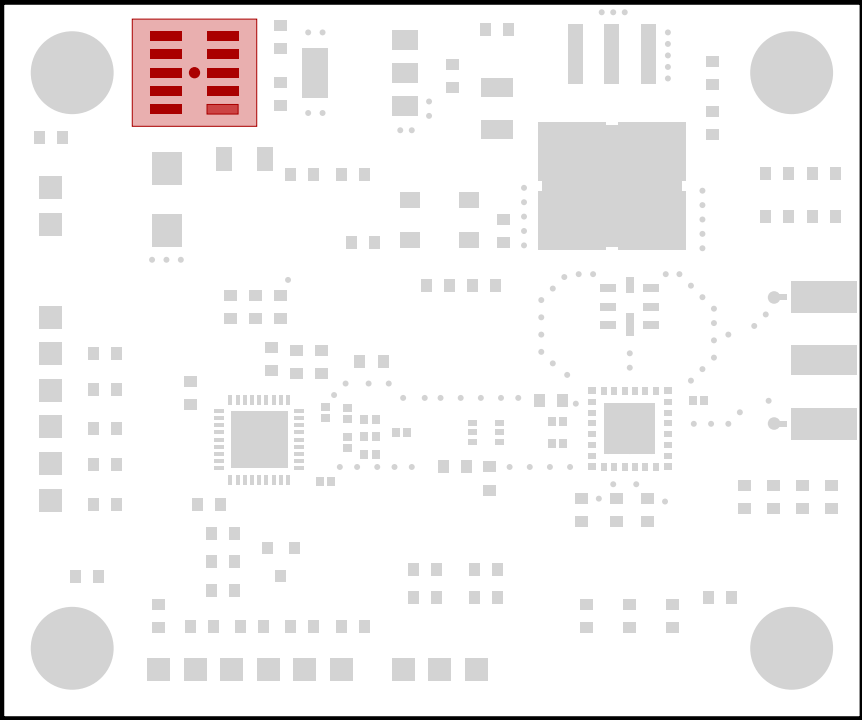
U8

1x UART1, PinHeader\_1x06\_P2.54mm\_Vertical



J4

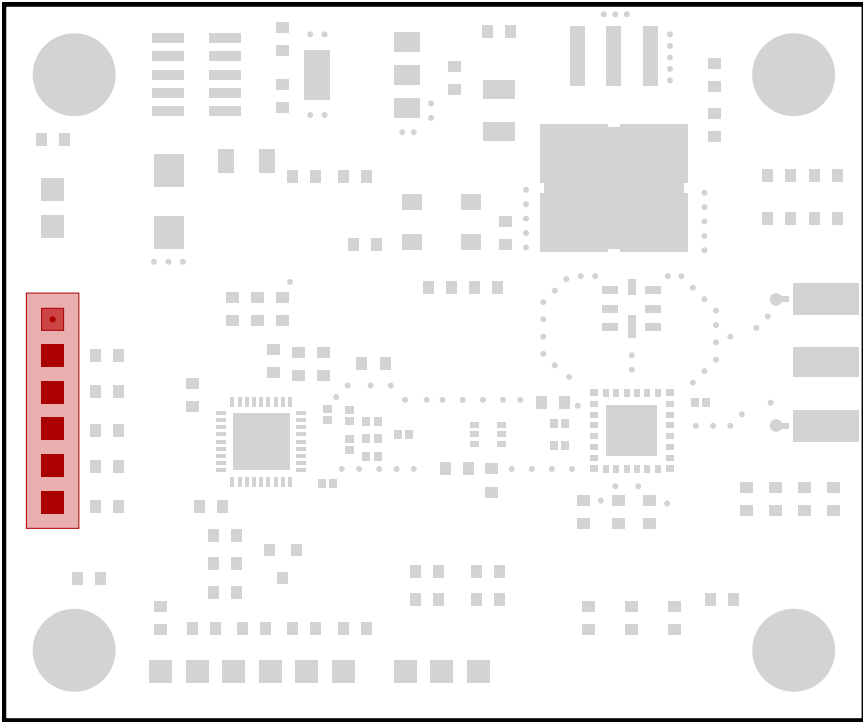
1x CC Prog/Debug, PinHeader\_2x05\_P1.27mm\_Vertical\_SMD



J2

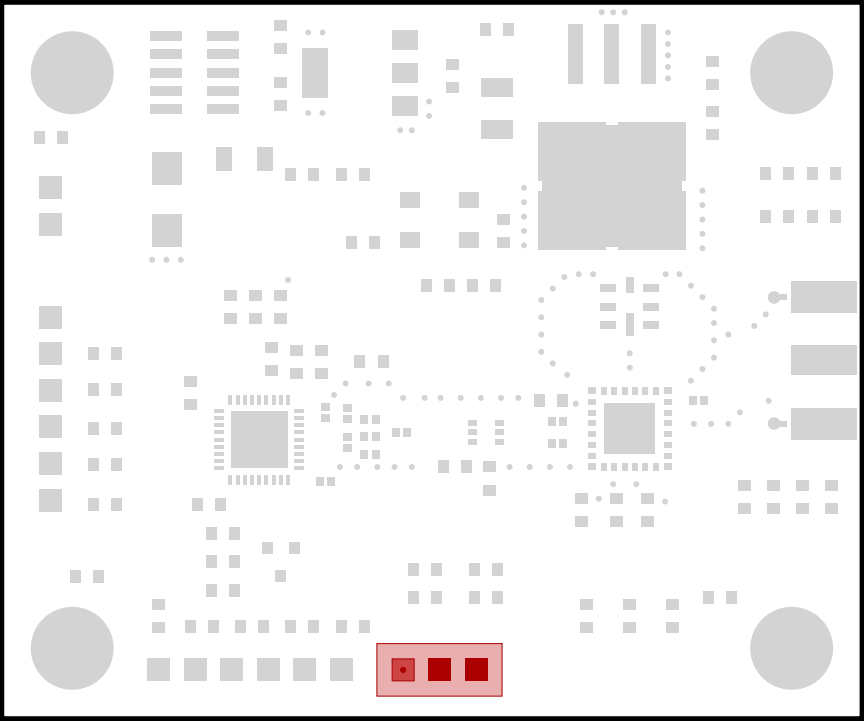


1x UART0, PinHeader\_1x06\_P2.54mm\_Vertical



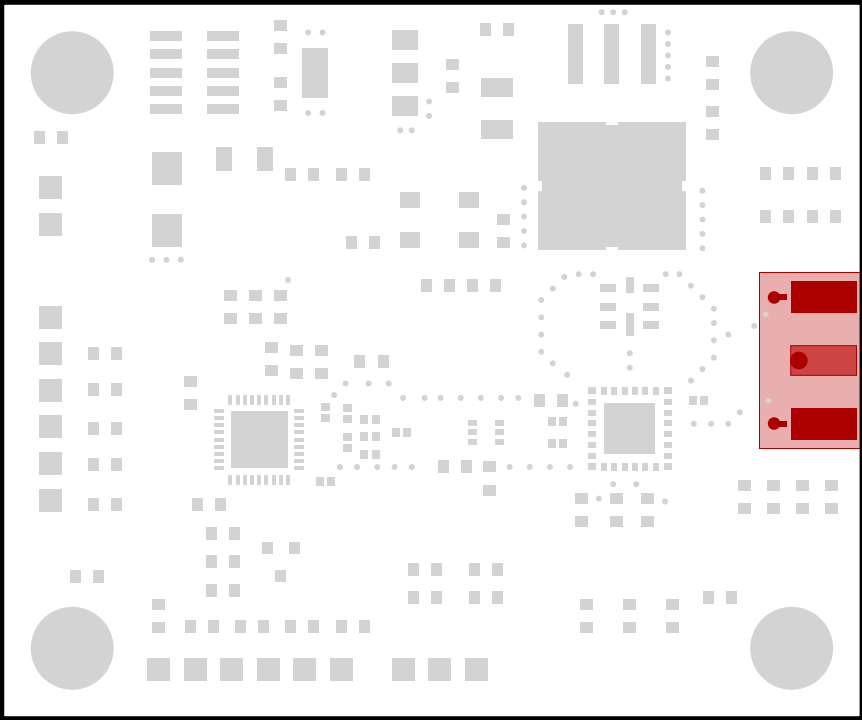
J3

1x Control Outputs, PinHeader\_1x03\_P2.54mm\_Vertical



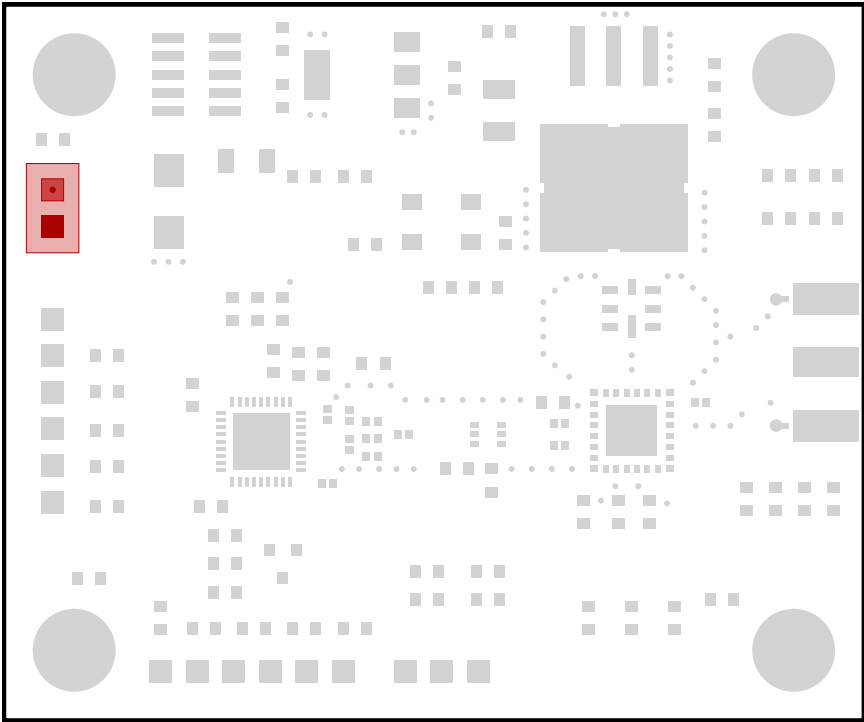
J5

1x ANT, SMA\_Molex\_73251-1153\_EdgeMount\_Horizontal



J6

1x 5VDC, PinHeader\_1x02\_P2.54mm\_Vertical



J1