(25×10°) x"017D7840" SW[7..07 SW[14] +32 K DATA-IN LOAD SW[15] RESET DELUIN CLOCK-50 · SW [17] CLUDUT ENABLE (2HZ) SW [16] (50 HHZ) UPDOWN K COUNT COUNTER FREQ DIVIDER > LEDR[7.0] COUNT[3.0] COUNT[7.47V BIN-IN BIN-IN BINTSEG BIN 75EG DECODER DECODER DEC-OUT DEC-OUT HEX1 HEXO