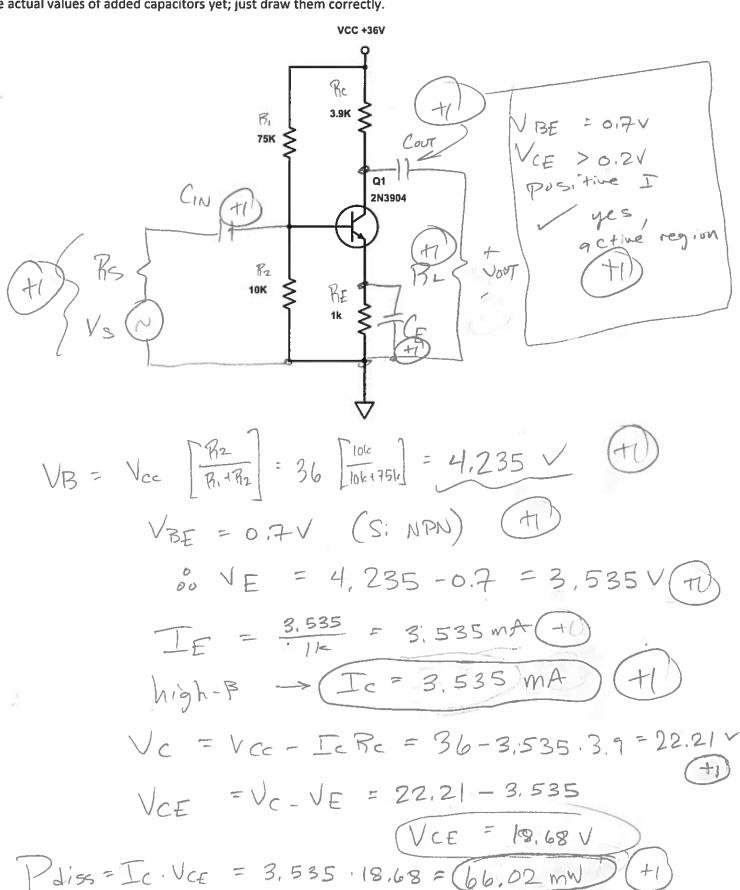
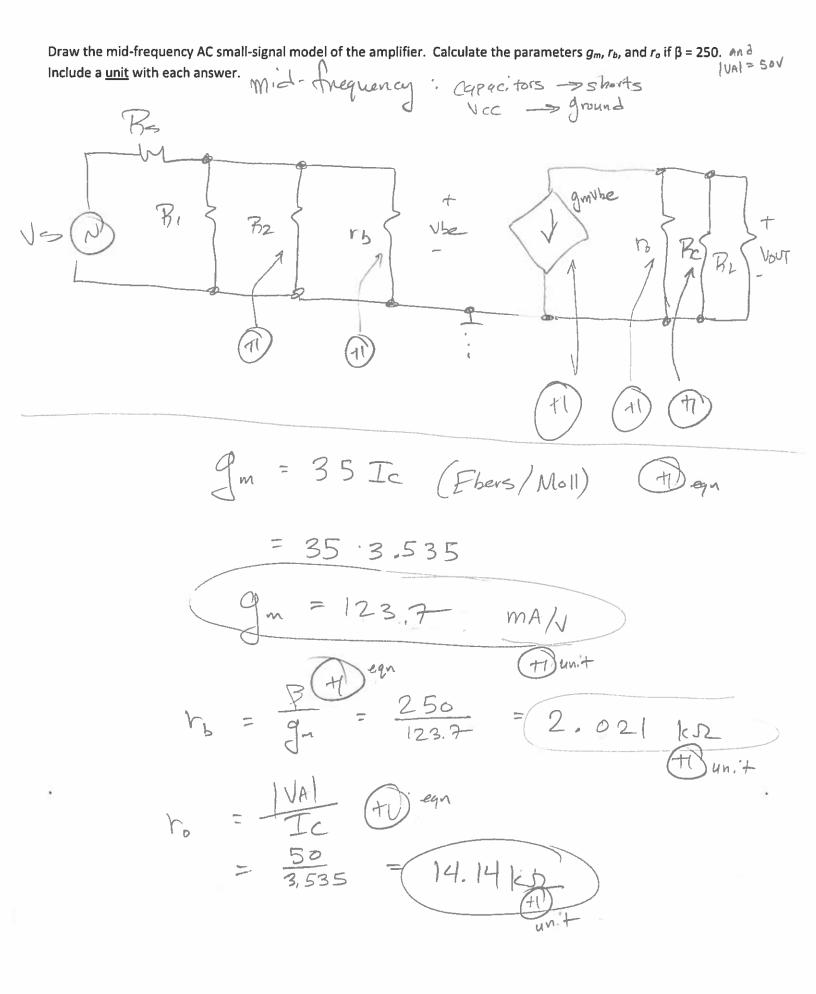
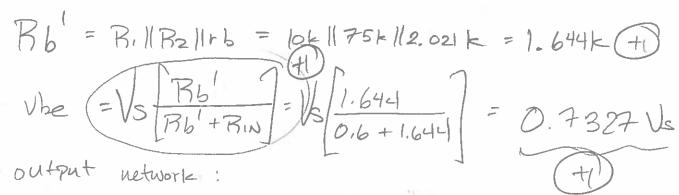
Determine the D.C. operating point of the following circuit ( $I_c$  and  $V_{ce}$ ) if base current may be assumed negligible. Check  $P_{diss}$  and verify that the transistor is operating in the active region. Draw additional components on the circuit to create a common-emitter voltage amplifier with a source resistance of 600  $\Omega$  and a load resistance of 25 k $\Omega$ . Don't worry about the actual values of added capacitors yet; just draw them correctly.



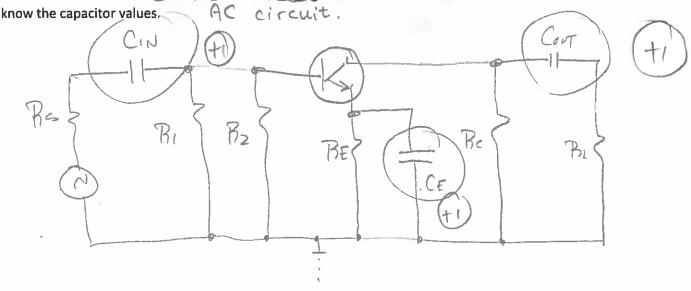


Determine the mid-frequency gain  $A_V$  and  $A_V$ (dB). The approximate Approxi

Input network:



Draw the low-frequency mediagram and computer anything-because you don't



Determine the high-frequency input and output capacitances using Miller's Theorem if  $C_{BC} = 6$  pF and  $C_{BE} = 22$  pF.

Compute the input and output HF cutoff frequencies and the approximate overall high-frequency cutoff,  $f_H$ .