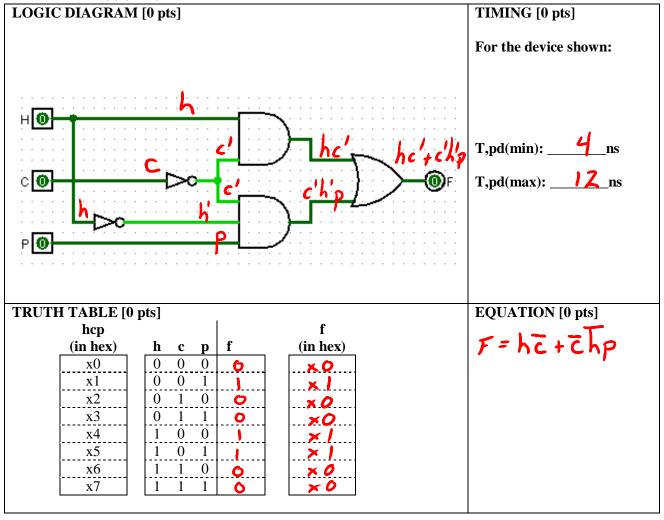
Half-hour Examination #1 - 30 minutes Closed Book, one 8.5x11" page of notes (double-sided)

NAME	Pilot ID: w	SCORE	/ 20

All problems on this examination require you to analyze combinational devices and representations and to produce equivalent forms of representation. For each problem, you will be provided with one form of representation: a logic diagram, a Boolean equation, or a truth table. Your task it to provide equivalent representations for the same functionality for all of the representations not provided. For example, if you are given a truth table, you must provide a functionally equivalent equation and a logic diagram. Make certain to use good documentation practices! Label ALL connections in your diagrams appropriately!

For timing questions involving propagation delay, assume that all combinational gates in the logic diagram have a minimum propagation delay equal to the number of inputs to the device (in nanoseconds) and a maximum propagation delay equal to twice the number of its inputs. For example, a 2-input AND gate would have a $t_{pd,min} = 2ns$ and a $t_{pd,max} = 4ns$. A 3-input AND gate would have a $t_{pd,min} = 3ns$ and a $t_{pd,max} = 6ns$.

EXAMPLE Problem #0



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Problem #1	
LOGIC DIAGRAM [0 pts]	TIMING [2 pts]
	For the device shown:
3 0	
(
	T,pd(min):ns
	T,pd(max):ns
TOUTH TADLE [2]	EQUATION (24-1
TRUTH TABLE [2 pts]	EQUATION [2 pts]
abc xy	
(in hex) a b c x y (in hex)	
x0 0 0 0	
x1 0 0 1	
x2 0 1 0 1 0	
x3 0 1 1	
x4 1 0 0	
x5 1 0 1	
x6 1 1 0	
x7 1 1 1	

EXAMPLE Problem #2

LOGIC DIAGRAM [3 pts]			TIMING [2 pts]		
					For the device shown:
					T,pd(min):ns
					T,pd(max):ns
TRITT	H TABLE [0	ntel			EQUATION [2 pts]
11011	abc	pts]		xy	EQUATION [2 pts]
	abc			X V	
		,			
	(in hex)			(in hex)	
		a b c 0 0	x y 0 0		
	(in hex)			(in hex)	
	(in hex)	0 0 0	0 0	(in hex)	
	(in hex) x0 x1	$\begin{array}{c cc} 0 & 0 & 0 \\ \hline 0 & 0 & 1 \end{array}$	$\begin{bmatrix} 0 & 0 \\ 0 & 0 \end{bmatrix}$	(in hex) 	
	(in hex)	$\begin{array}{c cccc} 0 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 1 & 0 \\ \end{array}$	0 0 0 0 1 0	(in hex)	
	(in hex) x0 x1 x2 x3	$\begin{array}{c cccc} 0 & 0 & 0 \\ 0 & 0 & 1 \\ \hline 0 & 1 & 0 \\ \hline 0 & 1 & 1 \\ \end{array}$	0 0 0 0 1 0 1 0	(in hex)	
	x0 x1 x2 x3 x4	$\begin{array}{c cccc} 0 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 1 & 0 \\ 0 & 1 & 1 \\ 1 & 0 & 0 \\ \end{array}$	0 0 0 0 1 0 1 0 1 0	(in hex)	
	x0 x1 x2 x3 x4 x5	0 0 0 0 0 1 0 1 0 0 1 1 1 0 0 1 0 1	0 0 0 0 1 0 1 0 1 0 1 1	(in hex) x0 x0 x2 x2 x2 x3	

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Problem #3			

I I UDICIII II J				
LOGIC DIAGRAM	[3 pts]			TIMING [2 pts]
				For the device shown:
				T,pd(min): ns
				T,pd(max): ns
TRUTH TABLE [2]	pts]	_		EQUATION [0 pts]
abc			xy	
(in hex)	a b c	x y	(in hex)	$\mathbf{X} = (\mathbf{AB} + \mathbf{C})(\mathbf{B} + \mathbf{AC})$
x0	0 0 0			Y = ABC
x1	0 0 1			
x2	0 1 0			
x3	0 1 1			
x4	1 0 0			
x5	1 0 1			
x6	1 1 0			
x7	1 1 1			
11.7				

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DO NOT BEGIN UNTIL INSTRUCTED TO DO SO
HONOR CODE: Before the end of the examination, please sign:
In recognition of and in the spirit of the Wright State University policies of academic honesty, I certify that I have neither given nor received unpermitted aid in this examination.
Name (Printed):
Signature:

DO NOT BEGIN UNTIL INSTRUCTED TO DO SO