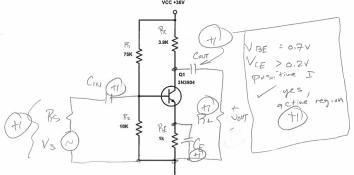


Determine the DC operating point of the following circuit. If  $V_{BE}$  or base current may be assumed negligible. Check  $\beta$  and verify that the transistor is operating in the active region. Draw additional components on the circuit to create a common-emitter voltage amplifier with a source resistance of  $400 \Omega$  and a load resistance of  $25 \text{ k}\Omega$ . Don't worry about the exact values of added capacitors just draw them correctly.



$$V_B = V_{CC} \left[ \frac{R_B}{R_B + R_E} \right] = 3.6 \left[ \frac{1k}{1k+3.3k} \right] = 4.235 \text{ V} \quad (1)$$

$$V_{BE} = 0.7 \text{ V} \quad (\text{S. NPN}) \quad (1)$$

$$\therefore V_E = 4.235 - 0.7 = 3.535 \text{ V} \quad (1)$$

$$I_E = \frac{3.535}{1k} = 3.535 \text{ mA} \quad (1)$$

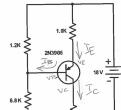
$$\text{high-f.} \rightarrow I_E = 3.535 \text{ mA} \quad (1)$$

$$V_{CE} = V_{CC} - I_E R_C = 3.6 - 3.535 \cdot 3.3 = 20.21 \text{ V} \quad (1)$$

$$V_{CE} = V_C - V_E = 22.21 - 3.535 \quad (1)$$

$$P_{diss} = I_E \cdot V_{CE} = 3.535 \cdot 18.68 = 66.02 \text{ mW} \quad (1)$$

1) Refer to the following circuit:



Compute  $V_A$ ,  $V_B$ ,  $V_E$ ,  $V_{CE}$ ,  $I_C$ ,  $I_E$ , and  $P_{diss}$  if  $\beta = 250$ . Compute  $V_A$  assuming base current is small compared to the current in the biasing resistors. Also verify that the transistor is operating in the active region.

$$V_B = 1.8 \left[ \frac{6.8}{1.2+6.8} \right] = 15.3 \text{ V} \quad (2)$$

$$V_{BE(\text{assum})} = -0.7 \text{ V} \quad (\text{NPN}) \quad (1)$$

$$\therefore V_E = V_B - V_{BE} \quad (1)$$

$$\therefore 15.3 - 0.7 = 14.6 \text{ V} \quad (1)$$

$$I_E = \frac{1.8}{1k} = \frac{1.8 - 14.6}{1k} = 2 \text{ mA} \quad (1)$$

$$I_B = \frac{I_E}{\beta} = \frac{2 \text{ mA}}{250} = 0.008 \text{ mA} \quad (1)$$

$$I_C = I_E \quad (1)$$

$$V_C = V_C - I_C \cdot 3.3k = 6.6 \text{ V} \quad (1)$$

$$V_{CE} = V_C - V_E = 6.6 - 1.8 = 4.8 \text{ V} \quad (1)$$

$$P_{diss} = V_{CE} \cdot I_C = 4.8 \cdot 2 = 9.6 \text{ mW} \quad (1)$$

Determine the mid-frequency gain  $A_V$  and  $A_{(d)}$ .

Input network:

$$R_{B1}' = R_B / (R_B + R_L) = 1.0k / (7.5k / 25k) = 1.64k \text{ }\Omega \quad (1)$$

$$V_{BE} = \sqrt{V_B / [R_{B1}' + R_{IN}]} = \sqrt{1.8 / [1.64k + 0.6k]} = 0.3327 V \quad (1)$$

output network:

$$R_C' = R_C / (R_C + R_L) = 14.0k / (9.9k / 25k) = 2.32k \text{ }\Omega \quad (1)$$

$$V_{out} = -g_m V_{BE} R_C' \quad (1)$$

$$= -12.97 \cdot 0.3327 V \cdot 2.32k \quad (1)$$

$$V_{out} = -2.469 \text{ V} \quad (1)$$

$$A_V = -2.469 \text{ V} \quad (1)$$

$$\text{or } (+4.735 \text{ dB}, \text{ inverting}) \quad (1)$$

Draw the low-frequency  $AC$  circuit. You do not have to compute anything because you know the capacitor values.

AC circuit:

know the capacitor values.

You do not have to compute anything because you know the capacitor values.

AC circuit.

You do not have to compute anything because you know the capacitor values.

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AC circuit.

Determine the high-frequency input capacitance using Miller's Theorem if  $C_{in} = 4.0 \text{ pF}$  and  $C_{out} = 10 \text{ pF}$ . Use it to compute the input HF cutoff frequency. You do not have to compute the output capacitance or cutoff frequency.

$$C_{BC}(in) = C_{BC}(1 - A_{v1})$$

$$= 4(1 - 23.31) = 97.24 \text{ pF} \quad (1)$$

$$\therefore f_H = \frac{1}{2\pi \cdot C_{BC}(in) \cdot (R_{in}/R_{in})} = \frac{1}{2\pi \cdot 97.24 \text{ pF} \cdot (100k/100k)} = \frac{1}{2\pi \cdot 97.24 \text{ pF} \cdot 1} = 20.25 \text{ kHz} \quad (2)$$

Compute the LF cutoff frequencies due to the input and output capacitors.

$$f_{C_{in}} = \frac{1}{2\pi C_{in} (R_{in} + R_{in})} = \frac{1}{2\pi \cdot 100 \text{ pF} \cdot (100k + 100k)} = 20.25 \text{ kHz} \quad (3)$$

$$f_{C_{out}} = \frac{1}{2\pi C_{out} (R_{out} + R_{out})} = \frac{1}{2\pi \cdot 100 \text{ pF} \cdot (100k + 100k)} = 20.25 \text{ kHz} \quad (4)$$

$$f_{C_{in}+out} = 14.47 \text{ Hz} \quad (5)$$

Compute the three LF cutoff frequencies and the approximate overall low-frequency cutoff,  $f_L$ .

$$R_{CE} = \frac{1}{g_m \parallel R_{RE}} = \frac{1}{6.89} = 14.25 \text{ k}\Omega \quad (6)$$

$$f_{CE} = \frac{1}{2\pi \cdot 14.25 \cdot 1000 \times 10^{-6}} = 11.14 \text{ Hz} \quad (7)$$

$$R_{C_E} = R_E + (R_E || R_D) = 10k + (4.95k \parallel 13.6k) = 14.58 \text{ k}\Omega \quad (8)$$

$$f_{C_E} = \frac{1}{2\pi \cdot 14.58 \cdot 10^6} = 1.091 \text{ Hz} \quad (9)$$

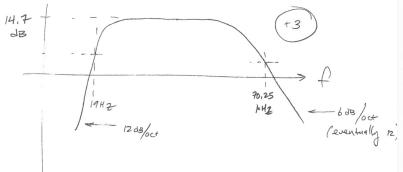
$$R_{C_{in}} = R_{in} + R_{in}^{\prime} = 0.04938k + 1.52k = 1.564k \quad (10)$$

$$f_{C_{in}} = \frac{1}{2\pi \cdot 1.564 \times 10^6 \cdot 1.564k} = 10.14 \text{ Hz} \quad (11)$$

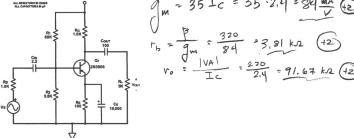
= dominant pole;  $f_L \approx 10.14 \text{ Hz}$

Finally, determine the approximate overall cutoff frequency  $f_L$  and sketch the overall magnitude response of the Electro Harmonix LPB-1.

$$f_L = \sqrt{12.2^2 + 14.47^2} = 18.93 \text{ Hz} \quad (12)$$



2) A silicon PNP transistor is used in a common-emitter amplifier configuration. Collector current is known to be 2.4 mA and the transistor has an Early voltage of 220 V and beta of 320. Calculate the parameters  $\beta_m$ ,  $\beta_0$ ,  $\beta_d$  and  $\beta_e$ .



Determine the mid-frequency small-signal gains  $A_{v1}$  and  $A_{v2}$  and the overall gain of the amplifier in dB.

$$R_{B1} = \frac{V_{BE}}{I_C} = \frac{V_{BE}}{2.4} = 2.14 \text{ k}\Omega \quad (1)$$

$$A_{v1} = \frac{V_{out}}{V_{in}} = \frac{R_L}{R_E + R_L} = \frac{2.4}{2.4 + 2.14} = 0.645 \text{ dB} \quad (2)$$

$$A_{v2} = -\frac{V_{out}}{V_{in}} = -\beta_m \left( \frac{R_L}{R_E + R_L} \right) = -84 \left( \frac{0.645}{1.14} \right) = -69.37 \text{ dB} \quad (3)$$

$$\text{overall midband gain: } -3.3 + 36.8 = 33.5 \text{ dB (including } 2.14 \text{ dB)}$$

Determine the high-frequency input capacitance using Miller's Theorem if  $C_{in} = 5.0 \text{ pF}$  and  $C_{out} = 22 \text{ pF}$ . Use it to compute the input HF cutoff frequency. You do not have to compute the output capacitance or cutoff frequency.

$$C_{BC}(in) = C_{BC}(1 - A_{v1}) = 4.5(1 - 69.37) = 316.7 \text{ pF} \quad (4)$$

$$C_{in} = C_{BC}(in) + C_{BE} = 316.7 + 1.2 = 327.9 \text{ pF} \quad (5)$$

$$f_H(in) = \frac{1}{2\pi C_{in} R_{in}/R_{in}} = \frac{1}{2\pi \cdot 327.9 \times 10^{-12} \cdot (1k/1m)} = 16.0 \text{ kHz} \quad (6)$$

$$f_H(out) = 104.9 \text{ kHz} \quad (7)$$

Determine the LF output frequency caused by the output capacitor. Hint: we know the output resistance of the amplifier, so we can compute the resistance "seen" by  $C_{out}$ . Finally, determine the approximate overall cutoff frequency  $f_L$ .

$$j_m = 35 I_C = 35 \cdot 2.4 = 84 \text{ mA} \quad (8)$$

$$\beta_0 = \frac{j_m}{j_m - j_d} = \frac{320}{320 - 3.81} = 3.81 \text{ k}\Omega \quad (9)$$

$$r_o = \frac{V_A}{I_C} = \frac{220}{2.4} = 91.67 \text{ k}\Omega \quad (10)$$

$$\therefore f_{out} = \frac{1}{j_m r_o} = \frac{1}{16.0 \cdot 91.67 \times 10^3} = 6.030 \text{ Hz} \quad (11)$$

$$P_{out} = P_{out} + P_L = 6.030 + 5k \approx 5k \quad (12)$$

$$f_L(\text{out}) = \frac{1}{2\pi \cdot C_{out} \cdot R_{out}} = \frac{1}{2\pi \cdot 100 \times 10^{-12} \cdot 5k} = 12.56 \text{ Hz} \quad (13)$$

$$f_L(\text{out}) = 3.143 \text{ Hz} \quad (14)$$

$$f_L \text{ dominated by } f_{C_{in}} \quad [eh, \text{ maybe...}] \quad \rightarrow \text{not } 5x! \quad (15)$$

$$\therefore f_L = 47\text{-ish Hz} \quad (16)$$

(in reality a little higher)

Determine the mid-frequency gain  $A_{v1}$  and  $A_{v2}$  (dB).

$$f_b^1 = 216.16 / 3.32k = 2.985 \text{ k}\Omega$$

$$= 1.520 \text{ k}\Omega \quad (17)$$

$$P_S = 0.75k / 10k = 0.04938 \text{ k} \quad (18)$$

$$V_{be} = V_S \left( \frac{1.520}{1.520 + 0.04938} \right) = 0.9685 \text{ V} \quad (19)$$

$$P_C^1 = 130.66 / 4.75k / 10k = 3.143 \text{ k}\Omega \quad (20)$$

$$V_{out} = -g_m V_{be} P_C^1 = -67 \cdot 0.9685 \text{ V} \cdot 3.143 \text{ k}\Omega \quad (21)$$

$$AV = \frac{V_{out}}{V_S} = -203.9 \quad (22)$$

or 46.2 dB inverting

Compute the LF cutoff frequencies due to the input and emitter capacitors.

$$f_{C_{in}} = \frac{1}{2\pi C_{in} (R_{in} + R_{in})} = \frac{1}{2\pi \cdot 100 \times 10^{-12} \cdot (1.25k + 2.18k)} = 12.5 \text{ Hz} \quad (23)$$

$$f_{C_{in}} = 47.09 \text{ Hz} \quad (24)$$

no penalty if  $R_{in}$  is included!

$$R_{CE} \approx \frac{1}{g_m} = \frac{1}{24.2 \times 10^{-3}} = 8.052 \text{ k}\Omega \quad (25)$$

$$f_{CE} = \frac{1}{2\pi \cdot C_E \cdot R_{CE}} = \frac{1}{2\pi \cdot 100 \times 10^{-6} \cdot 8.052} = 19.77 \text{ Hz} \quad (26)$$

$$f_{CE} = 19.77 \text{ Hz} \quad (27)$$

What type of transistor is  $Q_0$  and in what amplifier configuration is it being used?

NPN (1)  
common-collector emitter follower (1)

Determine  $V_{BE}$ ,  $V_{CE}$ ,  $I_{E2}$  and  $V_{CE2}$  if base current may be assumed negligible due to high  $\beta$ . Check  $P_{out}$  and verify that the transistor is operating in the active region. Note: we already know  $V_{be}$  due to the direct-coupled connection to  $Q_0$ . Also determine  $g_{os}$ .

$$V_{BE2} = V_{B2} - V_{BE1} = 7.807 - 0.7 = 7.107 \text{ V} \quad (1)$$

$$I_{E2} = \frac{V_{B2}}{R_{E2}} = \frac{7.107}{1.5k} = 4.738 \text{ mA} \quad (2)$$

$$V_{CE2} = V_{B2} - V_{E2} = 20 - 7.107 = 12.89 \text{ V} \quad (3)$$

$$\downarrow = V_{CC} \quad \text{yes, active} \quad (4)$$

$$P_{out2} = I_{E2} \cdot V_{CE2} = 4.738 \cdot 12.89 = 61.09 \text{ mW} \quad (5)$$

Calculate the parameters  $\beta_m$ ,  $\beta_0$  and  $\beta_d$  if  $V_S = 200$  V and the Early voltage  $|V_A| = 200$  V. Include all with each term.

$$\begin{aligned} \beta_0 &= \frac{V_A}{V_A + V_{BE1}} = \frac{200}{200 + 0.7} = 2.885 \text{ k}\Omega \\ &= 2.00 \text{ k}\Omega \quad (6) \end{aligned}$$

$$\begin{aligned} \beta_d &= \frac{V_A}{V_A + V_{BE2}} = \frac{200}{200 + 7.107} = 130.6 \text{ k}\Omega \quad (7) \\ &= 7.50 \text{ k}\Omega \quad (8) \end{aligned}$$

$$\begin{aligned} \beta_m &= \frac{V_A}{V_A + V_{BE1}} = \frac{200}{200 + 0.7} = 2.885 \text{ k}\Omega \\ &= 2.00 \text{ k}\Omega \quad (9) \end{aligned}$$