

The **inertial delay** model is the default delay implemented in VHDL because it's behavior is very similar to the delay of the device. The delay assignment syntax is:

$b \leq a$ **after** 20 ns;

In this example **b** take the value of **a** **after** 20 ns second of inertial delay.

This means that **if a value varies faster than 20 ns then b remain unchanged**. This simulation should clarify the concept.

