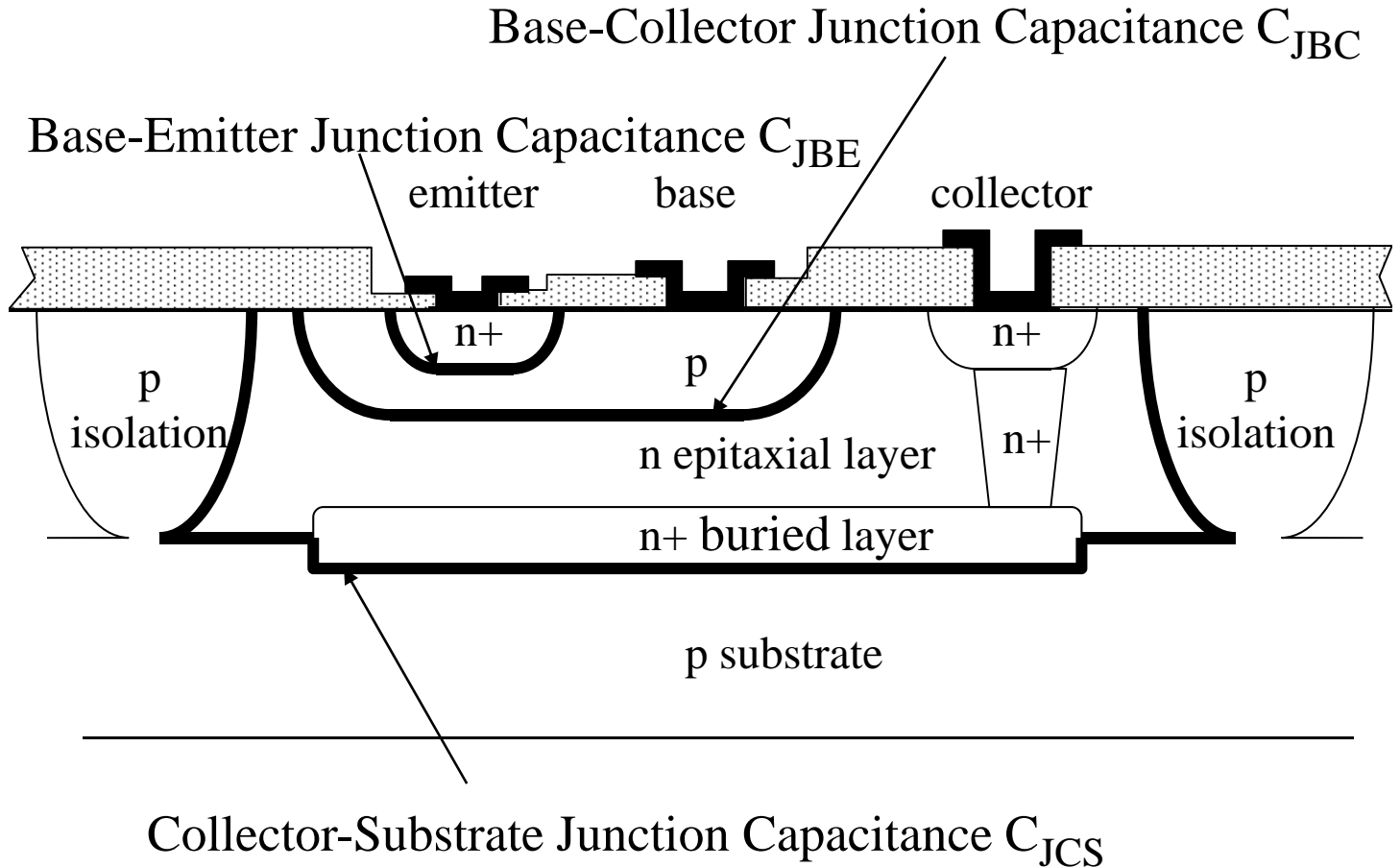


Q1 (b) 4 marks

The main junction capacitances of the BJT are as follows:



Q1 (c) 4 marks Expression for base-emitter capacitance

$$C_{JBE} = \frac{C_{JE}}{\left(1 - \frac{V_{BE}}{V_{JE}}\right)^{M_{JE}}} \quad V_{BE} \leq FC \cdot V_{JE}$$

$$Q = \tau I_C \quad C_{DE} = \frac{dQ}{dV_{BE}} = \frac{d(\tau I_C)}{dV_{BE}} = \tau \frac{dI_C}{dV_{BE}} = \tau g_m$$

$$C_{BE} = C_{JE} + C_{DE}$$

Q1 (a) 4 marks

The low-frequency small-signal elements of the BJT are as follows:

$$I_C = I_S \left(e^{\frac{qV_{BE}}{kT}} - 1 \right) \left(1 + \frac{V_{CE}}{V_A} \right) \quad I_B = \frac{I_C}{\beta}$$

$$g_m = \frac{dI_C}{dV_{BE}} = \frac{1}{V_T} I_S e^{\frac{V_{BE}}{V_T}} \left(1 + \frac{V_{CE}}{V_A} \right) = \frac{I_C}{V_T}$$

$$g_{out} = \frac{dI_C}{dV_{CE}} = \frac{1}{V_A} I_S e^{\frac{V_{BE}}{V_T}} \approx \frac{I_C}{V_A}$$

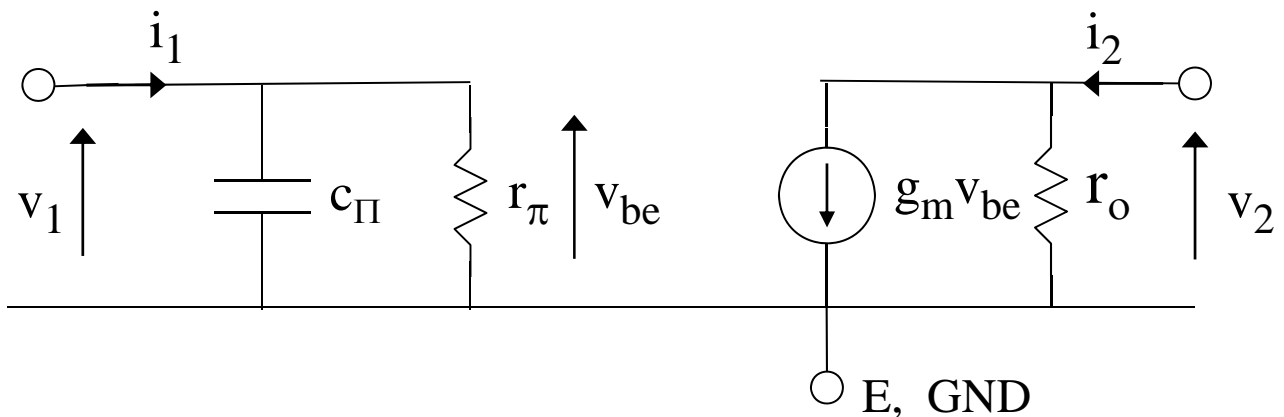
$$g_\pi = \frac{dI_B}{dV_{BE}} = \frac{d}{dV_{BE}} \left(\frac{I_C}{\beta} \right) = \frac{1}{\beta} \frac{dI_C}{dV_{BE}} = \frac{g_m}{\beta}$$

$$r_{out} = \frac{1}{g_{out}} = \frac{V_A}{I_C}$$

$$r_\pi = \frac{1}{g_\pi} = \frac{\beta}{g_m}$$

Q1 (d) 8 marks

y-parameters of BJT small-signal model



$$y_{11} = \left. \frac{i_1}{v_1} \right|_{v_2=0} = \frac{1}{r_\pi} + j\omega C_\pi \quad y_{12} = \left. \frac{i_1}{v_2} \right|_{v_1=0} = 0$$

$$y_{21} = \left. \frac{i_2}{v_1} \right|_{v_2=0} = g_m \quad y_{22} = \left. \frac{i_2}{v_2} \right|_{v_1=0} = \frac{1}{r_o} = g_o$$

Putting in the bias voltages and temperature and performing the calculations gives:

$$i_c = 5.1\text{mA} \quad i_b = 51.14\mu\text{A} \quad c_j = 0.6\text{pF}, c_d = 19.78\text{pF}, \\ c_{be} = 20.38\text{pF}, g_m = 0.198\text{S}, g_o = 0.51\text{mS}, r_{pi} = 505.6\Omega,$$

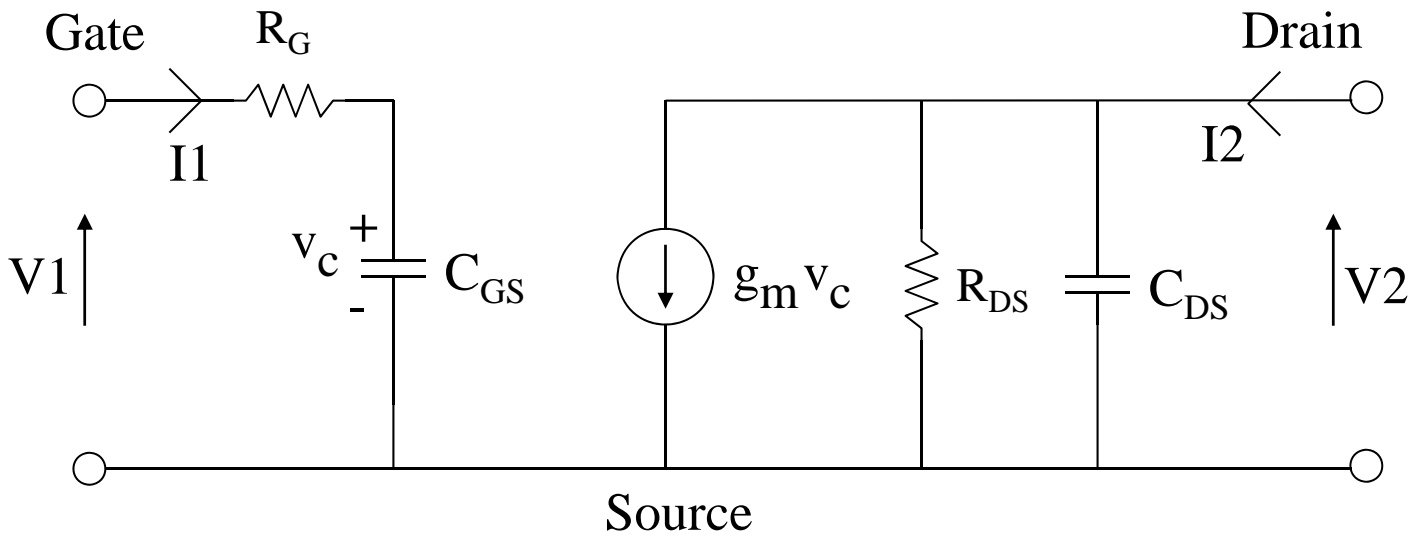
$$y_{11} = 0.002 + j0.192 = 0.192 \angle 89.4$$

$$y_{12} = 0$$

$$y_{21} = 0.198$$

$$y_{22} = 0.51\text{m}$$

MESFET small-signal circuit



$$y_{11} = \left. \frac{i_1}{v_1} \right|_{v_2=0} \quad y_{21} = \left. \frac{i_2}{v_1} \right|_{v_2=0} \quad y_{12} = \left. \frac{i_1}{v_2} \right|_{v_1=0} \quad y_{22} = \left. \frac{i_2}{v_2} \right|_{v_1=0}$$

Applying the above formulas to the equivalent circuit and simplifying the resulting expressions leads to the final y-parameter formulas:

$$y_{11} = \frac{j\omega C_{GS}}{1 + j\omega R_G C_{GS}} \quad y_{21} = \frac{g_m}{1 + j\omega R_G C_{GS}}$$

$$y_{12} = 0 \quad y_{22} = \frac{1}{R_{DS}} + j\omega C_{DS}$$

Question 2(b) 8 marks

The previous expressions for the y-parameters can be re-arranged to allow the small-signal element values to be determined from the y-parameters. Using the y-parameters at 2GHz:

$$y_{11} = 0.01 \angle 87.7^\circ$$

$$y_{12} = 0$$

$$y_{21} = 0.20 \angle -2.3^\circ$$

$$y_{22} = 0.004 \angle 73.6^\circ$$

$$R_G = \mathcal{Re}\left\{\frac{1}{y_{11}}\right\} = 4\Omega$$

$$C_{GS} = -\frac{1}{\omega \mathcal{Im}\left\{\frac{1}{y_{11}}\right\}} = 0.8 pF$$

$$g_m = \frac{1}{\mathcal{Re}\left\{\frac{1}{y_{21}}\right\}} = 0.2 S$$

$$R_{DS} = \frac{1}{\mathcal{Re}\{y_{22}\}} = 90\Omega$$

$$C_{DS} = \frac{\mathcal{Im}\{y_{22}\}}{\omega} = 0.3 pF$$

For a two-port network the ABCD parameters are defined as follows:

$$\begin{bmatrix} v_1 \\ i_1 \end{bmatrix} = \begin{bmatrix} A & B \\ C & D \end{bmatrix} \begin{bmatrix} v_2 \\ -i_2 \end{bmatrix}$$

The circuit could be directly analysed to give formulas for the ABCD parameters or they could be determined from the y-parameters:

$$\begin{bmatrix} i_1 \\ i_2 \end{bmatrix} = \begin{bmatrix} y_{11} & y_{12} \\ y_{21} & y_{22} \end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \end{bmatrix}$$

Using the two sets of relationships, conversion may be achieved as follows:

$$\begin{bmatrix} i_1 \\ i_2 \end{bmatrix} = \begin{bmatrix} y_{11} & y_{12} \\ y_{21} & y_{22} \end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \end{bmatrix}$$

$$A = -\frac{y_{22}}{y_{21}}$$

$$B = -\frac{1}{y_{21}}$$

$$C = \frac{y_{11}y_{22} - y_{12}y_{21}}{y_{21}}$$

$$D = -\frac{y_{11}}{y_{21}}$$

Performing the calculations for this transistor gives:

$$A = 0.02 \angle -104^\circ$$

$$B = 5 \angle -177^\circ$$

$$C = 1.98\text{e-}4 \angle 164^\circ$$

$$D = 0.05 \angle -90^\circ$$

EE4011 RF IC Design Summer 2011

Q3(a) 5 marks

$$\begin{aligned}
 y(t) = & \left[\alpha_1 A_1 + \frac{3}{4} \alpha_3 A_1^3 + \frac{3}{2} \alpha_3 A_1 A_2^2 \right] \cos(\varpi_1 t) \\
 & + \left[\alpha_1 A_2 + \frac{3}{4} \alpha_3 A_2^3 + \frac{3}{2} \alpha_3 A_1^2 A_2 \right] \cos(\varpi_2 t) \\
 & + \frac{1}{4} \alpha_3 A_1^3 \cos 3\varpi_1 t + \frac{1}{4} \alpha_3 A_2^3 \cos 3\varpi_2 t \\
 & + \frac{3}{4} \alpha_3 A_1^2 A_2 \cos(2\varpi_1 + \varpi_2)t + \frac{3}{4} \alpha_3 A_1 A_2^2 \cos(2\varpi_1 - \varpi_2)t \\
 & + \frac{3}{4} \alpha_3 A_1 A_2^2 \cos(2\varpi_2 + \varpi_1)t + \frac{3}{4} \alpha_3 A_1 A_2^2 \cos(2\varpi_2 - \varpi_1)t
 \end{aligned}$$

Taking the case of $A_1=A_2=A$, and considering the outputs at the fundamental frequencies to be the desired outputs, the amplitudes of the desired signals are:

$$\begin{aligned}
 A_{SIG} &= \left| \alpha_1 A + \frac{3}{4} \alpha_3 A^3 + \frac{3}{2} \alpha_3 A^3 \right| = \left| \alpha_1 A + \frac{9}{4} \alpha_3 A^3 \right| \\
 &\approx |\alpha_1| A \quad \text{if} \quad \alpha_1 \gg \frac{9}{4} \alpha_3 A^2
 \end{aligned}$$

In this case the unwanted 3rd-order inter-modulation (IM) signals are given by:

$$A_{IM3} = \frac{3}{4} |\alpha_3| A^3$$

As A increases the IM3 outputs will eventually reach the same level as the desired signal output. This condition is called the “third-order IM intercept point”, IP3. The input amplitude corresponding to this condition is $A=A_{IP3}$ and at this amplitude:

$$A_{SIG} = A_{IM3} \Rightarrow |\alpha_1| A_{IP3} = \frac{3}{4} |\alpha_3| A_{IP3}^3 \Rightarrow A_{IP3} = \sqrt{\frac{4 |\alpha_1|}{3 |\alpha_3|}}$$

The sensitivity of system is defined as the minimum input signal power which is required to give a specified minimum signal-to-noise ratio at the output.

For a given output SNR the input power can be found from the noise figure:

$$F = \frac{SNR_{in}}{SNR_{out}} = \frac{P_{sig} / P_{RS}}{SNR_{out}} \Rightarrow P_{sig} = P_{RS} \cdot F \cdot SNR_{out}$$

(per unit bandwidth)

Assuming the system bandwidth is B:

$$P_{sig} = P_{RS} \cdot F \cdot SNR_{out} \cdot B$$

Turning the quantities into logs and setting the output SNR to the minimum required value and the input signal power to the minimum value needed to give the required output SNR:

$$P_{min} = P_{RS}|_{dBm/Hz} + NF + SNR_{min}|_{dB} + 10\log_{10} B$$

$$\text{where } NF = 10\log_{10} F$$

If the input is conjugate matched to the source the noise power delivered to the input will be:

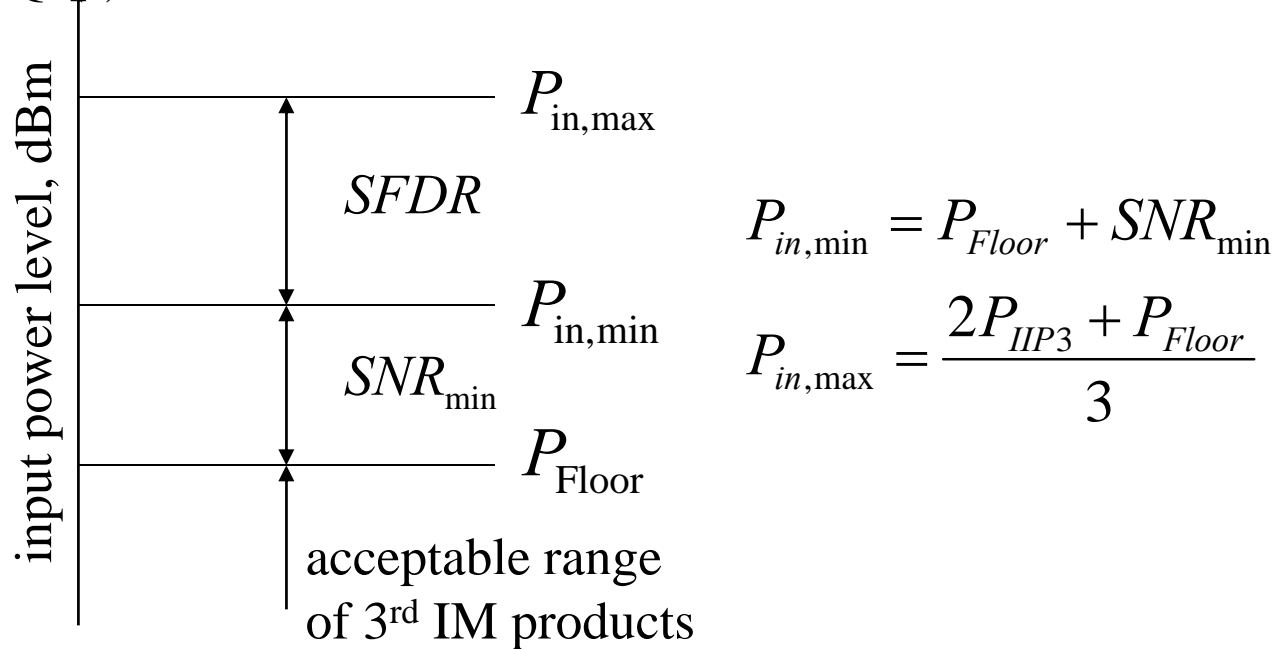
$$P_{RS} = \frac{\overline{v_n^2}}{4R_S} = \frac{4kTR_S}{4R_S} = kT = -174 \text{ dBm} / \text{Hz} \quad (\text{at } 300\text{K})$$

$$P_{in,min} = \underbrace{-174 \text{ dBm/Hz} + NF + 10\log_{10} B}_{\text{noise floor}} + SNR_{min}|_{dB}$$

$$P_{Floor} = -174 \text{ dBm/Hz} + NF + 10\log_{10} B$$

8 marks

Q3(c) 7 marks



The minimum acceptable power in dB is the noise floor plus the required minimum output SNR. As the input power level increase, two or more signals will give IM3 products. The maximum acceptable input power level is considered to be the input power level at which the IM3 products are as high as the noise floor. The range of power between the minimum level and the maximum level is known as the spurious free dynamic range (SFDR).

$$SFDR = P_{in,max} - P_{in,min} = \frac{2P_{IIP3} + P_{Floor}}{3} - (P_{Floor} + SNR_{min})$$

$$= \frac{2(P_{IIP3} - P_{Floor})}{3} - SNR_{min}$$

Using $NF = 10$ dB, $P_{IIP3} = -5$ dBm, $B = 2$ MHz, $T = 300$ K, $SNR_{min} = 15$ dB gives:

$$P_{Floor} = -173.83 + 10 + 10 \log_{10}(2 \times 10^6) = -100.82 \text{ dBm}$$

$$SFDR = \frac{2(P_{IIP3} - P_{Floor})}{3} - SNR_{min} = \frac{2(-5 + 100.82)}{3} - 15 = 48.88 \text{ dB}$$

EE4011 RF IC Design Summer 2011

Question 4(a)

$$s_{11} = 0.33 \angle -150^\circ \quad s_{12} = 0.01 \angle 60^\circ \quad s_{21} = 4.0 \angle -50^\circ \quad s_{22} = 0.50 \angle -45^\circ$$

$$F_{\min} = 3.0 \text{ dB} \quad \Gamma_{opt} = 0.75 \angle 180^\circ \quad R_N = 10.0 \Omega$$

(i) 2 marks

Rollet Stability Factor

$$K = \frac{1 - |s_{11}|^2 - |s_{22}|^2 + |\Delta|^2}{2|s_{12}s_{21}|} \quad \Delta = s_{11}s_{22} - s_{12}s_{21}$$

Test for Unconditional Stability:

$$K > 1 \quad \text{and} \quad |\Delta| < 1$$

This device is unconditionally stable because:

$$|\Delta| = 0.2 \quad k = 8.52$$

(ii) 2 marks

Maximum Unilateral Transducer Power Gain

$$G_{TU, \max} = \frac{1}{1 - |s_{11}|^2} |s_{21}|^2 \frac{1}{1 - |s_{22}|^2} = 23.94 = 13.8 \text{ dB}$$

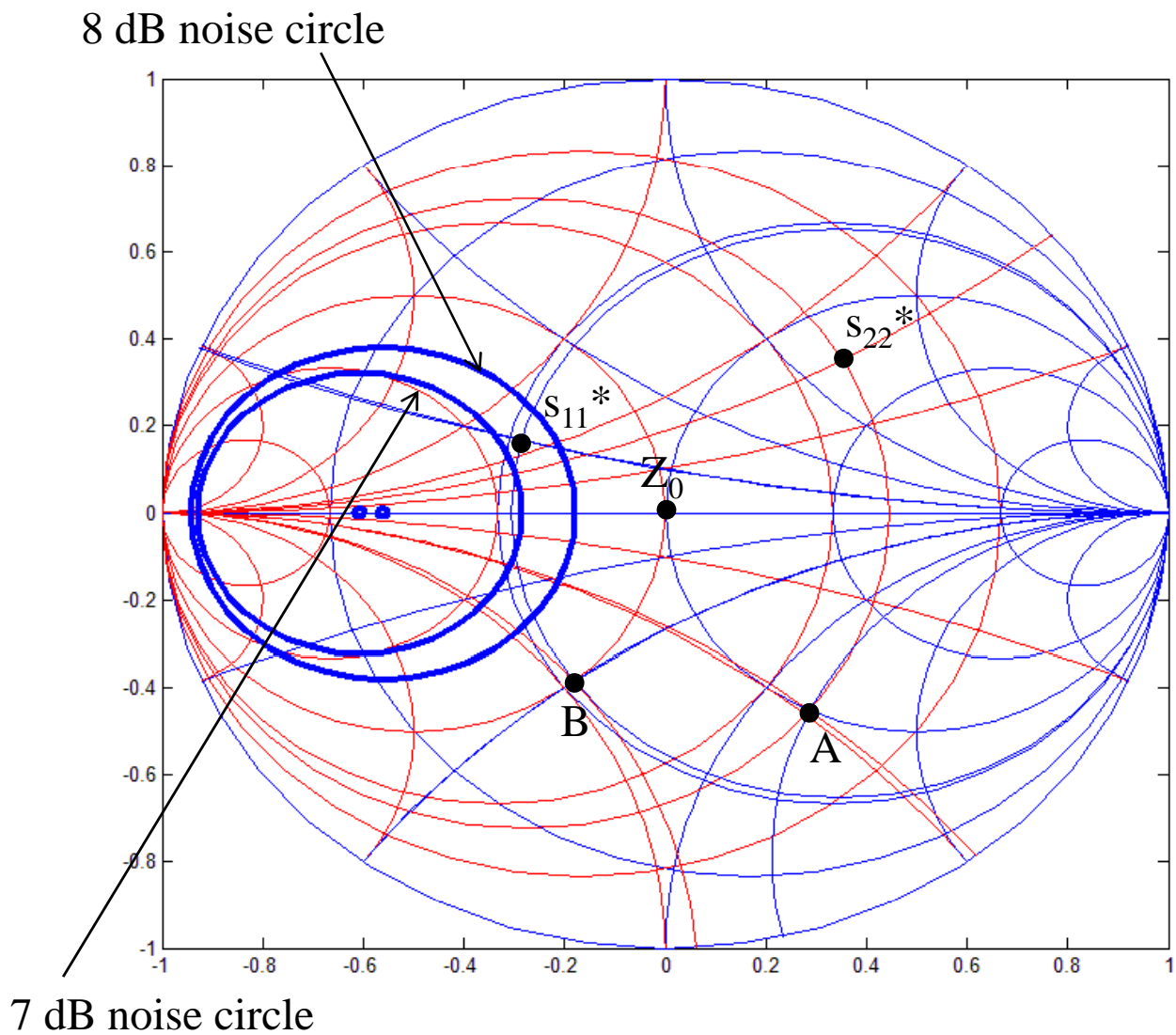
(iii) 2 marks

Unilateral Figure of Merit

$$M = \frac{|s_{11}| |s_{12}| |s_{21}| |s_{22}|}{(1 - |s_{11}|^2)(1 - |s_{22}|^2)} \quad \frac{1}{(1 + M)^2} < \frac{G_T}{G_{TU, \max}} < \frac{1}{(1 - M)^2}$$

Calculating M, gives the error in predicting gain of +/- 0.09dB

Smith Chart for (b) and (c) This is a Matlab generated plot.
The plot should be drawn on real Smith Chart paper



To transform Z_0 to s_{22}^* : Series capacitor, followed by shunt inductor

To transform Z_0 to s_{11}^* : Shunt capacitor, followed by series inductor

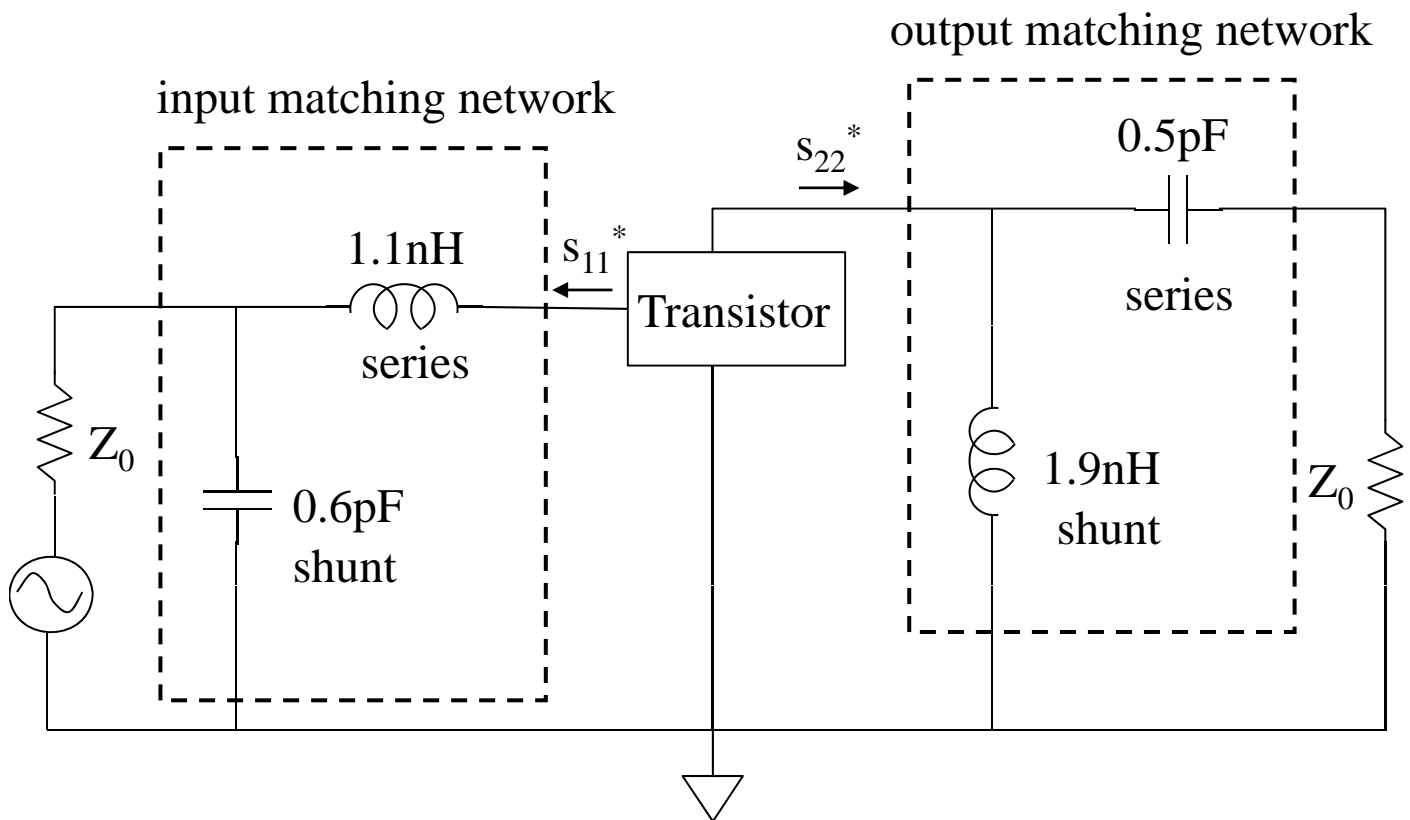
Q4(b) 10 marks

Series inductor: $L = \frac{Z_0 |\Delta x|}{2\pi f}$

Series capacitor: $C = \frac{1}{2\pi f |\Delta x| Z_0}$

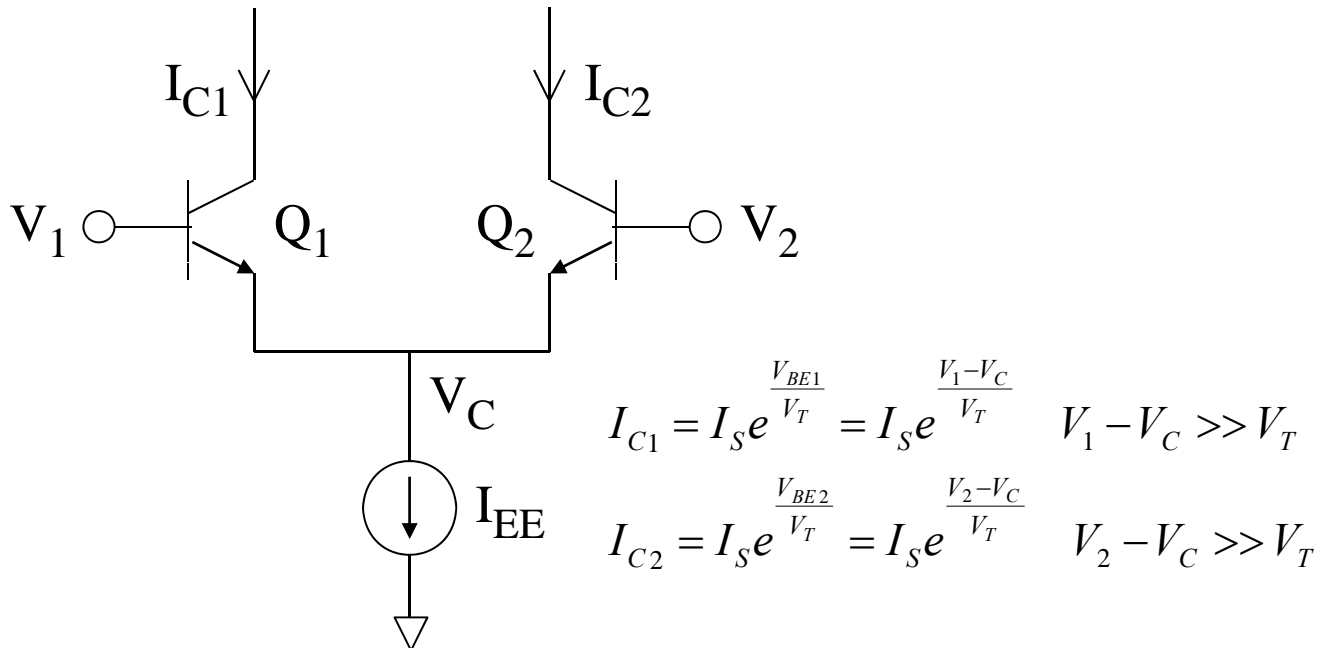
Shunt inductor: $L = \frac{Z_0}{2\pi f |\Delta b|}$

Shunt capacitor: $C = \frac{|\Delta b|}{2\pi f Z_0}$



Q4(c) 4 marks

A series of noise circles need to be drawn to identify those closest to the point s_{11}^* where the input reflection coefficient is placed. The nearest circles are 7dB and 8dB so the noise figure that is achieved is between 7 and 8 dB.



$$\frac{I_{C1}}{I_{C2}} = \frac{I_S e^{\frac{V_1 - V_C}{V_T}}}{I_S e^{\frac{V_2 - V_C}{V_T}}} = e^{\frac{V_1 - V_2}{V_T}} = e^{\frac{V_d}{V_T}} \Rightarrow \frac{I_{C2}}{I_{C1}} = e^{-\frac{V_d}{V_T}} \quad \text{where } V_d = V_1 - V_2$$

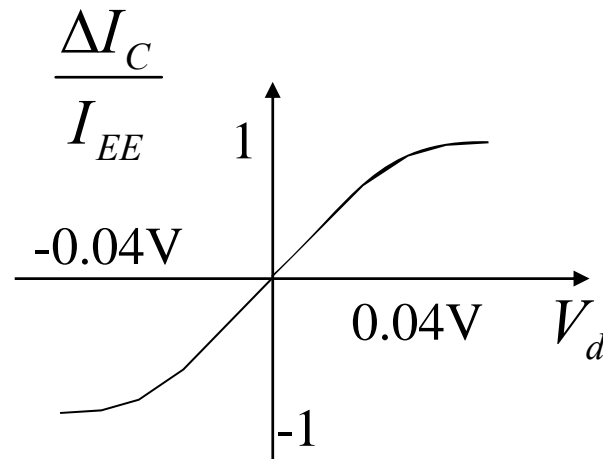
$$I_{C1} + I_{C2} = I_{EE} \Rightarrow I_{C1} + I_{C1} e^{-\frac{V_d}{V_T}} = I_{EE} \Rightarrow I_{C1} = \frac{I_{EE}}{1 + e^{-\frac{V_d}{V_T}}} \quad I_{C2} e^{\frac{V_d}{V_T}} + I_{C2} = I_{EE} \Rightarrow I_{C2} = \frac{I_{EE}}{1 + e^{\frac{V_d}{V_T}}}$$

$$\Delta I_C = I_{C1} - I_{C2} = \frac{I_{EE}}{1 + e^{-\frac{V_d}{V_T}}} - \frac{I_{EE}}{1 + e^{\frac{V_d}{V_T}}} = I_{EE} \left(\frac{1}{1 + e^{-\frac{V_d}{V_T}}} - \frac{1}{1 + e^{\frac{V_d}{V_T}}} \right)$$

$$= I_{EE} \left(\frac{e^{\frac{V_d}{V_T}}}{e^{\frac{V_d}{V_T}} + 1} - \frac{1}{1 + e^{\frac{V_d}{V_T}}} \right) = I_{EE} \frac{e^{\frac{V_d}{V_T}} - 1}{e^{\frac{V_d}{V_T}} + 1}$$

$$= I_{EE} \frac{e^{\frac{V_d}{2V_T}} - e^{-\frac{V_d}{2V_T}}}{e^{\frac{V_d}{2V_T}} + e^{-\frac{V_d}{2V_T}}} = I_{EE} \tanh\left(\frac{V_d}{2V_T}\right)$$

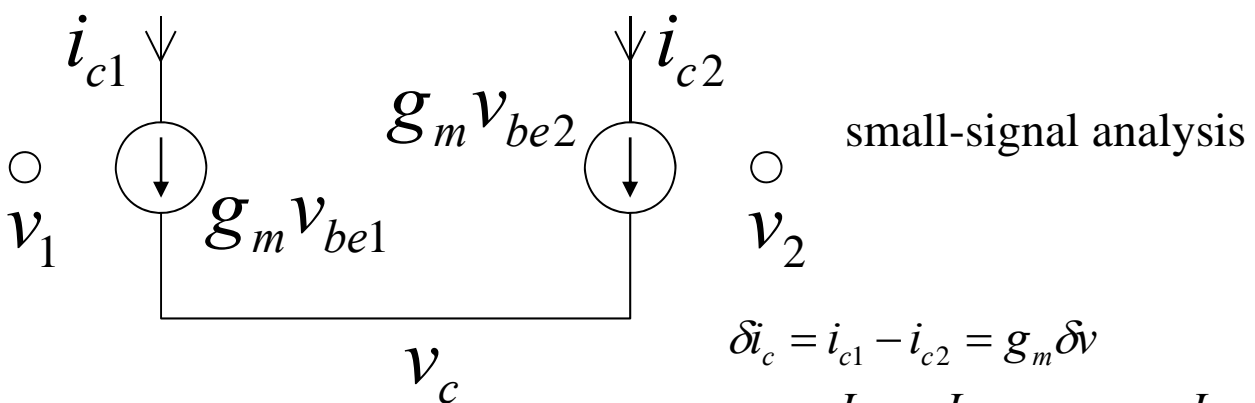
Q5(a) Continued



Q5(b) 4 marks

$$\Delta I_C = I_{EE} \tanh\left(\frac{V_d}{2V_T}\right)$$

For small V_d this becomes: $\Delta I_C = \frac{I_{EE}}{2V_T} V_d$

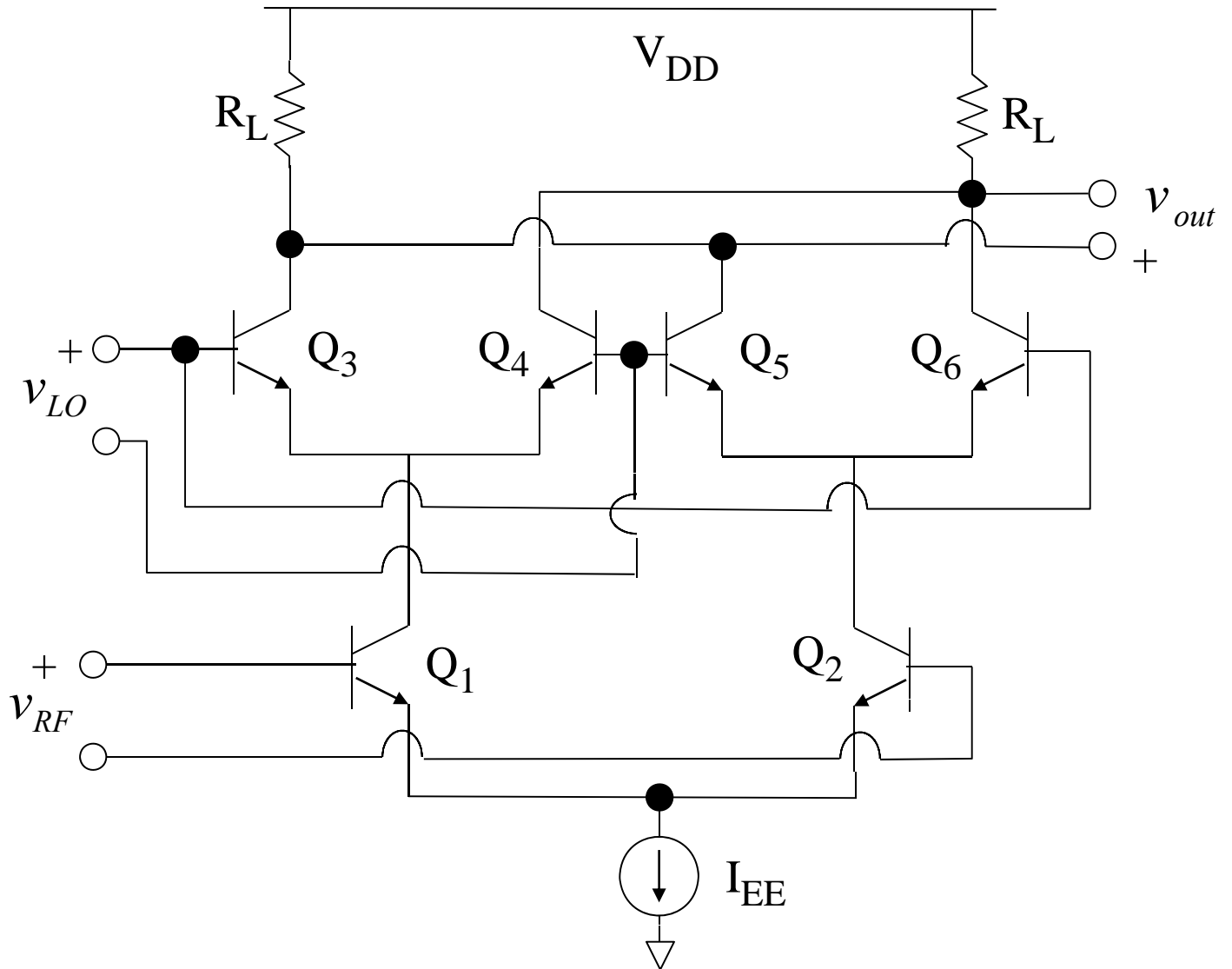


$$\delta i_c = i_{c1} - i_{c2} = g_m \delta v$$

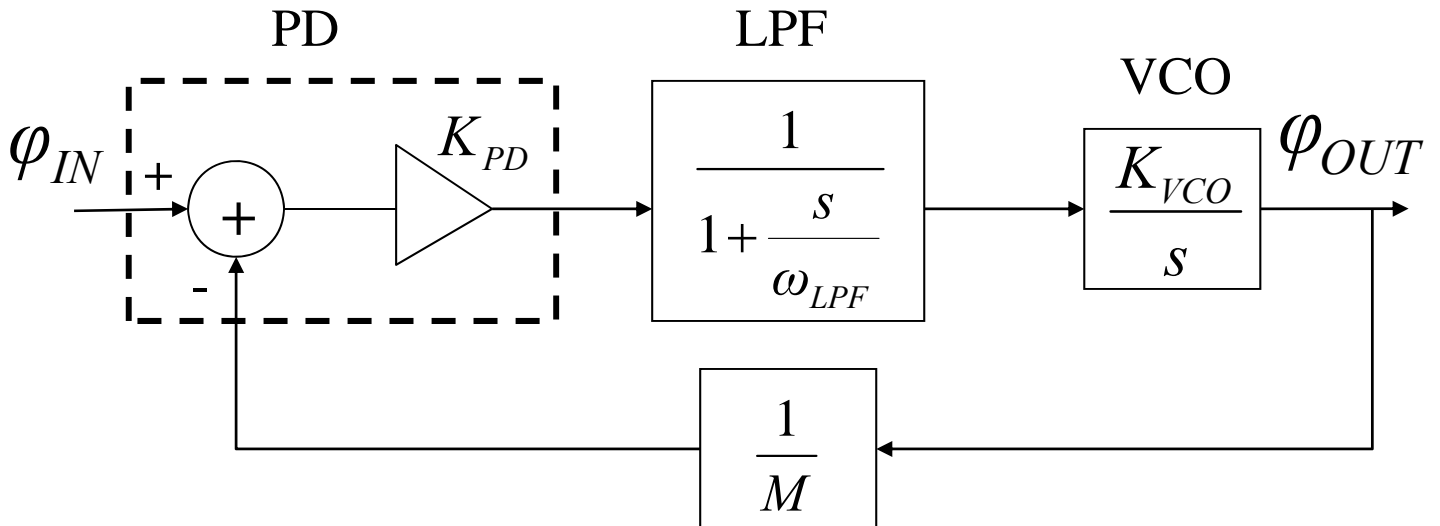
$$g_m = \frac{I_C}{V_T} = \frac{I_{EE}}{2V_T} \Rightarrow \delta i_c = \frac{I_{EE}}{2V_T} \delta v$$

(assuming that I_{EE} is shared equally by Q_1 and Q_2)

The small-signal analysis gives the same result as the large signal



Type 1 PLL with integer feedback divider



Q6(b) 8 marks

$$H(s)|_{OPEN} = \frac{\varphi_{OUT}(s)}{\varphi_{IN}(s)} \Big|_{OPEN} = K_{PD} \cdot \frac{1}{1 + \frac{s}{\omega_{LPF}}} \cdot \frac{K_{VCO}}{s} = \frac{K_{PD} K_{VCO}}{s + \frac{s^2}{\omega_{LPF}}}$$

$$\varphi_{OUT}(s) = H(s) \left(\varphi_{IN}(s) - \frac{\varphi_{OUT}(s)}{M} \right) \Rightarrow \varphi_{OUT}(s) \left(1 + \frac{H(s)}{M} \right) = H(s) \varphi_{IN}(s)$$

$$\Rightarrow \frac{\varphi_{OUT}(s)}{\varphi_{IN}(s)} = \frac{H(s)}{1 + \frac{H(s)}{M}} = \frac{1}{\frac{1}{H(s)} + \frac{1}{M}} = \frac{1}{\frac{s + \frac{s^2}{\omega_{LPF}}}{K_{PD} K_{VCO}} + \frac{1}{M}} = \frac{K_{PD} K_{VCO}}{s + \frac{s^2}{\omega_{LPF}} + \frac{K_{PD} K_{VCO}}{M}}$$

$$= \frac{K_{PD} K_{VCO} \omega_{LPF}}{s^2 + \omega_{LPF} s + \frac{K_{PD} K_{VCO} \omega_{LPF}}{M}}$$

Q6(c)

(i) 2 marks

For an integer feedback the reference frequency must be equal to the desired step size i.e. 200kHz in this case

(ii) 2 marks

The range of divider values needed are:

$$M = \frac{925}{0.2} = 4625 \quad \text{to} \quad M = \frac{960}{0.2} = 4800$$

(iii) 2 marks

A rule of thumb to ensure good stability is to set the low-pass filter cut-off frequency to 10% of the reference frequency i.e. 20kHz in this case.

(iv) 2 marks

With an integer feedback divider, the reference frequency must be the same as the minimum desired frequency step. If this step is small then because the low-pass filter cut-off frequency is usually 10-20% of the reference frequency, the LPF cut-off frequency will be low and the dynamic response of the PLL will be slow. A fractional-N divider allows a small frequency step for a larger reference frequency which facilitates a faster dynamic response.

Question 7 is an essay-type question based on a continuous assessment carried out during the year.