Totally Integrated
<b>Automation Porta</b>

## PLC\_1 [CPU 1211C DC/DC/DC]

	PLC_1 [CPU 1211C DC/DC/DC]							
PLC_1 General\Project inforr	nation							
Name	PLC_1	Author	i72014	Comment				
Slot	1	Rack	0					
General\Catalog infor Short designation	CPU 1211C DC/DC/DC	Description	Work memory 50 KB; 24VDC power supply with DI6 x 24VDC SINK/ SOURCE, DQ4 x 24VDC and AI2 on board; 3 high-speed counters (expandable with digital signal board) and 4 pulse outputs on board; signal board expands on-board I/O; up to 3 communication modules for serial communication; 0.04 ms/1000 instructions; PROFINET interface for programming, HMI and PLC to PLC communication	Article number	6ES7 211-1AE40-0XB0			
Firmware version General\Identification	V4.1							
Plant designation	i & Maintenance	Location identifier		Installation date	2016-04-07 09:52:18.455			
Additional informa- tion				1				
PROFINET interface [X	_		177044					
Name PROFINET interface [X	PROFINET-Schnittstelle_1 (1]\General\Project information	Author	i72014	Comment				
Name	DI 6/DQ 4_1	Comment		Name	AI 2_1			
Comment					<del>-</del>			
	(1]\Ethernet addresses\Interface netw	orked with						
Subnet:	PN/IE_1 (1]\Ethernet addresses\IP protocol							
IP configuration	Set IP address in the project	IP address:	192.168.0.1	Subnet mask:	255.255.255.0			
Use router	False							
_	(1]\Ethernet addresses\PROFINET							
PROFINET device	False	Generate PROFINET	True	PROFINET device	plc_1			
name is set directly at the device		device name auto- matically		name:				
Converted name:	plcxb1d0ed	Device number:	0					
	(1]\Time synchronization							
nization via NTP serv-	Enable time synchronization via NTP server		IP addresses	Server 1	0.0.0.0			
er Server 2	0.0.0.0	Server 3	0.0.0.0	Server 4	0.0.0.0			
Update interval	10sec (1]\Digital inputs\Channel0							
Channel address	10.0	Input filters	6.4 millisec	Enable pulse catch	0			
PROFINET interface [X Enable rising edge	(1]\Digital inputs\Channel0\	RidPrefixRisingEdgeE-	40152	Event name:	0			
detection		vent		Event name.	O			
Hardware interrupt:		Rising edge0	Rising edge0					
Enable falling edge detection	(1]\Digital inputs\Channel0\ 0	RidPrefixFallingEdg- eEvent	49280	Event name:	0			
Hardware interrupt:	0	Falling edge0	Falling edge0					
PROFINET interface [X	(1]\Digital inputs\Channel1							
Channel address PROFINET interface [X	10.1 (1]\Digital inputs\Channel1\	Input filters	6.4 millisec	Enable pulse catch	0			
Enable rising edge detection	0	RidPrefixRisingEdgeE- vent	49153	Event name:	0			
Hardware interrupt:		Rising edge1	Rising edge1					
PROFINET interface [X Enable falling edge	(1]\Digital inputs\Channel1\  0	RidPrefixFallingEdg-	49281	Event name:	0			
detection	O	eEvent	49281	Event name:	O			
Hardware interrupt:		Falling edge1	Falling edge1					
	(1]\Digital inputs\Channel2							
Channel address	10.2 (1]\Digital inputs\Channel2\	Input filters	6.4 millisec	Enable pulse catch	0			
Enable rising edge	0	RidPrefixRisingEdgeE-	49154	Event name:	0			
detection Hardware interrupt:	0	vent Rising edge2	Rising edge2					
	(1]\Digital inputs\Channel2\							
Enable falling edge detection	0	RidPrefixFallingEdg- eEvent	49282	Event name:	0			
Hardware interrupt:		Falling edge2	Falling edge2					
PROFINET interface [X Channel address	(1]\Digital inputs\Channel3	Innut filtous	6.4 millions	Enable nulse sateh	0			
PROFINET interface [X	10.3 (1]\Digital inputs\Channel3\  0	Input filters	6.4 millisec	Enable pulse catch	0			
Enable rising edge detection		RidPrefixRisingEdgeE- vent		Event name:	0			
Hardware interrupt: PROFINET interface [X	0 (1]\Digital inputs\Channel3\	Rising edge3	Rising edge3					
Enable falling edge	0	RidPrefixFallingEdg-	49283	Event name:	0			
detection Hardware interrupt:	0	eEvent Falling edge3	Falling edge3					
•	\footnote{\text{\text{Channel4}}}	ur anniy euges	i aimig edges					
Channel address	10.4	Input filters	6.4 millisec	Enable pulse catch	0			
	1							
	I							

ROFINET interface [X nable rising edge	(1]\Digital inputs\Channel4\	RidPrefixRisingEdgeE-	40156	Event name:	0
etection		vent	49130	Event name.	O
ardware interrupt:		Rising edge4	Rising edge4		
	(1]\Digital inputs\Channel4\ 0	RidPrefixFallingEdg-	49284	Event name:	0
etection		eEvent			
ardware interrupt:		Falling edge4	Falling edge4		
OFINET INTERTACE LX nannel address	(1]\Digital inputs\Channel5 I0.5	Input filters	6.4 millisec	Enable pulse catch	0
ROFINET interface [X	(1]\Digital inputs\Channel5\				
nable rising edge etection	0	RidPrefixRisingEdgeE- vent	49157	Event name:	0
ardware interrupt:		Rising edge5	Rising edge5		
	K1]\Digital inputs\Channel5\ 0	RidPrefixFallingEdg- eEvent	49285	Event name:	0
ardware interrupt:	0	Falling edge5	Falling edge5		
OFINET interface [X	(1]\Analog inputs\Noise reduction		3 - 3		
tegration time	50 Hz (20 ms)				
iorine i interface (X nannel address	(1]\Analog inputs\Channel0	Measurement type	Voltage	Voltage range	010 V
noothing	Weak (4 cycles)			Enable overflow diag-	
	(1]\Analog inputs\Channel1				
nannel address	IW66	Measurement type	Voltage	Voltage range	010 V
noothing	Weak (4 cycles)			Enable overflow diagnostics	
ROFINET interface [X					
	Use substitute value  (1]\Digital outputs\Channel0				
nannel address	Q0.0	Substitute a value of	0		
		1 on a change from RUN to STOP.			
	(1]\Digital outputs\Channel1	Substitute a value of	0		
nannel address	Q0.1	Substitute a value of 1 on a change from RUN to STOP.	O		
ROFINET interface [X	(1]\Digital outputs\Channel2	NOW to STOT.			
nannel address	Q0.2	Substitute a value of 1 on a change from	0		
DOFINET '		RUN to STOP.			
ROFINET Interface [X hannel address	(1]\Digital outputs\Channel3 Q0.3	Substitute a value of	0		
	40.5	1 on a change from RUN to STOP.			
ROFINET interface [X					
O controller O device	True	IO system		Device number	0
device	Falco				
ROFINET interface [X	False   K1]\I/O addresses\Input addresses				
tart address	(1]\\/O addresses\Input addresses 0.0	End address	0.7	Organization block	65535
art address ocess image	(1]\I/O addresses\Input addresses 0.0 65535	End address	0.7	Organization block	65535
art address rocess image ROFINET interface [X	(1]\\/O addresses\Input addresses 0.0	End address	0.7 67		65535
art address ocess image ROFINET interface [X art address ocess image	(1]\\/O addresses\\Input addresses 0.0 65535 (1]\\/O addresses\\Input addresses 64 65535			Organization block Organization block	
art address ocess image ROFINET interface [X art address ocess image ROFINET interface [X	(1]\\/O addresses\Input addresses 0.0 65535 (1]\\/O addresses\Input addresses	End address		Organization block	
cart address rocess image ROFINET interface [X cart address rocess image ROFINET interface [X cart address rocess image	(1]\I/O addresses\Input addresses 0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0	End address End address	67		65535
tart address rocess image ROFINET interface [X tart address rocess image ROFINET interface [X tart address rocess image ROFINET interface [X tart address	(1]\I/O addresses\Input addresses 0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option	End address End address	0.7	Organization block Organization block	0
cart address rocess image ROFINET interface [X cart address rocess image	(1]\I/O addresses\Input addresses 0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0	End address  End address  Permit overwriting of device names of all	0.7	Organization block	65535
cart address rocess image ROFINET interface [X cart address rocess image ROFINET interface [X cart address rocess image ROFINET interface [X capport device re- lacement without schangeable medi-	(1]\I/O addresses\Input addresses 0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option	End address  End address  s  Permit overwriting of	0.7	Organization block Organization block Use IEC V2.2 LLDP	0
art address ocess image ROFINET interface [X art address ocess image ROFINET interface [X art address ocess image ROFINET interface [X upport device re- acement without cchangeable medi-	(1]\I/O addresses\Input addresses 0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option	End address  End address  Permit overwriting of device names of all	0.7	Organization block Organization block Use IEC V2.2 LLDP	0
art address ocess image OFINET interface [X art address ocess image OFINET interface [X art address ocess image OFINET interface [X pport device re- acement without changeable medi- n eep-Alive connec- on monitoring	(1]\I/O addresses\Input addresses  0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True	End address  End address  Permit overwriting of device names of all assigned IO devices	0.7	Organization block Organization block Use IEC V2.2 LLDP	65535  0
art address rocess image ROFINET interface [X rart address rocess image ROFINET interface [X rart address rocess image ROFINET interface [X rart address rocess image ROFINET interface [X rapport device re- racement without rochangeable medi- racep-Alive connector monitoring ROFINET interface [X	(1]\I/O addresses\Input addresses 0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time setting	End address  End address  Permit overwriting of device names of all assigned IO devices	0.7	Organization block Organization block Use IEC V2.2 LLDP	0
art address ocess image ROFINET interface [X art address ocess image ROFINET interface [X art address ocess image ROFINET interface [X upport device re- acement without cchangeable medi- m eep-Alive connec- on monitoring ROFINET interface [X end clock:	(1]\I/O addresses\Input addresses  0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True	End address  End address  Permit overwriting of device names of all assigned IO devices	0.7	Organization block Organization block Use IEC V2.2 LLDP	65535  0
art address rocess image ROFINET interface [X apport device re- acement without rochangeable medi- m reep-Alive connec- ron monitoring ROFINET interface [X and clock: ROFINET interface [X alculated bandwidth	(1]\I/O addresses\Input addresses  0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\IO communication  gs\Real time options Calculated bandwidth	0.7 False	Organization block Organization block Use IEC V2.2 LLDP	65535  0
art address ocess image ROFINET interface [X art address ocess image ROFINET interface [X art address ocess image ROFINET interface [X apport device re- acement without schangeable medi- m rep-Alive connec- on monitoring ROFINET interface [X and clock: R	(1]\I/O addresses\Input addresses  0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 0.000ms	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\IO communication  gs\Real time options  Calculated bandwidth for cyclic IO data:	0.7 False	Organization block Organization block Use IEC V2.2 LLDP	65535  0
cart address rocess image ROFINET interface [X rart address rocess image ROFINET interface [X rart address rocess image ROFINET interface [X rapport device re- racement without rochangeable medi- rep-Alive connecton monitoring ROFINET interface [X rand clock: ROFINET interface [X	(1]\I/O addresses\Input addresses  0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\IO communication  gs\Real time options  Calculated bandwidth for cyclic IO data: neral	0.7 False	Organization block Organization block Use IEC V2.2 LLDP	65535  0
art address ocess image ocess	(1]\I/O addresses\Input addresses 0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 0.000ms (1]\Advanced options\Port [X1 P1]\Ge Port_1 (1]\Advanced options\Port [X1 P1]\Po	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\Real time options Calculated bandwidth for cyclic IO data: neral Author	0.7 False  0.000%  i72014 I port:	Organization block  Organization block  Use IEC V2.2 LLDP mode	65535  0
art address ocess image OFINET interface [X art address ocess image OFINET interface [X art address ocess image OFINET interface [X pport device re- acement without changeable medi- n ep-Alive connec- on monitoring OFINET interface [X nd clock: OFINET interface [X lculated bandwidth r cyclic IO data: OFINET interface [X ame OFINET interface [X ame	(1]\I/O addresses\Input addresses 0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 0.000ms (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 1.000ms	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\Real time options  Calculated bandwidth for cyclic IO data: neral  Author	0.7 False  0.000%	Organization block  Organization block  Use IEC V2.2 LLDP mode	65535  0
art address rocess image ROFINET interface [X art address rocess image ROFINET interface [X art address rocess image ROFINET interface [X apport device re- acement without richangeable medi- an apport device re- acement without richangeable medi- acep-Alive connec- an monitoring ROFINET interface [X alculated bandwidth ar cyclic IO data: ROFINET interface [X ame ROFINET interface [X ame ROFINET interface [X	(1]\I/O addresses\Input addresses  0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 0.000ms (1]\Advanced options\Port [X1 P1]\Ge Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\Real time options Calculated bandwidth for cyclic IO data: neral Author	0.7  False  0.000%  i72014  port: Copper	Organization block  Organization block  Use IEC V2.2 LLDP mode	65535 0 False
tart address rocess image ROFINET interface [X tart address rocess image ROFINET interface [X tart address rocess image ROFINET interface [X upport device re- lacement without xchangeable medi- m eep-Alive connec- on monitoring ROFINET interface [X end clock: ROFINET interface [X alculated bandwidth or cyclic IO data: ROFINET interface [X lame ROFINET interface [X lame ROFINET interface [X	(1]\I/O addresses\Input addresses  0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 0.000ms (1]\Advanced options\Port [X1 P1]\Ge Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\IO communication  gs\Real time options Calculated bandwidth for cyclic IO data: neral Author t interconnection\Local Medium:	0.7  False  0.000%  i72014  port: Copper	Organization block  Organization block  Use IEC V2.2 LLDP mode	65535 0 False
tart address rocess image ROFINET interface [X upport device re- lacement without xchangeable medi- m reep-Alive connec- on monitoring ROFINET interface [X end clock: ROFINET interface [X alculated bandwidth or cyclic IO data: ROFINET interface [X lame	(1]\I/O addresses\Input addresses  0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 0.000ms (1]\Advanced options\Port [X1 P1]\Ge Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\IO communication  gs\Real time options Calculated bandwidth for cyclic IO data: neral Author t interconnection\Local Medium:	0.7  False  0.000%  i72014  port: Copper	Organization block  Organization block  Use IEC V2.2 LLDP mode	65535 0 False
tart address rocess image ROFINET interface [X tart address rocess image	(1]\I/O addresses\Input addresses  0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 0.000ms (1]\Advanced options\Port [X1 P1]\Ge Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\IO communication  gs\Real time options Calculated bandwidth for cyclic IO data: neral Author t interconnection\Local Medium:	0.7  False  0.000%  i72014  port: Copper	Organization block  Organization block  Use IEC V2.2 LLDP mode	65535 0 False
tart address rocess image ROFINET interface [X tart address rocess image	(1]\I/O addresses\Input addresses  0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 0.000ms (1]\Advanced options\Port [X1 P1]\Ge Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1 (1]\Advanced options\Port [X1 P1]\Port_1	End address  End address  Permit overwriting of device names of all assigned IO devices  egs\IO communication  calculated bandwidth for cyclic IO data: neral Author t interconnection\Local Medium:	0.7  False  0.000%  i72014  port: Copper	Organization block  Organization block  Use IEC V2.2 LLDP mode	65535 0 False
art address rocess image ROFINET interface [X art address rocess image ROFINET interface [X art address rocess image ROFINET interface [X apport device re- acement without richangeable medi- m reep-Alive connec- ron monitoring ROFINET interface [X alculated bandwidth rr cyclic IO data: ROFINET interface [X ame ROFINET interface [X ame ROFINET interface [X ame ROFINET interface [X ame	(1]\I/O addresses\Input addresses 0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 0.000ms (1]\Advanced options\Port [X1 P1]\Ge Port_1 (1]\Advanced options\Port [X1 P1]\Po PLC_1\PROFINET-Schnittstelle_1 [X1]\Port_1 [X1 P1]	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\IO communication  gs\Real time options  Calculated bandwidth for cyclic IO data: neral  Author rt interconnection\Local  Medium:	0.7  False  0.000%  i72014  I port: Copper	Organization block  Organization block  Use IEC V2.2 LLDP mode	65535 0 False
art address ocess image ROFINET interface [X art address ocess image ROFINET interface [X art address ocess image ROFINET interface [X apport device re- acement without echangeable medi- m eep-Alive connec- on monitoring ROFINET interface [X and clock: ROFINET interface [X alculated bandwidth r cyclic IO data: ROFINET interface [X ame ROFINET interface [X ame ROFINET interface [X ame	(1]\I/O addresses\Input addresses 0.0 65535 (1]\I/O addresses\Input addresses 64 65535 (1]\I/O addresses\Output addresses 0.0 0 (1]\Advanced options\Interface option True  30s (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Real time settin 1.000ms (1]\Advanced options\Port [X1 P1]\Ge Port_1 (1]\Advanced options\Port [X1 P1]\Poi PLC_1\PROFINET-Schnittstelle_1 [X1]\Port_1 [X1 P1]	End address  End address  Permit overwriting of device names of all assigned IO devices  gs\IO communication  gs\Real time options  Calculated bandwidth for cyclic IO data: neral  Author rt interconnection\Local  Medium:	0.7  False  0.000%  i72014  I port: Copper	Organization block  Organization block  Use IEC V2.2 LLDP mode	65535 0 False

Totally Integrated Automation Portal					
PROFINET interface [X Activate this port for use	1]\Advanced options\Port [X1 P1]\Por True	t options\Activate			
ROFINET interface [X ransmission rate /	1]\Advanced options\Port [X1 P1]\Por Automatic	t options\Connection Monitor	False	Enable autonegotia-	True
	1]\Advanced options\Port [X1 P1]\Por False		False	tion End of the sync do-	False
ccessible devices		covery		main	
PROFINET interface [X Enable Web server us- ng this interface	-	The Web server must also be activated in the properties of the PLC.			
-	HSC)\HSC1\General\Enable	En alda Alda Islania	lo.	En alda shia bink	
Enable this high speed counter	0	Enable this high speed counter	0	Enable this high speed counter	0
nable this high	0	Enable this high	0	Enable this high	0
speed counter		speed counter		speed counter	
	HSC)\HSC1\General\Project informatio			Name	HSC 2
Name Comment	HSC_1	Comment Name	HSC_3	Comment	HSC_2
	HSC_4	Comment	<del>-</del> -	Name	HSC_5
Comment		1	HSC_6	Comment	
•	HSC)\HSC1\I/O addresses\Input addres		1000 7		10010
Start address End address	1000.0 1007.7	End address	1003.7	Start address Start address	1004.0
end address End address	1011.7	Organization block Organization block	0	Process image	0
Start address	1012.0	End address	1015.7	Organization block	0
· · · · · · · · · · · · · · · ·	0	Start address	1016.0	End address	1019.7
Organization block	0	Process image	0	Start address	1020.0
End address	1023.7	Organization block	0	Process image	0
- · J · · · · · · · · · · · · · · · ·	0  PWM)\PTO1/PWM1\General\Enable	Process image	0	Process image	0
Enable this pulse generator	0	Enable this pulse generator	0		
	/PWM)\PTO1/PWM1\General\Project in	11		II.	
Name Comment	Pulse_1	Comment		Name	Pulse_2
	 /PWM)\PTO1/PWM1\I/O addresses\Out	tput addresses			
Start address	1000.0	End address	1001.7	Start address	1002.0
End address	1003.7		0	Organization block	0
Process image Startup	0	Process image	0		
Startup after POWER	Warm restart - mode before POWER OFF	Comparison preset to actual configuration	Startup CPU even if mismatch	Configuration time	60000ms
ruptible Cycle					
Cycle monitoring	150ms			Enable minimum cy-	
time Minimum cycle time	30ms			cle time for cyclic OB	S
Communication load	Johns				
	20%				
communication					
	nory\System memory bits	Address of systems	1	First evels	
Enable the use of sys- em memory byte Diagnostic status	U	Address of system memory byte (MBx) Always 1 (high)		First cycle Always 0 (low)	
changed	con/Clock mamory bits				
•	nory\Clock memory bits	Address of clock	0	10 Hz clock	
clock memory byte		memory byte (MBx)			
Hz clock		2.5 Hz clock		2 Hz clock	
1.25 Hz clock		1 Hz clock		0.625 Hz clock	
).5 Hz clock Web server\General					
	False	Permit access only	True		
on all modules of this device	·	with HTTPS			
Web server\Automatic Enable automatic up-		Update interval	Os		
late	TI GC	Space milerval			
Web server\User interf					
Assign project langua	ge		User interface languages		
English (United States) English (United States)			German English		
English (United States)			French		
English (United States)			Spanish		
English (United States)			Italian		
English (United States)			Chinese (simplified)		
Neb server\User mana	ngement		Heavy wiseless		
<b>User name</b> Everybody			User rights		
Neb server\User defin	ed web pages				
Application name	HTML source path	Default HTML page index.htm	Files with dynamic content .htm;.html	Web DB number 333	Fragment DB number 334

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Web server\Overview o	of interfaces						
Device			Interface	1		Enabled web server ac	cess
PLC_1 User interface languag	IAS		PROFINET-Schnittstelle	_1	F	alse	
Assign project language				User interface lar	nguages		
English (United States)	,-			German			
English (United States)				English			
English (United States) English (United States)				French Spanish			
English (United States)				Italian			
English (United States)				Chinese (simplifie	d)		
Time of day\Local time Time zone		Daylin Dayn Dyusaala					
	Rome, Stockh	Berlin, Bern, Brussels, olm, Vienna					
Time of day\Daylight sa	aving time						
Activate daylight sav- ing time	1		Difference between standard and daylight saving time	60mins t			
		art of daylight saving tir					
Starting week of the month:	Last			Sunday		of	March
	01:00 a.m.						
Time of day\Daylight sa	aving time\St	art of standard time					
	Last			Sunday		of	October
at Protection & Security	02:00 a.m.						
Level of protection	No protection						
Protection & Security\C	Connection m						
PUT/GET communica- tion from remote	True						
partner Configuration controls	Configuration	n control for central conf	figuration				
Allow to reconfigure		. control for central conf	gurudon				
the device via the							
user program Connection resources\							
Connection resources,		Station resources - Rese	erved - Max- Station re	sources - Reserved - Con-	Station resou	ırces - Dynamic - Con-	Module resources - PLC_1 [CPU
		imum	figured		figured		1211C DC/DC/DC] - Configured
Maximum number of re		Maximum	62	4	6 Configured		68 Configured
PG communication:		4	Configured -	u	Configured -		Configured -
HMI communication:		12	2		0		2
S7 communication:		8	0		0		0
Open user communication:		8 30	0		0		-
Other communication:		-	-		0		0
Total resources used:			2		0		2
Available resources:	\0		60		6		66
	True	addresses\Overview of		True		Address gaps	False
	True		Outputs	nue		Address gaps	uise

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<b>Automation Portal</b>

Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO sys- tem	Rack	Slot
l	0	0	DI 6/DQ 4_1	None	PLC_1 [CPU 1211C DC/DC/DC]	-	1 Bytes	-	0	1 1
0	0	0	DI 6/DQ 4_1	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	1 Bytes	-	0	1 1
	64	67	AI 2_1	None	PLC_1 [CPU 1211C DC/DC/DC]	-	4 Bytes	-	0	1 2
I	1000	1003	HSC_1	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	4 Bytes	-	0	1 16
I	1004	1007	HSC_2	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	4 Bytes	-	0	1 17
I	1008	1011	HSC_3	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	4 Bytes	-	0	1 18
I	1012	1015	HSC_4	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	4 Bytes	-	0	1 19
I	1016	1019	HSC_5	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	4 Bytes	-	0	1 20
I	1020	1023	HSC_6	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	4 Bytes	-	0	1 21
0	1000	1001	Pulse_1	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	2 Bytes	-	0	1 32
0	1002	1003	Pulse_2	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	2 Bytes	-	0	1 33
0	1004	1005	Pulse_3	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	2 Bytes	-	0	1 34
0	1006	1007	Pulse_4	Automatic up- date	PLC_1 [CPU 1211C DC/DC/DC]	-	2 Bytes	-	0	1 35

Totally Integ Automation	grated Portal									
PLC_1 [C	PLC_1 [CPU 1211C DC/DC/DC] / Program blocks									
Main [OB1	Main [OB1]									
Main Propertie	es									
Name	Main		Numbe	er 1		Туре	ОВ		Language	LAD
Numbering Information	Automat									
Title	"Main Pro cle)"	ogram Sweep (Cy-	Author	r		Comment			Family	
Version	0.1			efined ID						
Name ✓ Input			Da	ta type	Default value		Commen	t		
Initial_Ca			Вос					of this OB		
Remaner Temp	nce		Вос	ol			=True, if r	emanent data ar	e available	
Constant										
Network 1:										
					<b>%FC9000</b> ab-Function-S71200"					
				— EN	ENO					
				I						
Network 2:	Anabei ta	a fwta apo ta ko	oumpia	sto factory	i/o					
					%FC1 uttonlights"					
				— EN	ENO					
				<u> </u>						
Network 3:	me to sta	rt ksekinaei to	ys diad	romous						
					%FC2 onvFromFactory"					
				— EN	ENO -					
Notwork 4:	Ta pracip	a ta allazoi dia	dromo	ı						
Network 4:	ra prasiii	a ta allazei diad	uromo							
				%FC3						
				"PushGreen" — EN ENG						
Network 5:										
					%FC4					
				"Pı EN	ut the tags"					
Network 6:										
				%FC5						
				emergency EN ENC						
Network 7:										
					%FC6					
				"Cou — EN	ent the boxes"			<del>_</del>		
Network 8:										
		T								

Totally Integrated **Automation Portal %M1.2** "Tag\_9" **%I0.1**"StopButton" %FC7 "stop" SR ENO "HMI".stop **%I0.0** "StartButton" "HMI".start

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### MHJ-PLC-Lab-Function-S71200 [FC9000]

MHJ-PLC-Lab-Function-S71200 Properties								
General								
Name	MHJ-PLC-Lab-Function- S71200	Number	9000	Туре	FC	Language	SCL	
Numbering	Manual							
Information								
Title		Author		Comment		Family		
Version	0.1	User-defined ID						

Name	Data type	Default value	Comment	
Input				
Output				
InOut				
<b>▼</b> Temp				
rdTimeReturn	Int			
▼ outputTime	DTL			
YEAR	UInt			
MONTH	USInt			
DAY	USInt			
WEEKDAY	USInt			
HOUR	USInt			
MINUTE	USInt			
SECOND	USInt			
NANOSECOND	UDInt			
SyncVal	Byte			
forVal	Int			
forVal_2	Int			
Value	Byte			
<b>▼</b> Constant				
CompVal	Byte	16#34		
Value_01	Byte	16#11		
Value_01_DW	DWord	16#A165_D992		
Value_02_DW	DWord	16#58BE_4401		
<b>▼</b> Return				
MHJ-PLC-Lab-Function-S71200	Void			

```
0001
0002 #Value:=PEEK(area := 16#82,
0003 dbNumber := 0,
0004
       byteOffset := 511);
0005 #Value := #Value + 1;
0006
0007 POKE (area := 16#82,
8000
     dbNumber := 0,
0009
       byteOffset := 511,
0010
      value := #Value);
0011
0012 POKE (area:=16#81,
0013 dbNumber:=0,
0014
       byteOffset:=1016,
0015
       value:=#Value_01_DW);
0016 POKE (area := 16#81,
0017 dbNumber := 0,
0018
       byteOffset := 1020,
0019
       value := #Value_02_DW);
0020
0021 POKE(area := 16#81,
0022
       dbNumber := 0,
0023
       byteOffset := 511,
0024
       value := B#16#00);
0025
0026 FOR #forVal := 0 TO 120 DO
0027
     FOR #forVal_2:=0 TO 10 DO
0028
         #rdTimeReturn:=RD SYS T(#outputTime);
0029
         #rdTimeReturn := WR SYS T(#outputTime);
0030
         #rdTimeReturn := RD_SYS_T(#outputTime);
0031
         #rdTimeReturn := WR_SYS_T(#outputTime);
0032
      END FOR;
0033
      #SyncVal:= PEEK(area := 16#81,
0034
               dbNumber := 0,
0035
               byteOffset := 511);
0036
     IF #SyncVal = #CompVal THEN
0037
          GOTO M 1;
0038 END IF;
0039 END FOR;
0040 RETURN;
0041
0042 M 1:
0043 POKE (area := 16#81,
```

#### Totally Integrated Automation Portal

```
0044    dbNumber := 0,
0045    byteOffset := 511,
0046    value := B#16#0);
0047
0048
0049
```

Symbol	Address	Туре	Comment
#CompVal	16#34	Byte	
#forVal		Int	
#forVal_2		Int	
#outputTime		DTL	
#rdTimeReturn		Int	
#SyncVal		Byte	
#Value		Byte	
#Value_01_DW	16#A165_D992	DWord	
#Value_02_DW	16#58BE_4401	DWord	

## Buttonlights [FC1]

Buttonlights Properties									
General									
Name	Name Buttonlights Number 1 Type FC Language LAD								
Numbering	Automatic								
Information									
Title		Author		Comment		Family			
Version	0.1	User-defined ID							

Name	Data type	Default value	Comment
Input			
Output			
InOut			
Temp			
Constant			
<b>▼</b> Return			
Buttonlights	Void		

### Network 1:

### Network 2:

```
%M0.1

"StopButton"

SR

"StopLight"

S Q

%I0.0

"StartButton"

R1
```

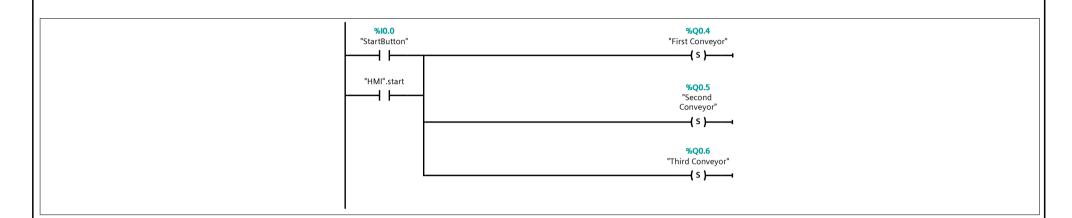
|--|

## StartConvFromFactory [FC2]

StartConvFromFactory Properties								
General								
Name	me StartConvFromFactory <b>Number</b> 2 <b>Type</b> FC <b>Language</b> LAD							
Numbering	Automatic							
Information								
Title		Author		Comment		Family		
Version	0.1	User-defined ID						

Name	Data type	Default value	Comment
Input			
Output			
InOut			
Temp			
Constant			
<b>▼</b> Return			
StartConvFromFactory	Void		

### Network 1:



Totally Integrated	
Automation Portal	

## PushGreen [FC3]

PushGreen Properties									
General									
Name	PushGreen Number 3 Type FC Language SCL								
Numbering	Automatic								
Information									
Title		Author		Comment		Family			
Version	0.1	User-defined ID							

Name	Data type	Default value	Comment
Input			
Output			
InOut			
Temp			
Constant			
▼ Return			
PushGreen	Void		

```
0001
0002 IF "Vision Sensor" = 4 AND "pusherbacklimit" = 1 THEN
0003    "pusher" := 1;
0004 ELSIF "Vision Sensor" <> 4 AND "pusherfrontlimit" = 1 THEN
0005    "pusher" := 0;
0006 END_IF;
```

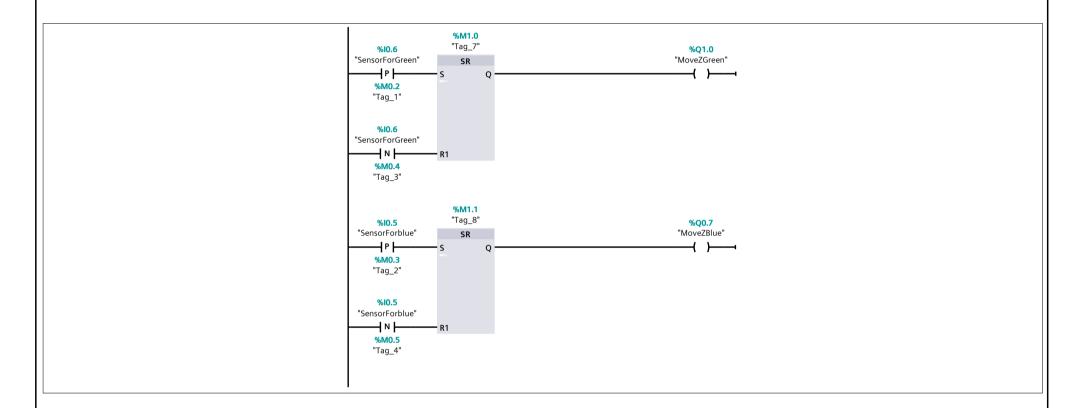
Symbol	Address	Type	Comment
"pusher"	%Q0.3	Bool	
"pusherbacklimit"	%10.3	Bool	
"pusherfrontlimit"	%10.4	Bool	
"Vision Sensor"	%ID34	DInt	

### Put the tags [FC4]

Put the tags Properties									
General									
Name	e Put the tags Number 4 Type FC Language LAD								
Numbering	Automatic								
Information									
Title		Author		Comment		Family			
Version	0.1	User-defined ID							

Name	Data type	Default value	Comment
	Data type	Delault value	Comment
Input			
Output			
InOut			
Temp			
Constant			
<b>▼</b> Return			
Put the tags	Void		

### Network 1:



Totally Inte Automation									
								•	
'LC_1 [C	PU 1211C D	C/DC/DC] /	Progra	m blocks					
HMI [DB1]	1								
ו פען וואור	J								
HMI Propertie	es								
General									
	HMI	Numb	er	1	Туре	DB	Languag	j <b>e</b> DE	3
lame									
	Automatic								
Numbering									
lumbering nformation		Autho	)r		Comment		Family		
Name Numbering Information Title Version			or defined ID		Comment		Family		

ne	Data type	Start value	Retain		able	HMI engi- neering		Supervi- sion	Comment
Static									
blue boxes	Int	0	False	True	True	True	False		
Reset Count	Bool	false	False	True	True	True	False		
Green boxes	Int	0	False	True	True	True	False		
emergency hmi	Bool	false	False	True	True	True	False		
start	Bool	false	False	True	True	True	False		
stop	Bool	false	False	True	True	True	False		

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Automatio	on Portal

### emergency [FC5]

emergency Properties									
General									
Name	emergency	Number	5	Type	FC	Language	SCL		
Numbering	Automatic								
Information									
Title		Author		Comment		Family			
Version	0.1	User-defined ID							

Name	Data type	Default value	Comment	
Input				
Output				
InOut				
Temp				
Constant				
<b>▼</b> Return				
emergency	Void			

Symbol	Address	Туре	Comment
"emergency stop"	%10.2	Bool	
"First Conveyor"	%Q0.4	Bool	
"HMI"."emergency hmi"		Bool	
"pusher"	%Q0.3	Bool	
"Second Conveyor"	%Q0.5	Bool	
"Sensor Forblue"	%10.5	Bool	
"SensorForGreen"	%10.6	Bool	
"Third Conveyor"	%Q0.6	Bool	

|--|

## Count the boxes [FC6]

Count the boxes	Count the boxes Properties									
General										
Name	Count the boxes	Number	6	Туре	FC	Language	LAD			
Numbering	Automatic									
Information										
Title		Author		Comment		Family				
Version	0.1	User-defined ID								

Name	Data type	Default value	Comment
Input			
Output			
InOut			
Temp			
Constant			
<b>▼</b> Return			
Count the boxes	Void		

#### Network 1:

```
%IO.7
"SensorBlueCount
"

N | CU Q

%M0.6
"Tag_5"

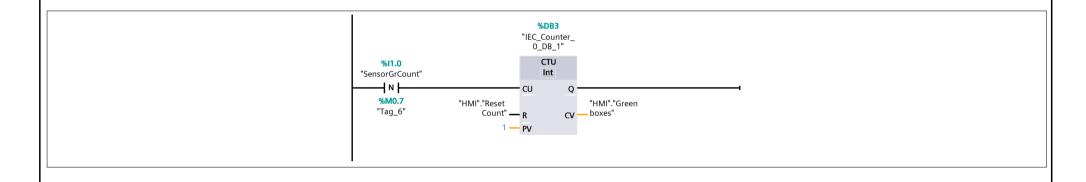
COUNT"—R

CV

Doxes"

WHMI"."blue
boxes"
```

### Network 2:



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### stop [FC7]

stan Dyanautias									
stop Properties									
General									
Name	stop	Number	7	Type	FC	Language	SCL		
Numbering	Automatic								
Information									
Title		Author		Comment		Family			
Version	0.1	User-defined ID							

Name	Data type	Default value	Comment
Input			
Output			
InOut			
Temp			
Constant			
<b>▼</b> Return			
stop	Void		

Symbol	Address	Туре	Comment
"First Conveyor"	%Q0.4	Bool	
"MoveZBlue"	%Q0.7	Bool	
"MoveZGreen"	%Q1.0	Bool	
"pusher"	%Q0.3	Bool	
"Second Conveyor"	%Q0.5	Bool	
"SensorForblue"	%IO.5	Bool	
"SensorForGreen"	%10.6	Bool	
"Third Conveyor"	%Q0.6	Bool	

Totally Inte									
PLC_1 [CPU 1211C DC/DC/DC] / Program blocks / System blocks / Program resources IEC_Counter_0_DB [DB2]									
IEC_Counter_0_DB Properties									
General									
Name	IFC Counter 0 DB	Number	2	Type	DB	Language	DB		

Numbering

Information

Automatic

miormation												
Title			Author	Simatic		Comment				Family	IEC	
Version	1.0		User-defined ID	efined ID CNTR								
Name		Data typ	oe Start val	ne	Retain	HMI/OPC UA	able	HMI engi- neering		Supervi- sion	Comment	
<b>▼</b> Static												
CU		Bool	false		True	True	True	True	False			
CD		Bool	false		True	True	True	True	False			
R		Bool	false		True	True	True	True	False			
LD		Bool	false		True	True	True	True	False			
QU		Bool	false		True	True	True	True	False			
QD		Bool	false		True	True	True	True	False			
PV		Int	0		True	True	True	True	False			
CV		Int	0		True	True	True	True	False			

	Totally Integrated Automation Portal									
		'DC] / Progra	ım blocks / Syste	em blocks /	Program resource	es				
	er_0_DB_1 [DB3]  _DB_1 Properties									
General										
Name	IEC_Counter_0_DB_1	Number	3	Туре	DB	Language	DB			
Numbering	Automatic					<u> </u>				
Information										
Title		Author	Simatic	Comment		Family	IEC			
Version	1.0	User-defined ID	CLUTD			<del>-</del>				

Version 1.0	User	defined ID CNTR							
Name	Data type	Start value	Retain		able	HMI engi- neering		Supervi- sion	Comment
<b>▼</b> Static									
CU	Bool	false	True	True	True	True	False		
CD	Bool	false	True	True	True	True	False		
R	Bool	false	True	True	True	True	False		
LD	Bool	false	True	True	True	True	False		
QU	Bool	false	True	True	True	True	False		
QD	Bool	false	True	True	True	True	False		
PV	Int	0	True	True	True	True	False		
CV	Int	0	True	True	True	True	False		

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mation Portal	

# PLC\_1 [CPU 1211C DC/DC/DC] / PLC tags / Standard-Variablentabelle [57]

# PLC tags

PLC tags									
	Name	Data type	Address	Retain		Writable from HMI/OPC UA	HMI engi-	Supervision	Comment
-00	StartButton	Bool	%10.0	False	True	True	True		
-	StopButton	Bool	%10.1	False	True	True	True		
400	StartLight	Bool	%Q0.0	False	True	True	True		
400	StopLlght	Bool	%Q0.1	False	True	True	True		
400	emergency stop	Bool	%10.2	False	True	True	True		
-	FlipFlop1	Bool	%M0.0	False	True	True	True		
-	FlipFlop2	Bool	%M0.1	False	True	True	True		
-	First Conveyor	Bool	%Q0.4	False	True	True	True		
-	Second Conveyor	Bool	%Q0.5	False	True	True	True		
-	Vision Sensor	DInt	%ID34	False	True	True	True		
-63	Third Conveyor	Bool	%Q0.6	False	True	True	True		
401	pusher	Bool	%Q0.3	False	True	True	True		
40	pusherbacklimit	Bool	%10.3	False	True	True	True		
-90	pusherfrontlimit	Bool	%10.4	False	True	True	True		
-	SensorForblue	Bool	%10.5	False	True	True	True		
-63	SensorForGreen	Bool	%10.6	False	True	True	True		
401	MoveZBlue	Bool	%Q0.7	False	True	True	True		
40	MoveZGreen	Bool	%Q1.0	False	True	True	True		
-90	Tag_1	Bool	%M0.2	False	True	True	True		
-90	Tag_2	Bool	%M0.3	False	True	True	True		
-	Tag_3	Bool	%M0.4	False	True	True	True		
-01	Tag_4	Bool	%M0.5	False	True	True	True		
401	SensorBlueCount	Bool	%10.7	False	True	True	True		
-	SensorGrCount	Bool	%I1.O	False	True	True	True		
-	Tag_5	Bool	%M0.6	False	True	True	True		
-	Tag_6	Bool	%M0.7	False	True	True	True		
-01	Tag_7	Bool	%M1.0	False	True	True	True		
-01	Tag_8	Bool	%M1.1	False	True	True	True		
-	Tag_9	Bool	%M1.2	False	True	True	True		