

# COMP2421 Computer Organization

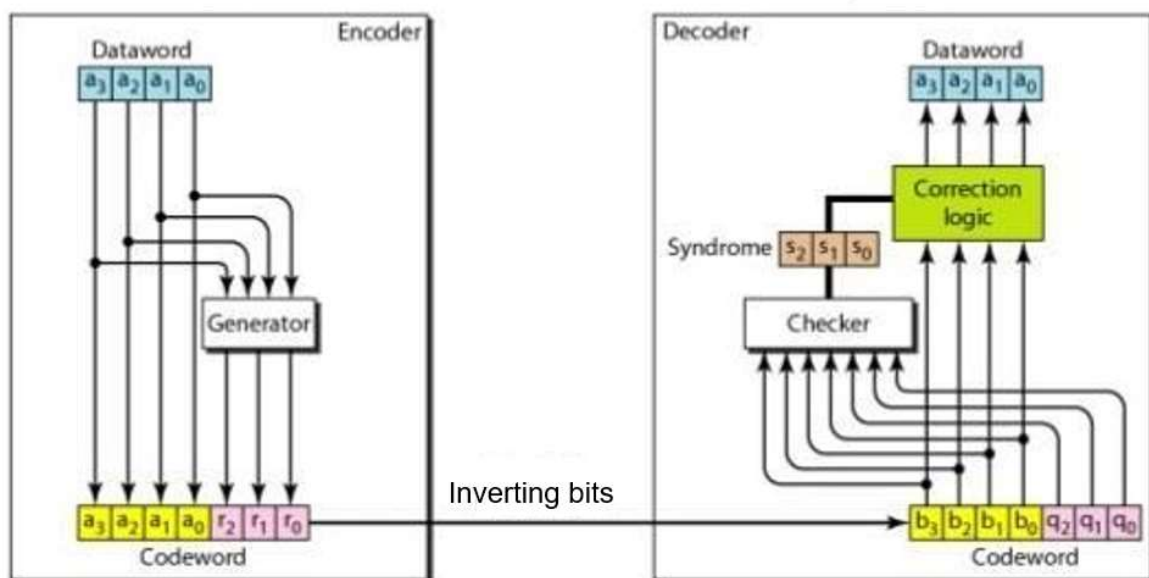
## Programming Assignment 2

**Due Date and Time: Apr 9, 2023, at 11:59pm**

### Hamming Code

#### 1. Objectives

The objective of this assignment is to use combinational logic to implement the (7-4) Hamming Code using Logisim. By completing the task, we will implement the Encoder and the Decoder as required in the figure below.



In the Encoder, a dataword ( $a_3, a_2, a_1, a_0$ ) will be provided at the beginning. With the dataword, the corresponding Hamming codeword ( $a_3, a_2, a_1, a_0, r_2, r_1, r_0$ ) will be generated. Part of the bits ( $r_0, r_1, a_0$ ) will be inverted as ( $b_3, b_2, b_1, b_0, q_2, q_1, q_0$ ). For example, ( $a_3, a_2, a_1, a_0, r_2, r_1, r_0$ ) as 1000111 can be changed ( $b_3, b_2, b_1, b_0, q_2, q_1, q_0$ ) as 1001100.

Taking the ( $b_3, b_2, b_1, b_0, q_2, q_1, q_0$ ) codeword, the Decoder will detect if there is any erroneous bit. When an error occurs, the Decoder will correct the respectively final dataword as ( $a_3, a_2, a_1, a_0$ ).

Your task is thus to implement the combinational logic. This task is further divided to two major sub-tasks and described in the next section.

**Note:** this task requires you to read the introduction and specifications of some components in Logisim, and learn by yourself based on what you have learned through the Lab sessions. Related references as well as examples are provided in the end of this document.

## 2. Task Description

The whole task is composed of two sub-tasks: (1) build up the Encoder and (2) implement the Decoder.

### 2.1 Subtask One

First, you will need to design an Encoder with 16 inputs and 7 outputs. Each input corresponds to one button and the 7 outputs are the seven-bit encoded result (denoted as  $a_3a_2a_1a_0r_2r_1r_0$ ). Specifically, if button  $i$  is pressed, the dataword  $a_3a_2a_1a_0$  would be the binary encoding of  $i$ . Example: if button 5 is pressed,  $a_3a_2a_1a_0 = 0101$ . You will use the component Button, Priority Encoder, and Splitter from Logisim to implement this function. Play with the Priority Encoder to make sure that it will produce the desired outputs. Especially, the bit width of the output of the Priority Encoder could be larger than 1. Thus, you will need a Splitter to split the output bits. You use a Hex Digital Display to show the values of  $a_3a_2a_1a_0$  dataword. Play with this circuit until you got the desired results. The figure shows an example of the Hex Digital display with the value ( $1010 == A$ ).



Next, you design/implement the Generator with the (7-4) Hamming coding method. The Generator will be resulted with  $r_2r_1r_0$  bit string. You use another Hex Digital Display to show the values of  $r_2r_1r_0$ .

### 2.2 Subtask Two

Second, you are asked to design/implement the Decoder. Before doing the Decoder work, a simple circuit design to perform inversions some of the bits ( $r_0, r_1, a_0$ ). For example, ( $a_3, a_2, a_1, a_0, r_2, r_1, r_0$ ) as 1000111 can be changed ( $b_3, b_2, b_1, b_0, q_2, q_1, q_0$ ) as 1001100. You use 2 Hex displays to show the values of  $b_3b_2b_1b_0$  and  $q_2q_1q_0$ .

In the Decoder, there are 2 steps. The initial step is to develop the Checker for detecting if there is any erroneous bit in  $b_3b_2b_1b_0q_2q_1q_0$  bit string. The Checker would have a checking result as a syndrome ( $s_2, s_1, s_0$ ). The following table shows the meanings of the detecting results.

$s_2s_1s_0$	Erroneous bit
000	None
100	$q_0$
010	$q_1$
110	$b_0$
001	$q_2$
101	$b_1$
011	$b_2$
111	$b_3$

The second step of the Decoder has been developed the Correction Logic circuit. Based on the s2s1s0 result, the final dataword (a3a2a1a0) would be generated. In order to verify the results, 2 Hex displays would show final a3a2a1a0 and s2s1s0.

### 3. What to submit

- 1) One circuit. Submit a Hamming Code circuit that implements the desired functions. Name the file as “HC\_circuit\_XXXXXXXX.circ”, where “XXXXXXXX” represents your 8-digit student number (the last letter is not needed).
- 2) An experiment report. The report should include the following contents:
  - Screen shot of the Hamming Code circuit.
  - Three pictures of 3 different buttons pressed. In each picture, you should show that when button i is pressed, the respectively Hex displays will be shown.

### 4. Assessment

- Subtask One (50 pts)
- Subtask Two (40 pts)
- Report (10 pts)

### 5. Useful References

- 1) Specifications of all components in Logisim.  
<http://www.cburch.com/logisim/docs/2.7/en/html/libs/plexers/index.html>
- 2) Example of using Priority Encoder. <http://www.cs.unca.edu/brock/classes/Fall2012/csci255/labs/lab03.html>
- 3) Hamming (7,4) Code Discussion and Implementation,  
<https://michaeldipperstein.github.io/hamming.html>
- 4) Logisim: How to use a hex display and a splitter, Logisim: How to use a hex display and a splitter - YouTube