

# Code Examples

## Release Notes

Product name: Code Examples

Release number and version: V1.0.4

Type of release: Official Release

Release method: Infineon GitHub

AUTOSAR specification: None

Compiler support: Tasking v1.1r4

Processor platform: AURIX™ TC2xx, AURIX™ TC3xx

Date: 2020-08-13

Previous release number and version: V1.0.3

## About this document

### Scope and purpose

This document details the release contents, all known issues in the release and the changes from the last release, together with information on tools, compiler options and support packages.

New issues identified since the last release of this document are detailed first, followed by all issues identified in previous versions of this release.

*Note: This release is not intended for production use.*

**Attention: Refer to the *Limitations and deviations* chapter before using the software for integration.**

### Intended audience

This document is intended for anyone using the AURIX™ Development Studio and the AURIX™ microcontroller products from Infineon Technologies AG.

### Reference documents

- None

## Table of contents

<b>About this document.....</b>	<b>1</b>
<b>Table of contents.....</b>	<b>2</b>
<b>1 Release contents .....</b>	<b>3</b>
1.1 Release overview .....	3
1.2 Released items .....	3
1.3 Compatibility .....	18
<b>2 Summary of changes .....</b>	<b>19</b>
<b>3 Limitations and deviations .....</b>	<b>22</b>
3.1 Limitations.....	22
3.2 Deviations.....	22
3.2.1 HIS-MISRA violations.....	22
<b>4 Installation .....</b>	<b>23</b>

## 1 Release contents

### 1.1 Release overview

The code examples provide a guide on how to use and configure the peripherals of AURIX™ TC2xx and AURIX™ TC3xx devices through the Infineon Low Level Drivers (iLLDs). All the projects should be compiled using the AURIX™ Development Studio.

### 1.2 Released items

The release contains 141 code examples, each providing all the required files for compiling and running it as a standalone project.

**Table 1 Release content**

Component	Version	Specifications
ADC_Background_Scan_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ADC_Filtering_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ADC_Filtering_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ADC_Group_Scan_1_KIT_TC297_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Changed baud rate from 9600 to 115200. Added a median filter on the oversampled bits for UART to improve reliability.
ADC_Queued_Scan_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ADC_Queued_Scan_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None

Component	Version	Specifications
ADC_Single_Channel_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ADC_Single_Channel_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ASCLIN_LIN_Master_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ASCLIN_LIN_Master_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ASCLIN_Shell_UART_1_KIT_TC237_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ASCLIN_Shell_UART_1_KIT_TC275_SB	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ASCLIN_Shell_UART_1_KIT_TC277_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ASCLIN_Shell_UART_1_KIT_TC297_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Renamed macro ENDL to ENDLINE due to a conflict with iLLDs
ASCLIN_Shell_UART_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ASCLIN_SPI_Master_1_KIT_TC297_TFT	V1.0.0	<b>Type</b>

Component	Version	Specifications
		Code example
		<b>Known Issues</b> None
ASCLIN_SPI_Master_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ASCLIN_UART_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
ASCLIN_UART_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Assembly_C_Code_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Assembly_C_Code_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Blinky_LED_1_KIT_TC234_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Blinky_LED_1_KIT_TC237_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Blinky_LED_1_KIT_TC265_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Blinky_LED_1_KIT_TC275_LK	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Blinky_LED_1_KIT_TC275_SB	V1.0.0	<b>Type</b>

Component	Version	Specifications
		Code example
		<b>Known Issues</b> None
Blinky_LED_1_KIT_TC277_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Blinky_LED_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Blinky_LED_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
BUS_Register_Protection_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
BUS_Register_Protection_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
CCU_Clock_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
CCU6_ADC_1_KIT_TC297_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Changed baud rate from 9600 to 115200
CCU6_Interrupt_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
CCU6_Interrupt_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example

Component	Version	Specifications
		<b>Known Issues</b> None
CCU6_PWM_Capture_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
CCU6_PWM_Capture_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
CCU6_PWM_Generation_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
CCU6_PWM_Generation_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
CPU_Perf_Counters_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
CPU_Perf_Counters_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
CPU_Trap_Recognition_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
DMA_ADC_Transfer_1_KIT_TC297_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Changed baud rate from 9600 to 115200. Added a median filter on the oversampled bits for UART to improve reliability.
DMA_Linked_List_Mode_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example

Component	Version	Specifications
		<b>Known Issues</b> None
DMA_Linked_List_Mode_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example <b>Known Issues</b> None
DMA_Mem_to_Mem_1_KIT_TC297_TFT	V1.0.1	<b>Type</b> Code example <b>Known Issues</b> None <b>Release Changes</b> V1.0.1: Added IfxCpu_enableInterrupts() function in Cpu0_Main.c file.
DMA_Mem_to_Mem_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example <b>Known Issues</b> None
DSADC_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example <b>Known Issues</b> None
ERU_Interrupt_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example <b>Known Issues</b> None
ERU_Interrupt_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example <b>Known Issues</b> None
FCE_CRC_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example <b>Known Issues</b> None
FCE_CRC_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example <b>Known Issues</b> None
Flash_Programming_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example <b>Known Issues</b>



Component	Version	Specifications
		None
Flash_Programming_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GPIO_LED_Button_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GPIO_LED_Button_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GPT12_PWM_Capture_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GPT12_PWM_Capture_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GPT12_PWM_Generation_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GPT12_PWM_Generation_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GPT12_Timer_Concatenation_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GPT12_Timer_Concatenation_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GPT12_Timer_Interrupt_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b>

Component	Version	Specifications
		None
GPT12_Timer_Interrupt_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_ATOM_Interrupt_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_ATOM_Interrupt_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_ATOM_PWM_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_ATOM_PWM_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_TIM_Capture_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_TIM_Capture_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_TOM_Interrupt_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_TOM_PWM_1_KIT_TC234_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_TOM_PWM_1_KIT_TC237_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b>

Component	Version	Specifications
		None
GTM_TOM_PWM_1_KIT_TC265_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_TOM_PWM_1_KIT_TC275_LK	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_TOM_PWM_1_KIT_TC275_SB	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_TOM_PWM_1_KIT_TC277_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_TOM_PWM_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
GTM_TOM_PWM_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
I2C_Read_Ext_Device_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
I2C_Read_Ext_Device_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Interrupt_Prio_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Interrupt_Prio_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b>

Component	Version	Specifications
		None
IOM_PWM_Monitor_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
IOM_Signals_Comparison_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
MCMCAN_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Memory_Access_Performance_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
MPU_Memory_Protection_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
MTU_MBIST_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
MULTICAN_1_KIT_TC297_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Renamed files from MULTICAN_1.c and MULTICAN_1.h to MULTICAN.c and MULTICAN.h, corrected accordingly the include instructions.
MULTICAN_FD_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
MULTICAN_GW_TX_FIFO_1_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b>

Component	Version	Specifications
		None
MULTICAN_RX_FIFO_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Multicore_1_KIT_TC275_LK	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Multicore_1_KIT_TC275_SB	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Multicore_1_KIT_TC277_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Multicore_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Multicore_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
PMS_Power_Down_Idle_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
PWM_Buzzer_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
PWM_Buzzer_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
RAM_Run_Function_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b>

Component	Version	Specifications
		None
RAM_Run_Function_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SCU_Clock_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SCU_Die_Temp_Sensor_1_KIT_TC297_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Version number increased to be synchronized with the tutorial document
SCU_Die_Temp_Sensor_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SCU_Emergency_Stop_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SCU_Emergency_Stop_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SCU_Power_Down_Idle_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SCU_Power_Down_Sleep_1_KIT_TC297_TFT	V1.0.2	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Version number increased to be synchronized with the tutorial document V1.0.2: Version number increased to be synchronized with the tutorial document

Component	Version	Specifications
SCU_Power_Down_Sleep_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SCU_Power_Down_Standby_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SCU_Reset_Detection_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SMU_Emergency_Stop_Alarm_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SMU_Emergency_Stop_Alarm_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SMU_Fault_Signaling_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SMU_Fault_Signaling_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SMU_IR_Alarm_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SMU_IR_Alarm_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SMU_Reset_Alarm_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None

Component	Version	Specifications
SMU_Reset_Alarm_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SPI_CPU_1_KIT_TC297_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Changes in the implementation, in order to use interrupts.
SPI_CPU_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SPI_DMA_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
SPI_DMA_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
STM_Interrupt_1_KIT_TC234_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Version number increased to be synchronized with the tutorial document
STM_Interrupt_1_KIT_TC237_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Version number increased to be synchronized with the tutorial document
STM_Interrupt_1_KIT_TC265_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None



Component	Version	Specifications
		<b>Release Changes</b> V1.0.1: Version number increased to be synchronized with the tutorial document
STM_Interrupt_1_KIT_TC275_LK	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
STM_Interrupt_1_KIT_TC275_SB	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Version number increased to be synchronized with the tutorial document
STM_Interrupt_1_KIT_TC277_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Version number increased to be synchronized with the tutorial document
STM_Interrupt_1_KIT_TC297_TFT	V1.0.1	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Version number increased to be synchronized with the tutorial document
STM_Interrupt_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
STM_System_Time_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
STM_System_Time_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
UART_DMA_Transfer_1_KIT_TC297_TFT	V1.0.0	<b>Type</b>

Component	Version	Specifications
		Code example
		<b>Known Issues</b> None
UART_DMA_Transfer_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
UART_VCOM_1_KIT_TC297_TFT	V1.0.2	<b>Type</b> Code example
		<b>Known Issues</b> None
		<b>Release Changes</b> V1.0.1: Changed baud rate from 19200 to 115200 V1.0.2: Version number increased to be synchronized with the tutorial document
UART_VCOM_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Watchdog_1_KIT_TC297_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None
Watchdog_1_KIT_TC397_TFT	V1.0.0	<b>Type</b> Code example
		<b>Known Issues</b> None

### 1.3 Compatibility

All the code examples are compatible with AURIX™ Development Studio V1.2.2.

## 2 Summary of changes

This chapter describes the fixes for issues from previous version(s).

### Version 1.0.4:

- Added the following code examples for the board KIT\_AURIX\_TC297\_TFT:
  - MPU\_Memory\_Protection\_1\_KIT\_TC297\_TFT, MULTICAN\_RX\_FIFO\_1\_KIT\_TC297\_TFT.
- Added the following code examples for the board KIT\_AURIX\_TC275\_LITE:
  - Blinky\_LED\_1\_KIT\_TC275\_LK, GTM\_TOM\_PWM\_1\_KIT\_TC275\_LK, Multicore\_1\_KIT\_TC275\_LK, STM\_Interrupt\_1\_KIT\_TC275\_LK.
- Added the following code examples for the board KIT\_A2G\_TC397\_5V\_TFT:
  - ADC\_Filtering\_1\_KIT\_TC397\_TFT, ADC\_Queued\_Scan\_1\_KIT\_TC397\_TFT, ADC\_Single\_Channel\_1\_KIT\_TC397\_TFT, MCMCAN\_1\_KIT\_TC397\_TFT.

### Version 1.0.3:

- Added the following code examples for the board KIT\_A2G\_TC397\_5V\_TFT:
  - ASCLIN\_LIN\_Master\_1\_KIT\_TC397\_TFT, ASCLIN\_Shell\_UART\_1\_KIT\_TC397\_TFT, ASCLIN\_SPI\_Master\_1\_KIT\_TC397\_TFT, ASCLIN\_UART\_1\_KIT\_TC397\_TFT, Assembly\_C\_Code\_1\_KIT\_TC397\_TFT, Blinky\_LED\_1\_KIT\_TC397\_TFT, BUS\_Register\_Protection\_1\_KIT\_TC397\_TFT, CCU\_Clock\_1\_KIT\_TC397\_TFT, CCU6\_Interrupt\_1\_KIT\_TC397\_TFT, CCU6\_PWM\_Capture\_1\_KIT\_TC397\_TFT, CCU6\_PWM\_Generation\_1\_KIT\_TC397\_TFT, CPU\_Perf\_Counters\_1\_KIT\_TC397\_TFT, DMA\_Linked\_List\_Mode\_1\_KIT\_TC397\_TFT, DMA\_Mem\_to\_Mem\_1\_KIT\_TC397\_TFT, ERU\_Interrupt\_1\_KIT\_TC397\_TFT, FCE\_CRC\_1\_KIT\_TC397\_TFT, Flash\_Programming\_1\_KIT\_TC397\_TFT, GPIO\_LED\_Button\_1\_KIT\_TC397\_TFT, GPT12\_PWM\_Capture\_1\_KIT\_TC397\_TFT, GPT12\_PWM\_Generation\_1\_KIT\_TC397\_TFT, GPT12\_Timer\_Concatenation\_1\_KIT\_TC397\_TFT, GPT12\_Timer\_Interrupt\_1\_KIT\_TC397\_TFT, GTM\_ATOM\_Interrupt\_1\_KIT\_TC397\_TFT, GTM\_ATOM\_PWM\_1\_KIT\_TC397\_TFT, GTM\_TIM\_Capture\_1\_KIT\_TC397\_TFT, GTM\_TOM\_PWM\_1\_KIT\_TC397\_TFT, I2C\_Read\_Ext\_Device\_1\_KIT\_TC397\_TFT, Interrupt\_Prio\_1\_KIT\_TC397\_TFT, Multicore\_1\_KIT\_TC397\_TFT, PMS\_Power\_Down\_Idle\_1\_KIT\_TC397\_TFT, PWM\_Buzzer\_1\_KIT\_TC397\_TFT, RAM\_Run\_Function\_1\_KIT\_TC397\_TFT, SCU\_Die\_Temp\_Sensor\_1\_KIT\_TC397\_TFT, SCU\_Emergency\_Stop\_1\_KIT\_TC397\_TFT, SCU\_Power\_Down\_Sleep\_1\_KIT\_TC397\_TFT, SMU\_Emergency\_Stop\_Alarm\_1\_KIT\_TC397\_TFT, SMU\_Fault\_Signaling\_1\_KIT\_TC397\_TFT, SMU\_IR\_Alarm\_1\_KIT\_TC397\_TFT, SMU\_Reset\_Alarm\_1\_KIT\_TC397\_TFT, SPI\_CPU\_1\_KIT\_TC397\_TFT, SPI\_DMA\_1\_KIT\_TC397\_TFT, STM\_Interrupt\_1\_KIT\_TC397\_TFT, STM\_System\_Time\_1\_KIT\_TC397\_TFT, UART\_DMA\_Transfer\_1\_KIT\_TC397\_TFT, UART\_VCOM\_1\_KIT\_TC397\_TFT, Watchdog\_1\_KIT\_TC397\_TFT.
- Added the following code examples for the board KIT\_AURIX\_TC297\_TFT:
  - ADC\_Filtering\_1\_KIT\_TC297\_TFT, ADC\_Queued\_Scan\_1\_KIT\_TC297\_TFT, Assembly\_C\_Code\_1\_KIT\_TC297\_TFT, GPT12\_PWM\_Generation\_1\_KIT\_TC297\_TFT, Memory\_Access\_Performance\_1\_KIT\_TC297\_TFT

- Examples updated:
  - DMA\_Mem\_to\_Mem\_1\_KIT\_TC297\_TFT (V1.0.1), SCU\_Power\_Down\_Sleep\_1\_KIT\_TC297\_TFT (V1.0.2), STM\_Interrupt\_1\_KIT\_TC234\_TFT (V1.0.1), STM\_Interrupt\_1\_KIT\_TC237\_TFT (V1.0.1), STM\_Interrupt\_1\_KIT\_TC265\_TFT (V1.0.1), STM\_Interrupt\_1\_KIT\_TC275\_SB (V1.0.1), STM\_Interrupt\_1\_KIT\_TC277\_TFT (V1.0.1), STM\_Interrupt\_1\_KIT\_TC297\_TFT (V1.0.1), UART\_VCOM\_1\_KIT\_TC297\_TFT (V1.0.2)

#### Version 1.0.2:

- Updated the name of all the code examples developed for the board KIT\_AURIX\_TC297\_TFT to end with the suffix “\_KIT\_TC297\_TFT”
- Added the following code examples for the board KIT\_AURIX\_TC297\_TFT:
  - ADC\_Single\_Channel\_1\_KIT\_TC297\_TFT, BUS\_Register\_Protection\_1\_KIT\_TC297\_TFT, CPU\_Trap\_Recognition\_1\_KIT\_TC297\_TFT, Flash\_Programming\_1\_KIT\_TC297\_TFT, GPT12\_Timer\_Interrupt\_1\_KIT\_TC297\_TFT, SCU\_Power\_Down\_Standby\_1\_KIT\_TC297\_TFT, SMU\_Fault\_Signaling\_1\_KIT\_TC297\_TFT, STM\_System\_Time\_1\_KIT\_TC297\_TFT
- Added the following code examples for the board KIT\_AURIX\_TC234\_TFT:
  - Blinky\_LED\_1\_KIT\_TC234\_TFT, GTM\_TOM\_PWM\_1\_KIT\_TC234\_TFT, STM\_Interrupt\_1\_KIT\_TC234\_TFT
- Added the following code examples for the board KIT\_AURIX\_TC237\_TFT:
  - ASCLIN\_Shell\_UART\_1\_KIT\_TC237\_TFT, Blinky\_LED\_1\_KIT\_TC237\_TFT, GTM\_TOM\_PWM\_1\_KIT\_TC237\_TFT, STM\_Interrupt\_1\_KIT\_TC237\_TFT
- Added the following code examples for the board KIT\_AURIX\_TC265\_TFT:
  - Blinky\_LED\_1\_KIT\_TC265\_TFT, GTM\_TOM\_PWM\_1\_KIT\_TC265\_TFT, STM\_Interrupt\_1\_KIT\_TC265\_TFT
- Added the following code examples for the board KIT\_AURIX\_TC275\_SB:
  - ASCLIN\_Shell\_UART\_1\_KIT\_TC275\_SB, Blinky\_LED\_1\_KIT\_TC275\_SB, GTM\_TOM\_PWM\_1\_KIT\_TC275\_SB, Multicore\_1\_KIT\_TC275\_SB, STM\_Interrupt\_1\_KIT\_TC275\_SB
- Added the following code examples for the board KIT\_AURIX\_TC277\_TFT:
  - ASCLIN\_Shell\_UART\_1\_KIT\_TC277\_TFT, Blinky\_LED\_1\_KIT\_TC277\_TFT, GTM\_TOM\_PWM\_1\_KIT\_TC277\_TFT, Multicore\_1\_KIT\_TC277\_TFT, STM\_Interrupt\_1\_KIT\_TC277\_TFT
- examples updated (all to version V1.0.1):
  - ADC\_Group\_Scan\_1\_KIT\_TC297\_TFT, ASCLIN\_Shell\_UART\_1\_KIT\_TC297\_TFT, CCU6\_ADC\_1\_KIT\_TC297\_TFT, DMA\_ADC\_Transfer\_1\_KIT\_TC297\_TFT, SCU\_Die\_Temp\_Sensor\_1\_KIT\_TC297\_TFT, SPI\_CPU\_1\_KIT\_TC297\_TFT, SCU\_Power\_Down\_Sleep\_1\_KIT\_TC297\_TFT, UART\_VCOM\_1\_KIT\_TC297\_TFT

#### Version 1.0.1:

- Added the following code examples: ADC\_Background\_Scan\_1, ADC\_Group\_Scan\_1, Blinky\_LED\_1, CCU6\_ADC\_1, CCU6\_PWM\_Capture\_1, GTM\_TIM\_Capture\_1, GTM\_TOM\_PWM\_1, IOM\_PWM\_Monitor\_1, IOM\_Signals\_Comparison\_1, MTU\_MBIST\_1, MULTICAN\_FD\_1, Multicore\_1, RAM\_Run\_Function\_1, SCU\_Power\_Down\_Idle\_1, SCU\_Power\_Down\_Sleep\_1, SPI\_DMA\_1, Watchdog\_1
- MULTICAN\_1 updated to version V1.0.1

## **3 Limitations and deviations**

This chapter describes the limitations and deviations due to software/hardware design constraints.

### **3.1 Limitations**

There are no limitations towards the established usage of the projects for the purpose of demonstrating AURIX™ TC2xx, AURIX™ TC3xx microcontroller hardware and software products.

### **3.2 Deviations**

There are no deviations towards the established usage of the projects for the purpose of demonstrating AURIX™ TC2xx, AURIX™ TC3xx microcontroller hardware and software products.

#### **3.2.1 HIS-MISRA violations**

Not applicable for this product.

## **4 Installation**

Please refer to the **Infineon-AURIX\_Getting\_Started\_with\_AURIX\_Development\_Studio-EN.pdf** document, which can be found at <http://www.infineon.com/aurixdevelopmentstudio> .

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**Document reference**

**Code\_Examples\_V1\_0\_4\_ReleaseNote**

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