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Optimizing GPU Programs by Partial Evaluation

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Problem Statement

Memory traffic is a bottleneck of GPGPU programs. In the data analysis, part of kernel parameters is fixed during many kernel runs.

- Patterns in substring matching
- HMM in homology search
- Query in graph database querying

Known parameters still increase memory traffic. Can we automatically optimize procedure when some parameters are known?

Results

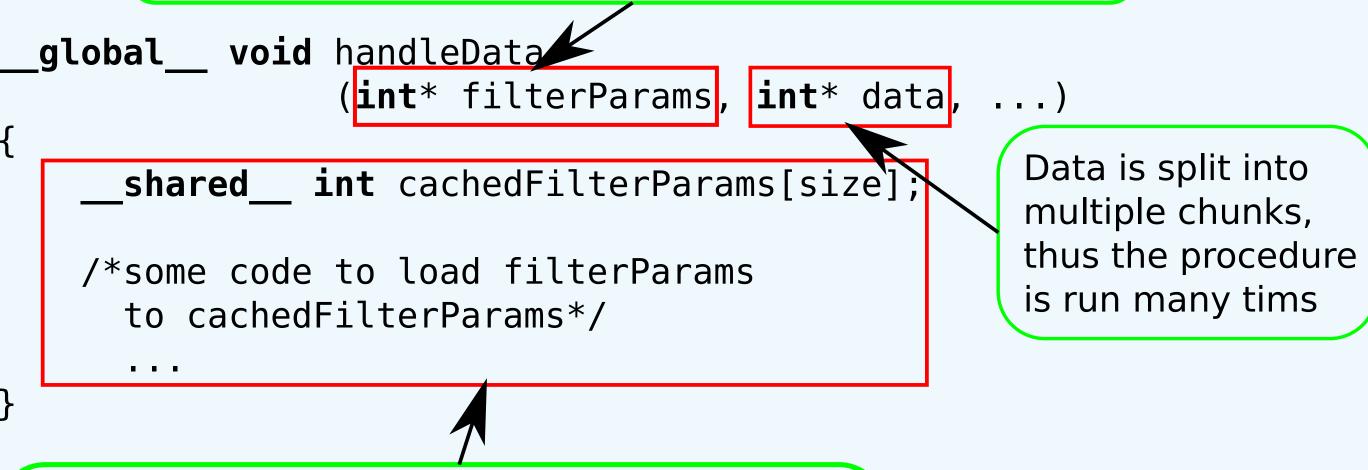
- It is possible to optimize procedures with partially known parameters by means of **partial** evaluation [1].
 - Optimized procedure for substring matching is up to 2 times faster.
 - Automatically optimized procedure for 2D convolution is comparable with the one optimized manually.
 - Optimization effect depends on GPU architecture and initial memory access pattern.

Future Research

- Utilization of LLVM.mix, a partial evaluator for LLVM IR, to use CUDA C instead of DSL.
- Reduction of specialization overhead to make it applicable in run-time.
- Integration with shared memory register spilling [2].
- Real-world examples evaluation.
 - Homology search in bioinformatics.
 - Regular expression matching.

Motivating Example

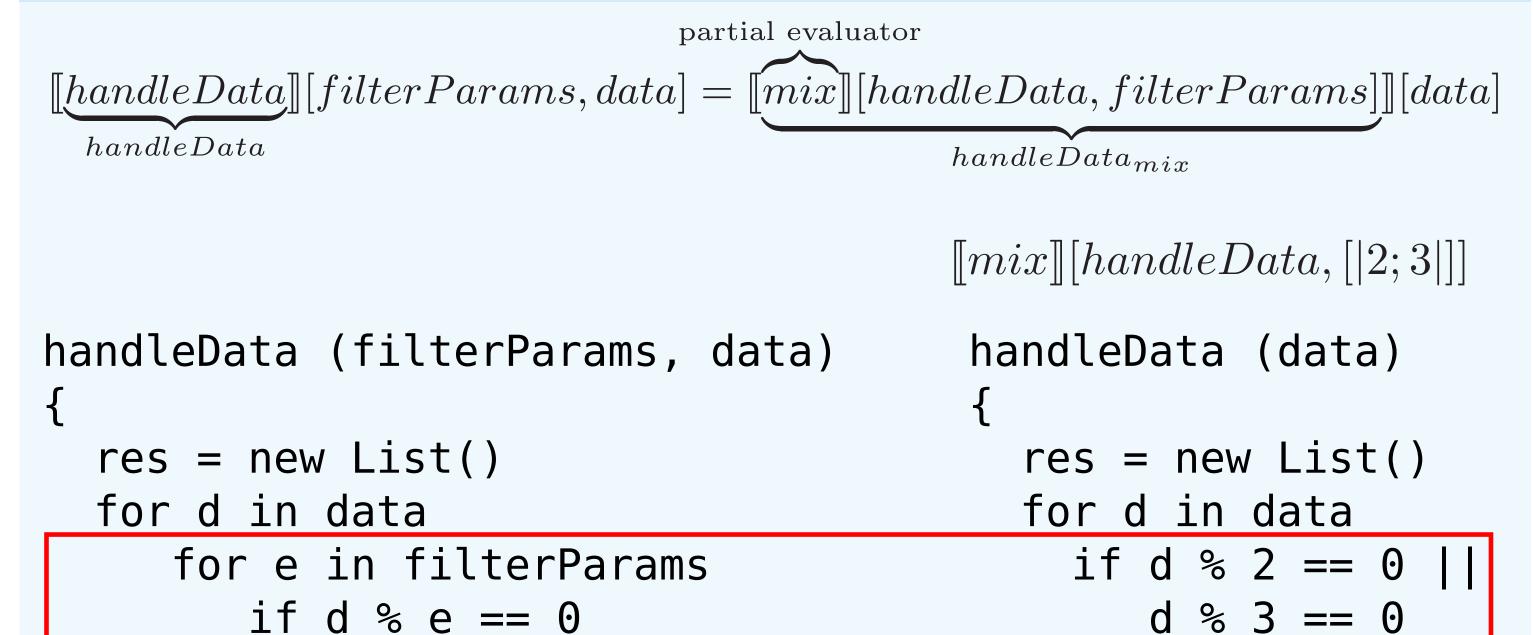
Filter parameters are fixed during one data processing session which runs procedure multiple times.



Filter params are read only and the same for all threads, so they are copied into shared memory to reduce memory traffic. In some cases this data can be placed in the constant memory.

Can we automatically create an optimal procedure for partially known parameters?

Partial Evaluation |1|



Implementation

We use AnyDSL [4] framework for Application: data curving 2600 partially_evaluated ahead-of-time partial evaluation.

- Substring matching: partially evaluated naïve implementation and naïve implementation with different locations of patterns.
- 2D convolution: versions from CUDA SDK examples with manually unrolled loops for a number of filters' size and without such optimization, partially evaluated version which equals to SDK example without unrolling.

Hardware

- GTX-1070: Pascal architecture, 8GB GDDR5, 1920 CUDA cores.
- Tesla T4: Turing architecture, 16GB GDDR6, 2560 CUDA cores.

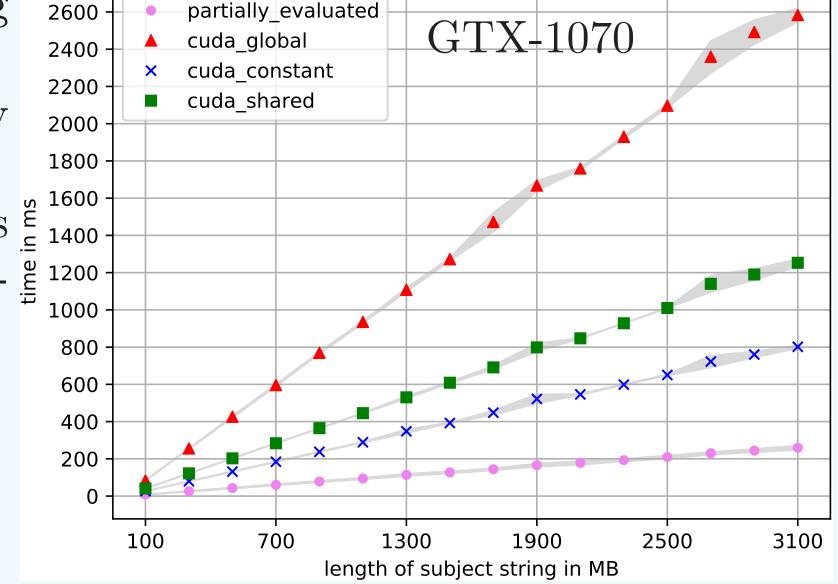
Evaluation: Substring Matching

then res.Add(d)

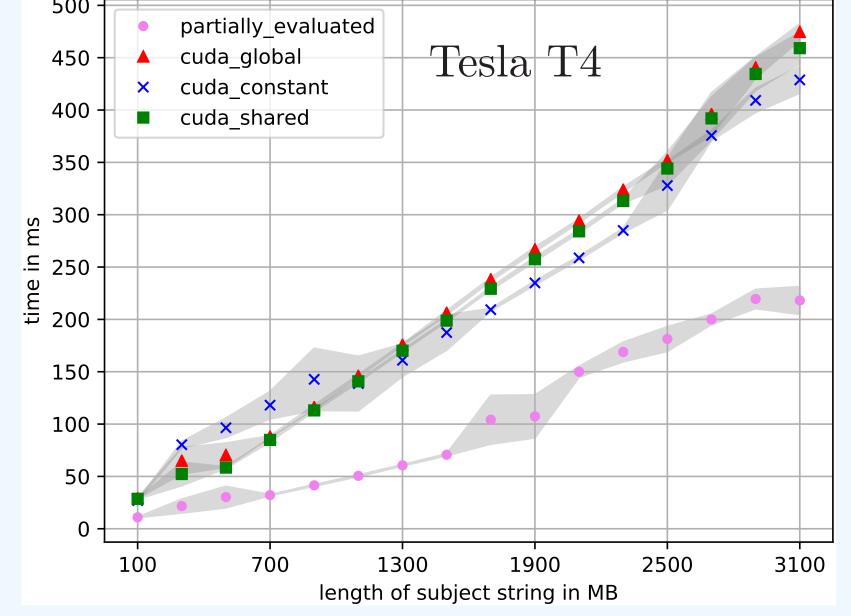
in cyber forensics.

Subject string: byte array from real hard drive.

Patterns: 16 file signatures \(\frac{2}{5} \) 1400 from GCK's file signatures ta- \(\frac{\mathbb{E}}{2}\) 1200 ble [3].



return res



then res.Add(d)

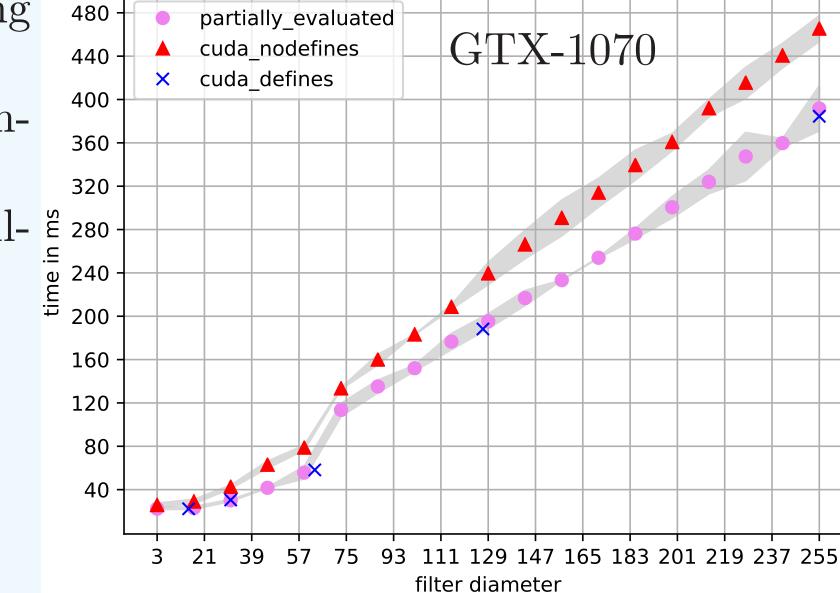
return res

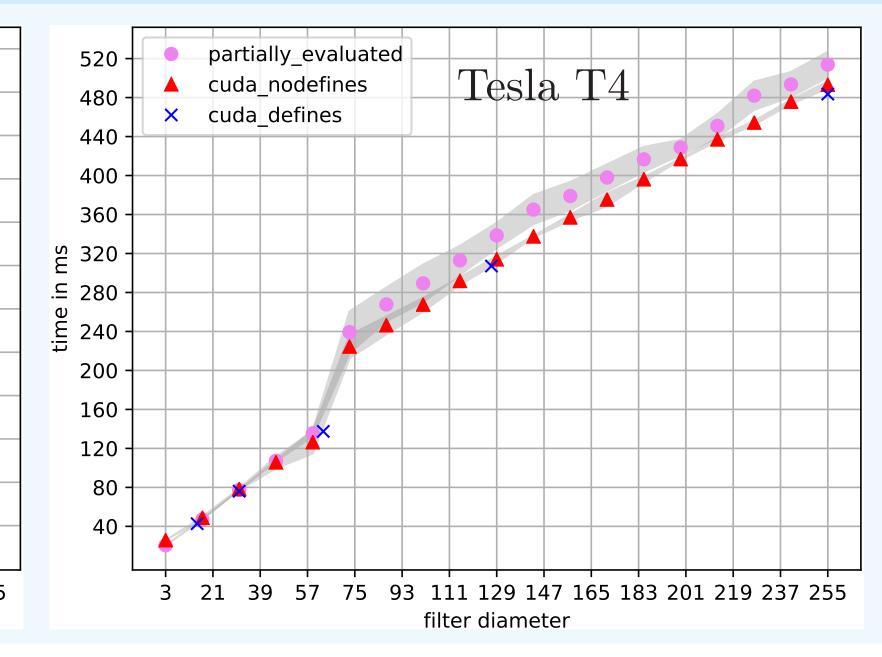
Evaluation: 2D Convolution

Application: data curving in cyber forensics.

Subject image: random image of size 1GB.

Filters: random square fil- \ 280 ters with diameter 3 to 255.





Contact Us

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References

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- [4] Roland Leissa, Klaas Boesche, Sebastian Hack, Arsène Pérard-Gayot, Richard Membarth, Philipp Slusallek, André Müller, and Bertil Schmidt. Anydsl: A partial evaluation framework for programming high-performance libraries. Proc. ACM Program. Lang., 2(OOPSLA):119:1-119:30, October 2018.

Acknowledgments

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