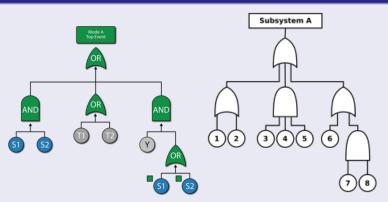
Разбиение графа на подграфы Выделение модулей в деревьях отказа Бинарные диаграммы решений

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СП6ГУ, 2018

Разбиение на модули

Дерево отказов

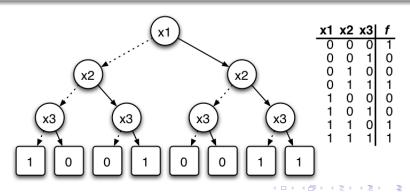


Назовем внутреннюю вершину модулем, если никакие события не встречаются в другом модуле. (Например, на левом рисунке есть 2 модуля — Тор Event и OR (Т1, Т2), а на правом 5 — каждая внутренняя вершина является модулем)

Binary Decision Diagram (BDD)

Бинарная диаграмма решений (БДР)

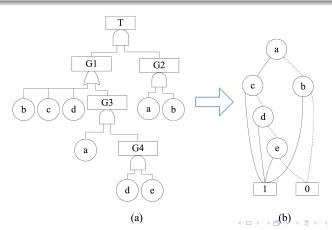
— форма представления булевой функции $f(x_1,x_2,...,x_n)$ от n переменных в виде направленного ациклического графа, состоящего из внутренних узлов решений (помеченных x_i), каждый из которых имеет по два потомка, и двух терминальных узлов (помеченных 0 и 1), каждый из которых соответствует одному из двух значений булевой функции.



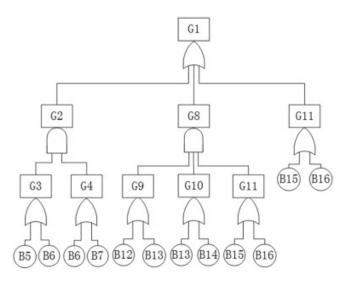
Преобразование FT в BDD

Задача

Хотим найти все комбинации элементов, которые приведут к отказу всей системы — минимальные сечения (MCS) или решения, применив преобразование FT в BDD.

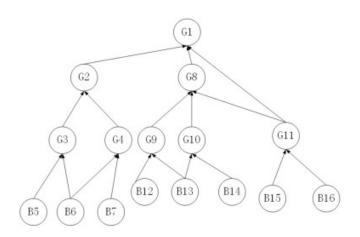


Дерево отказов(FT - fault tree)



Z.F. Li, Y. Ren, L.L. Liu, Z.L. Wang, Parallel algorithm for finding modules of large-scale coherent fault trees

Направленный ациклический граф (DAG – Directed Acyclic Graph)



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Линейный алгоритм (LTA – Linear time algorithm)

The algorithm has 3 steps:

- 1. Initialize the counters; traverse the list of nodes.
- 2. Perform the first 'depth-first left-most' traversal of the graph to set counters; for leaves, dates #1 and #2 are identical.
- 3. Perform the second 'depth-first left-most' traversal of the graph in which it collects, for each internal event *v*, the minimum of the first dates and the maximum of the last dates of its children. *v* is a module if only and if:
 - a) the collected minimum is greater than the first date of *v*.
 - b) the collected maximum is less than the last date of v.

Z.F. Li, Y. Ren, L.L. Liu, Z.L. Wang, Parallel algorithm for finding modules of large-scale coherent fault trees

Параллельный алгоритм

 V_{out} the output edge number of node in the directed acyclic graph V_M it means whether the node is module top or not, if and only if $V_M \le 1$, the node is module top.

We set the V_{out} when we construct the directed acyclic graph from fault tree. So we can conclude the following parallel algorithm basing on the above description.

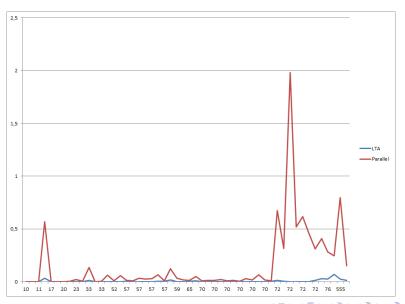
The algorithm consists of 4 steps:

- 1. Initial variable. For all leave node, $V_M = V_{out}$.
- 2. Perform the 'depth-first left-most' traversal from the leave nodes, and judge the internal node e whether it is visited or not. If it is not visited, updating its V_M value according to the formula $e_M = \max(s_M, s_{out})$. s is an input of node e. Or determine whether the path $s\overline{e}$ is passed or not. If the path is passed, updating its V_M value according to the formula $e_M = \min(s_M, e_M 1)$. Or updating the V_M according to formula $e_M = \min(s_M, e_M) 1$.
- 3. Perform the procedure described in step 2 on every subgraph.
- 4. Union of the internal nodes of all subgraphs marked through above procedures, so we get all module tops. The rule to union of the nodes is described as follows. If any $V_M \le 1$ for the same node in all subgraphs, we predict that the node is a module top. If the node does not follow the rule, it is not a module top.

Z.F. Li, Y. Ren, L.L. Liu, Z.L. Wang, Parallel algorithm for finding modules of large-scale coherent fault trees

8 / 23

Результаты по разбиению на модули



Возможные варианты присоединения модулей

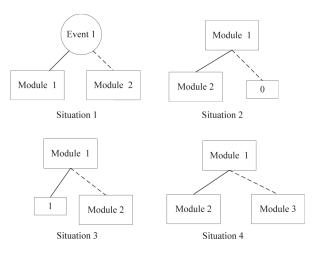


Fig. 7. Four situations in BDD structure with modules.

Fig. 8. Key to deal with situation 1.

```
Module 2 Traversing (T, H) /* T=ite(x, F, G, p_x), where x<10000*/
                        /* H=ite(h, F_h, G_h, p_h)*/
            MCS \leftarrow add(x), p = p_x * p
            if(F=1)
                 Traversing(H)
                 if(G=0)
                 else
                        MCS \leftarrow add(-x), p = (1 - p_x) * p
                        Module2_Traversing(G, H)
            else
                  Modul2 Traversing(F, H)
                  if(G=0)
                 else
                        MCS \leftarrow add(-x), p = (1 - p_r) * p
                        Module2 Traversing(G, H)
```

Fig. 9. Recursive function to deal with situation 2.

```
Module3_Traversing (T, H) /* T=ite(x, F, G, p_x), where x<10000*/
                        /* H=ite(h, F_h, G_h, p_h)*/
             MCS \leftarrow add(x), p = p_x * p
            if(F=1)
                  MCSs \leftarrow MCS, p_{mcs} \leftarrow p, p_{top} = p + p_{top}
                  MCS=new, p = 1
                  MCS \leftarrow add(-x), p = (1 - p_x) * p
                  if(G=0)
                        Traversing(H)
                  else
                        Module3 Traversing(G, H)
            else
                  Modul3 Traversing(F, H)
                  MCS \leftarrow add(-x), p = (1 - p_x) * p
                  if(G=0)
                        Traversing(H)
                  else
                        Module3 Traversing(G, H)
```

Fig. 10. Recursive function to deal with situation 3.

```
Module4 Traversing (T, H, K) /* T=ite(x, F, G, p_x), where x<10000*/
                               /* H=ite(h, F_h, G_h, p_h)*/
                               /* K=ite(h, F_{k}, G_{k}, p_{k})*/
            MCS \leftarrow add(x), p = p_x * p
            if(F=1)
                 Traversing(H)
                  MCS \leftarrow add(-x), p = (1 - p_x) * p
                  if(G=0)
                        Traversing(K)
                  else
                        Module3 Traversing(G, K)
            else
                  Module2 Traversing(F, H)
                  MCS \leftarrow add(-x), p = (1 - p_x) * p
                  if(G=0)
                        Module3 Traversing(F, K)
                  else
                        Module4 Traversing(F, G, H)
```

Fig. 11. Recursive function to deal with situation 4.

Результаты

Table 4 FT test results of BDD and BDD based on modularization.

FT	Size	BDD based on modularization			BDD	
		Modules	Solutions	Time(s)	Solutions	Time(s)
Das9201	160	32	14217	0.97	14217	>300
Das9204	83	13	16704	0.10	16704	1.05
Chinese	61	1	392	0.03	392	0.03
Isp9604	338	76	746574	3.82	_	_
Isp9606	130	17	1776	0.27	1776	106
Isp9607	138	14	150436	0.94	150436	>300
edf9201	314	23	579720	10.6	_	_

Size — количество элементов в дереве, Solutions — количество минимальных сечений

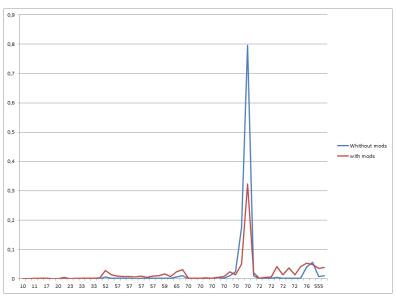
Yunli Deng, He Wang, Biao Guo, BDD algorithms based on modularization for fault tree analysis

15 / 23

Мой алгоритм обхода

```
Traverse(T, modNum) where T = ite(x, H, L)
             \bullet if(T.lsZero) zeroImp[modNum] \leftarrow prImp
             oldsymbol{a} else if(T.lsOne) onelmp[modNum] \leftarrow prlmp
             else if(T.lsModul)
             0 {
             Traverse(BDDs[x], x)
             • foreach z in zeroImp[x] : prImp \leftarrow z; Traverse(L, mod Num)
             of foreach o in one Imp[x]: Imp \leftarrow o; I
             \bigcirc prlmp\leftarrowx; Traverse(H, modNum)
             Traverse(L, modNum)
             ① }
```

Результаты по поиску минимальных сечений



Список литературы

- Z.F. Li, Y. Ren, L.L. Liu, Z.L. Wang, Parallel algorithm for finding modules of large-scale coherent fault trees
- Y. Dutuit, A. Rauzy, A linear-time algorithm to find modules in fault trees
- Yunli Deng, He Wang, Biao Guo, BDD algorithms based on modularization for fault tree analysis
- http://www.cs.cornell.edu/bindel/class/cs5220-f11/slides/lec19.pdf