One Algorithm to Evaluate Them All: Unified Linear Algebra Based Approach to Evaluate Both Regular and Context-Free Path Queries

Ekaterina Shemetova
katyacyfra@gmail.com
Saint Petersburg Academic
University
St. Petersburg, Russia

Rustam Azimov
rustam.azimov19021995@gmail.com
Saint Petersburg State University
St. Petersburg, Russia

Egor Orachyov
egororachyov@gmail.com
Saint Petersburg State University
St. Petersburg, Russia

Ilya Epelbaum
iliyepelbaun@gmail.com
Saint Petersburg State University
St. Petersburg, Russia
JetBrains Research
St. Petersburg, Russia

Semyon Grigorev
s.v.grigoriev@spbu.ru
semyon.grigorev@jetbrains.com
Saint Petersburg State University
St. Petersburg, Russia
JetBrains Research
St. Petersburg, Russia

ABSTRACT
We propose a new algo for CFPQ! Abstract is very abstract.

CCS CONCEPTS
• **Information systems** → **Graph-based database models**; **Query languages for non-relational engines**; • **Theory of computation** → **Grammars and context-free languages**; **Regular languages**; • **Mathematics of computing** → **Paths and connectivity problems**; *Graph algorithms.*

1 INTRODUCTION
Language-constrained path querying [5] is one of techniques for graph navigation querying. This technique allows one to use formal languages as constraints on paths in edge-labeled graphs: path satisfies constraints if labels along it form a word from the specified language.

The utilization of regular languages as constraints, or *Regular Path Querying* (RPQ), is most well-studied and widely spread. Different aspects of RPQs are actively studied in graph databases [2, 4, 30], and support of regular constraints is implemented in such popular query languages as PGQL [42], or SPARQL[1] [27] (property paths). Even that, improvement of RPQ algorithms efficiency on huge graphs is an actual

---
[1]Specification of regular constraints in SPARQL propert paths: https://www.w3.org/TR/sparql11-property-paths/. Access date: 07.07.2020.

problem nowadays, and new algorithms and solutions are being created [34, 44].

At the same time, utilization of more powerful languages, namely context-free languages, gain popularity in the last few years. *Context-Free Path Querying* problem (CFPQ) was introduced by Mihalis Yannakakis in 1990 in [45]. A number of different algoritms was proposed since that time, but recently, in [28] Jochem Kuijpers et al. show that state-of-the-art CFPQ algorithms are not performant enough to be used in practice. This fact motivates to find new algorithms for CFPQ.

One of the promising ways to achieve high-performance solutions for graph analysis problems is to reduce graph algorithms to linear algebra. This way, the description of basic linear algebra primitives, the GraphBLAS [25] API, was proposed. Solutions that use libraries that implement this API, such as SuiteSparse [15] and CombBLAS [9], show that reduction to linear algebra is a way to utilize high-performance parallel and distributed computations for graph analysis.

Rustam Azimov in [3] shows how to reduce CFPQ to matrix multiplication. Late, in [33] and [40], it was shown that utilization of appropriate libraries of linear algebra for Azimov's algorithm implementation allows one to get practical solution for CFPQ. But Azimov's algorithm requires transforming the input grammar to Chomsky Normal Form, which leads to the grammar size increase, thus worsen performance especially for regular queries and complex context-free queries.

To solve these problems, recently, an algorithm based on automata intersection was proposed [?]. This algorithm is based on linear algebra and does not require the input grammar transformation. In this work we improve it. First of all, we reduce it to operations over Boolean matrices, thus simplify its description and implementation. Also, we show that this algorithm is performant enough for regular queries, so it is a good candidate for integration with real-world query languages: we can use one algorithm to evaluate both regular and context-free queries.

Moreover, we show that this algorithm is a way to attack a long-standing problem of subcubic CFPQ. The best-known result for the general case is an $O(n^3/\log n)$ algorithm of Swarat Chaudhuri [12]. Also, there are solutions for partial cases. For example, there is a truly subcubic algorithm for 1-Dyck language proposed by Phillip Bradford [7]. But this solution cannot be generalized to arbitrary CFPQs. So, in our knowledge, there is no truly subcubic general algorithm for CFPQs. In this work we show that incremental transitive closure is a bottleneck on the way to get subcubic time complexity for CFPQ.

To summarize, we make the following contributions in this paper.

(1) We rethink and improve tensor-product-based algorithm for CFPQ. First of all, we reduce this algorithm to operations over Boolean matrices. All paths semantics. Previous matrix-based solution only single path. For both regualr and context-free path queries. Correctness and time complexity.

(2) We demonstrate interconnection between CFPQ and incremental transitive closure. Conjecture on sublinear incremental transitive closure and subcubic CFPQ. We show that incremental transitive closure is a bottleneck on the way to get subcubic CFPQ algorithm.

(3) By using existing results we show how to get slightly subcubic algorithm for general case, and subcubic combinatorial algorithm for partial cases. This cretarion is output-sensitive, so it is not practical, but open a theoretical way to find more subclass with subcubic complexity.

(4) We implement the described algorithm and evaluate it on real-world data. RPQ, CFPQ. Results show that !!!

2 PRELIMINARIES

In this section we introduce basic notation and definitions from graph theory and formal language theory which are used in our work.

2.1 Language-Constrained Path Querying Problem

We use a directed edge-labeled graph as a data model. To introduce the *Language-Constraint Path Querying Problem* [?] over directed edge-labeled graphs we should give both language and grammar definitions.

Definition 2.1. The edge-labeled directed graph $\mathcal{G} = \langle V, E, L \rangle$, where:

- V is a finite set of vertices
- $E \in V \times L \times V$ is a finite set of edges
- L is a finite set of edge labels

Since V has finite size, one can always introduce bijection between V and $Q = \{0, \ldots, |V| - 1\}$, thus in our work we guess that $V = \{0, \ldots, |V| - 1\}$.

The example of a graph which we use in further examples is presented in Figure 1.

Definition 2.2. Adjacency matrix for a edge-labeled directed graph $\mathcal{G} = \langle V, E, L \rangle$ is a matrix M, that:

- M has size $|V| \times |V|$
- $M[i, j] = \{l \mid \mid e = (i, l, j) \in E\}$

Adjacency matrix M_2 of the graph $\mathcal{G}$ is

Figure 1: The example of input graph $\mathcal{G}$

$$M_2 = \begin{pmatrix} \cdot & \{a\} & \cdot & \cdot \\ \cdot & \cdot & \{a\} & \cdot \\ \{a\} & \cdot & \cdot & \{b\} \\ \cdot & \cdot & \{b\} & \cdot \end{pmatrix}.$$

Definition 2.3. Boolean matrices decomposition, or Boolean adjacency matrix, for a edge-labeled directed graph $\mathcal{G} = \langle V, E, L \rangle$ with adjacency matrix M is a set of matrices $\mathcal{M} = \{M^l \mid l \in L, M^l[i,j] = 1 \iff l \in M[i,j]\}$.

In our work we use decomposition of the adjacency matrix to a set of Boolean matrices. As an example, matrix M_2 can be represented as a set of two Boolean matrices M_2^a and M_2^b as presented in Figure 2.

$$M_2^a = \begin{pmatrix} \cdot & 1 & \cdot & \cdot \\ \cdot & \cdot & 1 & \cdot \\ 1 & \cdot & \cdot & \cdot \\ \cdot & \cdot & \cdot & \cdot \end{pmatrix}, M_2^b = \begin{pmatrix} \cdot & \cdot & \cdot & \cdot \\ \cdot & \cdot & \cdot & \cdot \\ \cdot & \cdot & \cdot & 1 \\ \cdot & \cdot & 1 & \cdot \end{pmatrix} \quad (1)$$

Figure 2: The representation of the matrix M_2 **as a set of Boolean matrices**

In this way we reduce operations which are necessary for our algorithm from operations over custom semiring (over edge labels) to operations over a Boolean semiring.

In this work we also use the following notation $\mathcal{M}(\mathcal{G})$ and $\mathcal{G}(\mathcal{M})$ to describe the Boolean decomposition matrices for some graph and the graph formed by its adjacency Boolean matrices correspondingly.

Also, we should define the path in the graph and the word formed by the path.

Definition 2.4. Path π in the graph $\mathcal{G} = \langle V, E, L \rangle$ is a sequence $e_0, e_1, \ldots, e_{n-1}$, where $e_i = (v_i, l_i, u_i) \in E$ and for any e_i, e_{i+1} $u_i = v_{i+1}$. We denote path from v to u as $v \pi u$.

Definition 2.5. The word formed by a path

$$\pi = (v_0, l_0, v_1), (v_1, l_1, v_2), \ldots, (v_{n-1}, l_{n-1}, v_n)$$

is a concatenation of labels along the path: $\omega(\pi) = l_0 l_1 \ldots l_{n-1}$.

The next part is a definitions from the formal language theory.

Definition 2.6. A language $\mathcal{L}$ over a finite alphabet Σ is a subset of all possible sequences formed by symbols from the alphabet: $\mathcal{L}_\Sigma = \{\omega \mid \omega \in \Sigma^*\}$.

Now we are ready to introduce CFPQ problem for the given graph $\mathcal{G} = \langle V, E, L \rangle$ and the given language $\mathcal{L}$ with reachability and all paths semantics.

Definition 2.7. To evaluate context-free path query with reachability semantics is to construct a set of pairs of vertices (v_i, v_j) such that there exists a path $v_i \pi v_j$ in $\mathcal{G}$ which forms the word from the given language:

$$R = \{(v_i, v_j) \mid \exists \pi : v_i \pi v_j, \omega(\pi) \in \mathcal{L}\}$$

Definition 2.8. To evaluate context-free path query with all paths semantics is to construct a set of paths π in $\mathcal{G}$ which form the word from the given language:

$$\Pi = \{\pi \mid \omega(\pi) \in \mathcal{L}\}$$

Note that Π can be infinite, thus in practice, we should provide a way of enumerating such paths with reasonable complexity, instead of explicit construction of the Π.

2.2 Regular Path Queries and Finite State Machine

The first case of language-constrained path querying is *Regular Path Querying* (RPQ): the language L is a regular language. This case is widely spread in practice [**?**].

Usual way to specify regular languages is *regular expressions*. We use the following definition of regular expressions.

Definition 2.9. Regular expression (and regular language) over alphabet Σ can be inductively defined as follows.

- $\varnothing$ (empty language) is regular expression
- ε (empty string) is regular expression
- $a_i \in \Sigma$ is regular expression
- if R_1 and R_2 are regular expressions, then $R_1 \mid R_2$ (alternation), $R_1 \cdot R_2$ (concatenation), R_1^* (Kleene star) are also regular expressions.

For example, one can specify regular expression $R_1 = ab^*$ to find paths in the graph $\mathcal{G}$ (fig. 1). Expected result is set of paths which start with a-labeled edge and contain zero or more b-labeled edges after that.

In this work we use the notion of *Finite-State Machine* (FSM) or *Finite-State Automaton* (FSA) for RPQs.

Definition 2.10. Deterministic Finite-State Machine T is a tuple $\langle \Sigma, Q, Q_s, Q_f, \delta \rangle$ where

- Σ is an input alphabet,
- Q is a finite set of states,
- $Q_s \subseteq Q$ is a set of start (or initial) states,

- $Q_f \subseteq Q$ is a set of final states,
- $\delta : Q \times \Sigma \to Q$ is a transition function.

It is well known, that every regular expression can be converted to deterministic FSM without ε-transitions. To do it one can use [22]. In our work we use FSM as a representation of RPQ. FSM can be naturally represented by a directed edge-labeled graph: $V = Q, L = \Sigma, E = \{(q_i, l, q_j) \mid \delta(q_i, l) = q_j\}$, where some vertices have special markers to specify start and final states. Example of graph-style representation of FSM T_1 for the regular expression R_1 is presented in Figure 3.
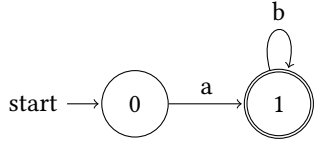
Figure 3: The example of graph representation of FSM for the regular expression ab^*

As a result, FSM also can be represented as a set of Boolean adjacency matrices $\mathcal{M}$ with additional information about start and final vertices. Such representation of T_1 is presented in Figure 4.

$$M^a = \begin{pmatrix} 0 & 1 \\ 0 & 0 \end{pmatrix}, \; M^b = \begin{pmatrix} 0 & 0 \\ 0 & 1 \end{pmatrix}.$$

Figure 4: The representation of the FSM T_1 as a set of Boolean matrices

Note, that the edge-labeled graph is an FSM: edges are transitions, all vertices should be both start and final at the same time. Thus RPQ evaluation is an intersection of two FSMs, and the result also can be represented as FSM, because regular languages are closed under intersection.

2.3 Context-Free Path Querying and Recursive State Machines

An even more general case, than RPQ, is a *Context-Free Path Querying Problem (CFPQ)*, where one can use context-free languages as constraints. These constraints are more expressive than the regular ones, for example, one can express classical same-generation query using context-free language, but not a regular one.

Definition 2.11. Context-free grammar $G = \langle \Sigma, N, S, P \rangle$ where Σ is a finite set of terminals (or terminal alphabet), N is a finite set of nonterminals (or nonterminal alphabet), $S \in N$ is a start nonterminal, and P is a finite set of productions (grammar rules) of form $N_i \to \alpha$ where $N_i \in N, \alpha \in (\Sigma \cup N)^*$.

Definition 2.12. The sequence $\omega_2 \in (\Sigma \cup N)^*$ is derivable from $\omega_1 \in (\Sigma \cup N)^*$ in one derivation step, or $\omega_1 \to \omega_2$, in the grammar $G = \langle \Sigma, N, S, P \rangle$ iff $\omega_1 = \alpha N_i \beta, \omega_2 = \alpha \gamma \beta$, and $N_i \to \gamma \in P$.

Definition 2.13. Context-free grammar $G = \langle \Sigma, N, S, P \rangle$ specifies a *context-free language*: $\mathcal{L}(G) = \{\omega \mid S \xrightarrow{*} \omega\}$, where $(\xrightarrow{*})$ denotes zero or more derivation steps $(\to)$.

Thus, one can use the grammar $G_1 = \langle \{a, b\}, \{S\}, S, \{S \to a \; b; \; S \to a \; S \; b\} \rangle$ to find paths which form words in the language $\mathcal{L}(G_1) = \{a^n b^n \mid n > 0\}$ in the graph $\mathcal{G}$ (fig. 1).

Regular expressions can be transformed to a FSM, and a context free grammar can be transformed to *Recursive State Machine* (RSM) (also known as recursive networks [?], recursive automata [?], !!!.) in the similar way. In our work we use the following definition of RSM.

Definition 2.14. A recursive state machine R over a finite alphabet Σ is defined as a tuple of elements $(M, m, \{C_i\}_{i \in M})$, where:

- M is a finite set of labels of boxes.
- $m \in M$ is an initial box label.
- Set of *component state machines* or *boxes*, where $C_i = (\Sigma \cup M, Q_i, q_i^0, F_i, \delta_i)$:
  - $\Sigma \cup M$ is a set of symbols, $\Sigma \cap M = \emptyset$
  - Q_i is a finite set of states, where $Q_i \cap Q_j = \emptyset, \forall i \neq j$
  - q_i^0 is an initial state for C_i
  - F_i is a set of final states for C_i, where $F_i \subseteq Q_i$
  - $\delta_i : Q_i \times (\Sigma \cup M) \to Q_i$ is a transition function

RSM behaves as a set of finite state machines (or FSM). Each FSM is called a *box* or a *component state machine* [1]. A box works almost the same way as a classical FSM, but it also handles additional *recursive calls* and employs an implicit *call stack* to *call* one component from another and then return execution flow back.

The execution of an RSM could be defined as a sequence of the configuration transitions, which are done on input symbols reading. The pair (q_i, S), where q_i is current state for box C_i and S is stack of *return states*, describes execution configurations.

The RSM execution starts form configuration $(q_m^0, \langle \rangle)$. The following list of rules defines the machine transition from configuration (q_i, S) to (q', S') on some input symbol a from input sequence, which is read as usual for FSA:

- $(q_i^k, S) \rightsquigarrow (\delta_i(q_i^k, a), S)$
- $(q_i^k, S) \rightsquigarrow (q_j^0, \delta_i(q_i^k, j) \circ S)$
- $(q_j^k, q_i^t \circ S) \rightsquigarrow (q_i^t, S)$, where $q_j^k \in F_j$

Some input sequence of the symbols $a_1...a_n$, which forms some input word, is accepted, if machine reaches configuration $(q, \langle \rangle)$, where $q \in F_m$. It is also worth noting that the RSM makes nondeterministic transitions, without reading

the input character when it *calls* some component or makes a *return*.

According to [1], recursive state machines are equivalent to pushdown systems. Since pushdown systems are capable of accepting context-free languages [22], it is clear that RSMs are equivalent to context-free languages. Thus RSMs suit to encode query grammars. Any CFG can be easily converted to an RSM with one box per nonterminal. The box which corresponds to a nonterminal A is constructed using the right-hand side of each rule for A.

An example of such RSM R constructed for the grammar G with rules $S \rightarrow aSb \mid ab$ is provided in Figure 5. For a given example of the grammar and the RSM consider the following sequence of the machine configuration transitions, in case, where one want to determine, if input word $aabb$ belongs to the language $L(G)$. The RSM execution starts from configuration $(q_S^0, \langle\rangle)$, reads symbols a and goes to $(q_S^1, \langle\rangle)$. Then, in the nondeterministic manner it tries to read b but fails, and in the same time tries to derive S and goes to configuration $(q_S^0, \langle q_S^2 \rangle)$, where q_S^2 is *return* state. Then machine reads a and goes to $(q_S^1, \langle q_S^2 \rangle)$. In this case, in the nondeterministic choice it fails to derive S, but successfully reads b and goes to configuration $(q_S^3, \langle q_S^2 \rangle)$. Since q_S^3 is final state for the box S, the RSM tries to make *return* and goes to $(q_S^2, \langle\rangle)$. Then it reads b and transits to $(q_S^3, \langle\rangle)$. Since $q_S^3 \in F_S$ and the *return* stack is empty, the machine accepts the input sequence $aabb$.

Since R is a set of FSMs, it is useful to represent R as an adjacency matrix for the graph where vertices are states from $\bigcup_{i \in M} Q_i$ and edges are transitions between q_i^a and q_i^b with label $l \in \Sigma \cup M$, if $\delta_i(q_i^a, l) = q_i^b$. An example of such adjacency matrix M_R for the machine R is provided in section **??**.

Box S

Figure 5: The recursive state machine R for grammar G

Similarly to a FSM, an RSM can be representaed as a graph and, hence, as a set of Boolean adjacency matrices. For our example, M_1 is:

$$M_1 = \begin{pmatrix} . & . & \{a\} & . \\ . & . & \{S\} & \{b\} \\ . & . & . & \{b\} \\ . & . & . & . \end{pmatrix}$$

Matrix M_1 can be represented as a set of Boolean matrices as follows:

$$M_1^S = \begin{pmatrix} . & . & . & . \\ . & . & 1 & . \\ . & . & . & . \\ . & . & . & . \end{pmatrix}, \ M_1^a = \begin{pmatrix} . & . & 1 & . \\ . & . & . & . \\ . & . & . & . \\ . & . & . & . \end{pmatrix}, \ M_1^b = \begin{pmatrix} . & . & . & . \\ . & . & . & 1 \\ . & . & . & 1 \\ . & . & . & . \end{pmatrix}$$

Similarly to an RPQ, a CFPQ is the intersection of the given context-free language and a FSM specified by the given graph. As far as every context-free language is closed under intersection with regular languages, such intersection can be represented as an RSM. Also, one can look at the RSM as a FSM over $\Sigma \cup N$. In this work we use this point of view to propose unified algorithm for evaluation both regular and context-free path queries with zero overhead for regular ones.

2.4 Graph Kronecker Product and Machines Intersection

First of all, we introduce classical Kronecker product definition, describe graph Kronecker product and its relation to Boolean matrices algebra, RSM and FSM intersection.

Definition 2.15. Given two matrices A and B of sizes $m_1 \times n_1$ and $m_2 \times n_2$ respectively, with element-wise product operation $\cdot$. The Kronecker product of these two matrices is a new matrix $C = A \otimes B$, where:

- C has size $m_1 * m_2 \times n_1 * n_2$
- $C[u * m_1 + v, n_1 * p + q] = A[u, p] \cdot B[v, q]$

It is worth mention, that the Kronecker product produces blocked matrix C, with total number of the blocks $m_1 * n_1$, where each block has size $m_2 * n_2$ and is defined as $A[i, j] \cdot B$ (scalar to matrix).

Definition 2.16. Given two edge-labeled directed graphs $\mathcal{G}_1 = \langle V_1, E_1, L_1 \rangle$ and $\mathcal{G}_2 = \langle V_2, E_2, L_2 \rangle$ the Kronecker product of these two graphs is a edge-labeled directed graph $\mathcal{G} = \mathcal{G}_1 \otimes \mathcal{G}_2$, where $\mathcal{G} = \langle V, E, L \rangle$:

- $V = V_1 \times V_2$
- $E = \{((u, v), l, (p, q)) \mid (u, l, p) \in E_1 \wedge (v, l, q) \in E_2\}$
- $L = L_1 \cap L_2$

The Kronecker product for graphs produces a new graph with a property, that if some path $(u, v)\pi(p, q)$ exists in the result graph then paths $u\pi_1 p$ and $v\pi_2 q$ exist in the input graphs, and $\omega(\pi) = \omega(\pi_1) = \omega(\pi_2)$. These paths π_1 and π_2 could be easily found from π by its definition.

The Kronecker product for directed graphs can be easily described as the Kronecker product of the corresponding adjacency matrices of graphs, what gives us the following definition:

Definition 2.17. Given two adjacency matrices M_1 and M_2 of sizes $m_1 \times n_1$ and $m_2 \times n_2$ respectively, for some directed

graphs $\mathcal{G}_1$ and $\mathcal{G}_2$. The Kronecker product of these two adjacency matrices is the adjacency matrix M of a some graph $\mathcal{G}$, where:

- M has size $m_1 * m_2 \times n_1 * n_2$
- $M[u * m_1 + v, n_1 * p + q] = M_1[u, p] \cap M_2[v, q]$

By the definition, the Kronecker product for adjacency matrices gives an adjacency matrix with the same set of edges as in the resulting graph in the Def. 2.16. Thus, $M(\mathcal{G}) = M(\mathcal{G}_1) \otimes M(\mathcal{G}_2)$, where $\mathcal{G} = \mathcal{G}_1 \otimes \mathcal{G}_2$.

Definition 2.18. Given two FSMs $T_1 = \langle \Sigma, Q^1, Q_S^1, S_F^1, \delta^1 \rangle$ and $T_2 = \langle \Sigma, Q^2, Q_S^2, S_F^2, \delta^2 \rangle$. The intersection of this two machines is a new FSM $T = \langle \Sigma, Q, Q_S, S_F, \delta \rangle$, where:

- $Q = Q^1 \times Q^2$
- $Q_S = Q_S^1 \times Q_S^2$
- $Q_F = Q_F^1 \times Q_F^2$
- $\delta : Q \times \Sigma \rightarrow Q$, $\delta(\langle q_1, q_2 \rangle, s) = \langle q_1', q_2' \rangle$,
  if $\delta(q_1, s) = q_1'$ and $\delta(q_2, s) = q_2'$

According to [22], the above definition of the FSM intersection allows to construct the new machine with the following property: $L(T) = L(T_1) \cap L(T_2)$.

The most computationally expensive part of such procedure is the δ function construction for the new machine T. Using adjacency matrices decomposition for FSMs we can reduce the intersection to the Kronecker product of such matrices over Boolean semiring at some extent, since the transition function δ of the machine T in matrix form is exactly the same as the product result. More precisely:

Definition 2.19. Given two adjacency matrices $\mathcal{M}_1$ and $\mathcal{M}_2$ over Boolean semiring. The Kronecker product of these matrices is a new matrix $\mathcal{M} = \mathcal{M}_1 \otimes \mathcal{M}_2$, defined as follows:

- $\mathcal{M} = \{M_1^a \otimes M_2^a \mid a \in \Sigma\}$
- The element-wise operation is *and* over Boolean values

Applying the Kronecker product theory for both the FSM and the edge-labeled directed graph, we can intersect this objects as shown in Def. 2.19, since the graph could be interpreted as an FSM with transitions matrix represented as the Boolean adjacency matrix.

In this work we show how to express RSM and FSM intersection in terms of Kronecker product and transitive closure over Boolean semiring.

3 CONTEXT-FREE PATH QUERYING BY KRONECKER PRODUCT

In this section, we introduce the algorithm for CFPQ which is based on Kronecker product of Boolean matrices. The algorithm provides the ability to solve all-pairs CFPQ in all-path semantics (according to Hellings [21]) and consists of the two following parts.

(1) Index creation. In the first step, the algorithm computes an index which contains information which is necessary to restore paths for specified pairs of vertices. This index can be used to solve the reachability problem without paths extraction. Note that this index is finite even if the set of paths is infinite.

(2) Paths extraction. All paths for the given pair of vertices can be enumerated by using the index computed at the previous step. As far as the set of paths can be infinite, all paths cannot be enumerated explicitly, and advanced techniques such as lazy evaluation are required for implementation. Anyway, a single path can by always extracted by using standard techniques.

We describe both these steps, prove correctness, and provide time complexity estimations. For the first step we firstly introduce naïve algorithm. After that we show how to achieve cubic time complexity by using dynamic transitive closure algorithm and demonstrate that this technique allow us to get truly subcubic CFPQ algortihm for planar graphs.

After that we provide step-by-step example of query evaluation by using the proposed algorithm.

3.1 Index Creation Algorithm

The *index creation* algorithm outputs the final adjacency matrix $\mathcal{M}_2$ for the input graph with all vertices pairs, which are reachable through some nonterminal in the input grammar G, and the index matrix C_3, which allows to extract paths in the *path extraction* algorithm.

The algorithm is based on the generalization of the FSM intersection for an RSM, and the edge-labeled directed input graph. Since the RSM is composed as set of FSMs, it could be easily presented as adjacency matrix for some graph over labels set $\Sigma \cup S$. As shown in the Def. 2.19 we can apply Kronecker product from Boolean matrices to *intersect* the RSM and the input graph to some extent. But the RSM contains the nonterminal symbols from N with additional *recursive calls* logic, what requires *transitive closure* step for such symbols extraction.

Applying the Kronecker product and transitive closure theory together, we get the idea of the algorithm: iterative Kronecker product evaluation for the RSM and the input graph, followed by transitive closure, nonterminal extraction and the update of the graph adjacency matrix.

3.1.1 Boolean Matrices Based Version. Listing 1 shows main steps of the algorithm. The algorithm accepts context-free grammar $G = (\Sigma, N, P)$ and graph $\mathcal{G} = (V, E, L)$ as an input. An RSM R is created from the grammar G. Note, that R must have no ε-transitions. $\mathcal{M}_1$ and $\mathcal{M}_2$ are the Boolean adjacency matrices for the machine R and the graph $\mathcal{G}$ correspondingly.

Then for each vertex i of the graph $\mathcal{G}$, the algorithm adds loops with non-terminals, which allows deriving ε-word.

Here the following rule is implied: each vertex of the graph is reachable by itself through an ε-transition. Since the machine R does not have any ε-transitions, the ε-word could be derived only if a state s in the box B of the R is both initial and final. This data is queried by the *getNonterminals* function for each state s.

The algorithm terminates when the matrix $\mathcal{M}_2$ stops changing. Kronecker product of matrices $\mathcal{M}_1$ and $\mathcal{M}_2$ is evaluated for each iteration. The result is stored in $\mathcal{M}_3$ as a Boolean matrix. Since we are interested only in the reachability of some vertices, there is no need to store a separate Boolean matrix for each label from $\Sigma \cup N$. Therefore, we can collapse it into one Boolean matrix M_3', what is done in the next step. These Boolean matrix could be interpreted as an adjacency matrix for some directed graph without labels with the same set of the vertices, as in the graph formed by $\mathcal{M}_3$. From that point of view the matrix M_3' has the following property from its definition: if some vertices connected by some path in the graph $\mathcal{G}(M_3')$ then these vertices are connected by one or many paths in the graph $\mathcal{G}(\mathcal{M}_3)$.

For the given M_3' a C_3 transitive closure matrix is evaluated by the corresponding function call. Then the algorithm iterates over cells of the C_3. For the pair of indices (i, j), it computes s and f — the initial and final states in the recursive automata R which relate to the concrete $C_3[i, j]$ of the closure matrix. If the given s and f belong to the same box B of R, $s = q_B^0$, and $f \in F_B$, then *getNonterminals* returns the respective nonterminal. Then for each such nonterminal the respective matrix of the graph adjacency matrix $\mathcal{M}_2$ is updated and a new edge as a Boolean value in the appropriate cell is added.

The functions *getStates* and *getCoordinates* (see Listing 2) are used to map indices between Kronecker product arguments and the result matrix. The Implementation appeals to the blocked structure of the matrix C_3, where each block corresponds to some automata and graph edge.

The algorithm returns the computed path extraction index C_3 and the updated matrix $\mathcal{M}_2$, which contains the initial graph $\mathcal{G}$ data as well as data for nonterminals from N. If a cell $M_2^S[i, j]$ for any valid indices i and j and $S \in N$ contains $\{1\}$, then vertex j is reachable from vertex i in grammar G for nonterminal S.

3.1.2 Index creation for RPQ.
In case of the RPQ, the main **while** loop takes only one iteration to actually append data. Since the input query is provided in form of the regex, one can construct the corresponding RSM, which consists of the single *component state machine*. This CSM is built from the regex and labeled as the S for example, which has no *recursive calls*. The adjacency matrix of the machine is build over Σ only. Therefore, calculating the Kronecker product,

all relevant information is taken into account at the first iteration of the loop.

Listing 1 Kronecker product based CFPQ

```
1:  function CONTEXTFREEPATHQUERYING(G, 𝒢)
2:      R ← Recursive automata for G
3:      𝓜₁ ← Boolean adjacency matrix for R
4:      𝓜₂ ← Boolean adjacency matrix for 𝒢
5:      C₃ ← The empty matrix
6:      for s ∈ 0..dim(𝓜₁) − 1 do
7:          for S ∈ getNonterminals(R, s, s) do
8:              for i ∈ 0..dim(𝓜₂) − 1 do
9:                  M₂ˢ[i, i] ← {1}
10:     while Matrix 𝓜₂ is changing do
11:         𝓜₃ ← 𝓜₁ ⊗ 𝓜₂              ▷ Evaluate Kronecker product
12:         M₃′ ← ⋁_{M₃ᵃ∈𝓜₃} M₃ᵃ         ▷ Collapse to Boolean matrix
13:         C₃ ← transitiveClosure(M₃′)
14:         n ← dim(𝓜₃)                 ▷ Matrix 𝓜₃ size = n × n
15:         for (i, j) ∈ [0..n − 1] × [0..n − 1] do
16:             if C₃[i, j] then
17:                 s, f ← getStates(C₃, i, j)
18:                 x, y ← getCoordinates(C₃, i, j)
19:                 for S ∈ getNonterminals(R, s, f) do
20:                     M₂ˢ[x, y] ← {1}
21:     return 𝓜₂, C₃
```

Listing 2 Help functions for Kronecker product based CFPQ

```
1:  function GETSTATES(C, i, j)
2:      r ← dim(𝓜₁) ▷ mathcalM₁ is Boolean adjacency matrix for R
3:      return ⌊i/r⌋, ⌊j/r⌋
4:  function GETCOORDINATES(C, i, j)
5:      n ← dim(𝓜₂)        ▷ 𝓜₂ is Boolean adjacency matrix for 𝒢
6:      return i mod n, j mod n
```

LEMMA 3.1. *Let $\mathcal{G} = (V, E, L)$ be a graph and $G = \langle \Sigma, N, S, P \rangle$ be a grammar. Let $\mathcal{M}_{2,(k)}$ be an adjacency matrix $\mathcal{M}_2$ after the execution of some iteration $k \geq 0$ of the algorithm in Listing 1. Then for any valid indices i, j and for each nonterminal $A \in N$ such that cell $M_{2,(k)}^A[i, j]$ contains $\{1\}$, the following statement holds: in the graph $\mathcal{G} \; \exists i \pi j : A \xrightarrow{*} l(\pi)$.*

PROOF. (Proof by induction)

Basis: For $k = 0$ and the statement of the lemma holds, since $\mathcal{M}_{2,(0)} = \mathcal{M}_2$, where $\mathcal{M}_2$ is adjacency matrix of the graph $\mathcal{G}$. The nonterminals, which allow to derive ε-word, are also added at algorithm preprocessing step, since each vertex of the graph is reachable by itself through an ε-transition.

Inductive step: Assume that the statement of the lemma holds for any $k \leq (p − 1)$ and show that it also holds for $k = p$, where $p \geq 1$.

For the algorithm iteration p the Kronecker product $\mathcal{M}_3$, M_3' and transitive closure C_3 are evaluated as described in the

algorithm. By the properties of this operations, some edge $e = ((s, i), (f, j))$ exists in the directed graph, represented by adjacency matrix C_3, if and only if $\exists s\pi' f$ in the RSM graph, represented by matrix $\mathcal{M}_1$, and $\exists i\pi j$ in graph, represented by $\mathcal{M}_{2,(p-1)}$. Concatenated symbols along the path π' form some derivation string v, composed from terminals and non-terminals, where $v \xrightarrow{*} l(\pi)$ by the inductive assumption.

The new $\{1\}$ will be added to the cell $M_{2,(k)}^A[i, j]$ only if s and f are initial and final states of some box of the RSM corresponding to the non-terminal A. In this case, the grammar G has the derivation rule $A \to v$, and by the inductive assumption $v \xrightarrow{*} l(\pi)$. Therefore, $A \xrightarrow{*} l(\pi)$ and this completes the proof of the lemma.

□

LEMMA 3.2. *Let* $\mathcal{G} = (V, E, L)$ *be a graph and* $G = \langle \Sigma, N, S, P \rangle$ *be a grammar. Let* $\mathcal{M}_{2,(k)}$ *be an adjacency matrix* $\mathcal{M}_2$ *after the execution of some iteration* $k \geq 0$ *of the algorithm in Listing 1. For any path* $i\pi j$ *in the graph* $\mathcal{G}$ *with word* $l = l(\pi)$ *if exists the derivation tree of* l *from the nonterminal* A *of the grammar* G *with the height* $h \leq k + 1$, *then* $M_{2,(k)}^A[i, j]$ *contains* $\{1\}$.

PROOF. (Proof by induction)
Basis: Show that statement of the lemma holds for the $k = 0$. Matrix $\mathcal{M}_{2,(0)} = \mathcal{M}_2$ and edges of the graph $\mathcal{G}$ contains only labels from L. Since the derivation tree of height $h = k + 1 = 1$ contains only one non-terminal A as a root and only symbols from $\Sigma \cup \varepsilon$ as leafs, for all paths, which form a word with derivation tree of the height $h = 1$, the corresponding nonterminals will be added to the $M_{2,(0)}^A[i, j]$ via preprocessing step. Thus, the lemma statement holds for the $k = 1$.

Inductive step: Assume that the statement of the lemma hold for any $k \leq (p - 1)$ and show that it also holds for $k = p$, where $p \geq 2$.

For the algorithm iteration p the Kronecker product $\mathcal{M}_3, M_3'$ and transitive closure C_3 are evaluated as described in the algorithm. By the properties of this operations, some edge $e = ((s, i), (f, j))$ exists in the directed graph, represented by adjacency matrix C_3, if and only if $\exists s\pi' f$ in the RSM graph, represented by matrix $\mathcal{M}_1$, and $\exists i\pi j$ in graph, represented by $\mathcal{M}_{2,(p-1)}$.

For any path $i\pi j$, such that exist derivation tree of height $h < p + 1$ for the word $l(\pi)$ with root non-terminal A, the cell $M_{2,(p)}^A[i, j]$ contains $\{1\}$ by inductive assumption.

Suppose, that exists derivation tree T of height $h = p + 1$ with the root non-terminal A for the path $i\pi j$. The tree T is formed as $A \to a_1..a_d, d \geq 1$ where $\forall x \in [1..d]$ a_x is subtree of height $h_x \leq p$ for the sub-path $i_x\pi_x j_x$. By inductive hypothesis, there exists path π_x for each derivation sub-tree, such that $i = i_1\pi_1 i_2..i_d\pi_d j_d = j$ and concatenation of these

paths forms $i\pi j$, and the root nonterminals of this sub-trees are included in the matrix $M_{2,(p-1)}$.

Therefore, vertices i_x $\forall x \in [1..d]$ form path in the graph, represented by matrix $\mathcal{M}_{2,(p-1)}$, with complete set of labels. Thus, new $\{1\}$ will be added to the cell $M_{2,(p)}^A[i, j]$ corresponding to the vertices i and j and nonterminal A. This completes the proof of the lemma.

□

THEOREM 3.3. *Let* $\mathcal{G} = (V, E, L)$ *be a graph and* $G = \langle \Sigma, N, S, P \rangle$ *be a grammar. Let* $\mathcal{M}_2$ *be a result adjacency matrix after the execution of the algorithm in Listing 1. Then for any valid indices* i, j *and for each nonterminal* $A \in N$ *the following statement holds: the cell* $M_{2,(k)}^A[i, j]$ *contains* $\{1\}$, *if and only if there is a path* $i\pi j$ *in the graph* $\mathcal{G}$ *such that* $A \xrightarrow{*} l(\pi)$.

PROOF. This theorem is a consequence of the Lemma 3.1 and Lemma 3.2.

□

THEOREM 3.4. *Let* $\mathcal{G} = (V, E, L)$ *be a graph and* $G = \langle \Sigma, N, S, P \rangle$ *be a grammar. The algorithm in Listing 1 terminates in finite number of steps.*

PROOF. The main *while-loop* in the algorithm is executed while graph adjacency matrix $\mathcal{M}_2$ is changing. Since the algorithm only adds the edges with non-terminals from N, the maximum required number of iterations is $|N| \times |V| \times |V|$, where each component has finite size. This completes the proof of the theorem.

□

3.1.3 Application of Dynamic Transitive Closure. In this subsection we show how to reduce the time complexity of the algorithm in Listing 1 by avoiding redundant calculations.

It is easy to see that the most time-consuming steps in this algorithm are the Kronecker product and transitive closure computations. Recall that the matrix $\mathcal{M}_2$ is always changed in incremental manner i. e. elements (edges) are added to $\mathcal{M}_2$ (and are never deleted from it) on each iteration of the algorithm in Listing 1. So one does not need to recompute the whole product or transitive closure if an appropriate date structure is maintained.

To deal with the Kronecker product computation, we use the left-distributivity of the Kronecker product. Let $\mathcal{A}_2$ be a matrix with newly added elements and $\mathcal{B}_2$ be a matrix with the all previously found elements, such that $\mathcal{M}_2 = \mathcal{A}_2 + \mathcal{B}_2$. Then by the left-distributivity of the Kronecker product we have $\mathcal{M}_1 \otimes \mathcal{M}_2 = \mathcal{M}_1 \otimes (\mathcal{A}_2 + \mathcal{B}_2) = \mathcal{M}_1 \otimes \mathcal{A}_2 + \mathcal{M}_1 \otimes \mathcal{B}_2$. Notice that $\mathcal{M}_1 \otimes \mathcal{B}_2$ is known and is already in the matrix $\mathcal{M}_3$ and its transitive closure also is already in the matrix C_3, because it was calculated on the previous iterations, so it is left to update some elements of $\mathcal{M}_3$ by computing $\mathcal{M}_1 \otimes \mathcal{A}_2$,
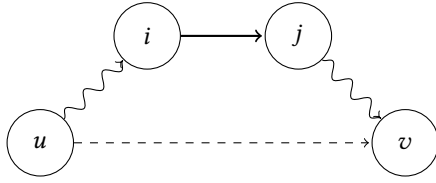
Figure 6: The vertex j become reachable from the vertex u after the addition of edge (i, j). Then the vertex v is reachable from u after inserting the edge (i, j) if v is reachable from j.

which can be done in $O(|\mathcal{A}_2||\mathcal{M}_1|)$ time, where $|\mathcal{A}|$ denotes the number of non-zero elements in a matrix $\mathcal{A}$.

The fast computation of transitive closure can be obtained by using incremental dynamic transitive closure technique. We use an approach by Ibaraki and Katoh [23] to maintain dynamic transitive closure. The key idea of their algorithm is to recalculate reachability information only for those vertices, which become reachable after insertion of the certain edge (see Figure 6 for details). The algorithm is presented in Listing 3 (we have slightly modified it to efficiently track new elements of the matrix C_3).

Listing 3 The dynamic transitive closure procedure

```
1:  function ADD(C₃, i, j)
2:      n ← Number of rows in C₃
3:      C₃′ ← Empty matrix of size n × n
4:      for u ≠ 0 ∈ checkCondition(C₃, i, j) do
5:          newReachablePairs(C₃, C₃′, u, j)
6:      return C₃′
7:  function CHECKCONDITION(C₃, i, j)
8:      A ← Empty array of size n
9:      for u ∈ 0…n | u ≠ j do      ▷ 1 ∧ 1 = 0 ∧ 0 = 1 ∧ 0 = 0; 0 ∧ 1 = 1
10:         A[u] = C₃[u, j] ∧ C₃[u, i]
11:     return A
12: function NEWREACHABLEPAIRS(C₃, C₃′, u, j)
13:     C₃′[u, v] = C₃[u, v] ∧ C₃[j, v]      ▷ 1 ∧ 1 = 0 ∧ 0 = 1 ∧ 0 = 0;
            0 ∧ 1 = 1
```

Final version of the modified algorithm from Listing 1 is shown in Listing 4.

THEOREM 3.5. *Let $\mathcal{G} = (V, E, L)$ be a graph and $G = \langle \Sigma, N, S, P \rangle$ be a grammar. The algorithm from Listing 4 calculates a result matrices $\mathcal{M}_2$ and C_3 in $O(n^3)$ time where $n = |V|$.*

PROOF. Let $|\mathcal{A}|$ be a number of non-zero elements in a matrix $\mathcal{A}$. Consider the total time which is needed for computing the Kronecker products. The elements of the matrices $\mathcal{A}_2^{(i)}$ are pairwise distinct on every i-th iteration of the algorithm therefore we have

$$\sum_i T(\mathcal{M}_1 \otimes \mathcal{A}_2^{(i)}) = |\mathcal{M}_1| \otimes \sum_i |\mathcal{A}_2^{(i)}| = |\mathcal{M}_1|O(n^2)$$

Listing 4 Kronecker product based CFPQ using dynamic transitive closure

```
1:  function CONTEXTFREEPATHQUERYING(G, 𝒢)
2:      R ← Recursive automata for G
3:      M₁ ← Adjacency matrix for R
4:      M₂ ← Adjacency matrix for 𝒢
5:      A₂ ← Adjacency matrix for 𝒢
6:      C₃ ← The empty matrix
7:      for s ∈ 0..dim(M₁) − 1 do
8:          for i ∈ 0..dim(M₂) − 1 do
9:              M₂[i, i] ← M₂[i, i] ∪ getNonterminals(R, s, s)
10:     while Matrix M₂ is changing do
11:         M₃′ ← M₁ ⊗ A₂
12:         A₂ ← The empty matrix of size n × n
13:         for M₃′[i, j] | M₃′[i, j] = 1 do
14:             C₃[i, j] ← 1
15:             C₃′ ← ⋃₍ᵢ,ⱼ₎ add(C₃, i, j)  ▷ Updating the transitive closure
16:             C₃ ← C₃ + C₃′
17:         n ← dim(M₃)
18:         for (i, j) | C₃′[i, j] ≠ 0 do
19:             s, f ← getStates(C₃′, i, j)
20:             if getNonterminals(R, s, f) ≠ ∅ then
21:                 x, y ← getCoordinates(C₃′, i, j)
22:                 M₂[x, y] ← M₂[x, y] ∪ getNonterminals(R, s, f)
23:                 A₂[x, y] ← A₂[x, y] ∪ getNonterminals(R, s, f)
24:     return 𝓜₂, C₃
```

operations in total.

Now we derive the time complexity of maintainig the dynamic transitive closure. Notice that C_3 has size of $O(n^2)$ so no more than $O(n^2)$ edges will be added during all iterations of the Algorithm. The function *checkCondition* from the Listing 3 takes $O(n)$ time for every inserted edge (i, j). Thus we have $O(n^2 n) = O(n^3)$ operations in total. The function *newReachablePairs* requires $O(n)$ time for a given vertex u. This operation is performed for every pair (j, v) of vertices such that a vertex j became reachable from the vertex u. The vertex j become reachable from the vertex u (and accordingly the value of the matrix cell $C_3[u, j]$ becomes 1 from 0) only once during the entire computation, so the function *newReachablePairs* will be executed at most $O(n^2)$ times for every u and hence $O(n^3)$ times in total for all vertices. Therefore $O(n^3)$ operations are performed to maintain dynamic transitive closure during all iteration of the algorithm from Listing 4.

Notice that the matrix $C_3′$ contains only new elements, therefore C_3 can be updated directly using only $|C_3′|$ operations and hence $O(n^2)$ operations in total. The same holds for cycle in line 18 of the algorithm from Listing 4, because operations are performed only for non-zero elements of the matrix $|C_3′|$. Finally, we have that the time complexity of the algorithm is $O(n^2) + O(n^3) + O(n^2) + O(n^2) = O(n^3)$. □

3.1.4 Speeding up by a factor of $\log n$. In this subsection we use the Four Russians' trick to speed up the dynamic transitive closure algorithm from the Listing 3.

THEOREM 3.6. *The computation of transitive closure matrices can be done in* $O(n^3/\log n)$ *time when* n^2 *edges are added to the graph.*

PROOF. Consider the function *checkCondition* from the Listing 3. Its operations are equivalent to the element-wise (Hadamard) product of two vectors of size n, where multiplication operation is denoted as $\wedge$ and has the following properties: $1 \wedge 1 = 0 \wedge 0 = 1 \wedge 0 = 0$ and $0 \wedge 1 = 1$. The first vector represents reachability of a given vertex i from other vertices $\{u_1, u_2, ..., u_n\}$ of the graph and the second vector represents the same for a given vertex j. The function *newReachablePairs* also can be reduced to the computation of the Hadamard product of two vectors of size n for a given u_k. The first vector contains the information whether vertices $\{v_1, v_2, ..., v_n\}$ of the graph are reachable from a given vertex u_k and the second vector represents the same for a given vertex j. The element-wise product of two vectors can be calculated naively in time $O(n)$ which gives the $O(n^3)$ time for maintaining the transitive closure. Thus, the time complexity of the transitive closure can be reduced by speeding up element-wise product of two vectors of size n.

To achive this goal, we use the Four Russians' trick. Split each vector into $n/\log n$ parts of size $\log n$. Create a table S such that $S(a, b) = a \wedge b$ where $a, b \in \{0, 1\}^{\log n}$. This takes a time $O(n^2 \log n)$, since there are $2^{\log n} = n$ variants of Boolean vectors of size $\log n$ and hence n^2 pairs of vectors (a, b) in total, and each component takes $O(\log n)$ time. With table S, we can calculate product of two parts of size $\log n$ in constant time. There are $n/\log n$ such parts, so the element-wise product of two vectors of size n can be calculated in time $O(n/\log n)$ with $O(n^2 \log n)$ preprocessing. This gives us a dynamic transitive closure algorithm running in time $O(n^3/\log n)$: both of the functions *checkCondition* and *newReachablePairs* are evaluated no more than $O(n^2)$ times during the whole computation, and each function calculates Hadamard product of two vectors in $O(n/\log n)$ time. $\square$

Notice that the maintaining of the dynamic transitive closure dominates the cost of the algorithm from Listing 4, therefore we immediately deduce the following.

COROLLARY 3.7. *Let* $\mathcal{G} = (V, E, L)$ *be a graph and* $G = \langle \Sigma, N, S, P \rangle$ *be a grammar. The result result matrices* $\mathcal{M}_2$ *and* C_3 *can be calculated in* $O(n^3/\log n)$ *time.*

Finally, we formulate the theorem which connects the time complexity of CFPQ and time complexity of specific incremental transitive closure of a directed graph.

THEOREM 3.8. *Subcubic incremental transitive closure leads to subcubic CFPQ. Suppose the incremental transitive closure problem where only insertion queries are allowed and the result of each insertion is a set of newly connected pairs. If one can solve this problem in* $O(n^{3-\varepsilon})$ *total time for* n^2 *insertions, then one can solve CFPQ in* $O(n^{3-\varepsilon})$*, where* n *is a number of vertices in the graph in both cases.*

3.2 Paths Extraction Algoritm

After index created one can enumerate all paths between specified vertices. Note, that the index stores information about all reachable pairs for all nonterminals. Thus, the most natural way to use this index is to query paths between specified vertices derivable from specified nonterminal.

To do it we provide a function GETPATHS(v_s, v_f, N), where v_s is a start vertex of the graph, v_f — the final vertex, and N is a nonterminal. Implementation of this function is presented in Listing 5.

Listing 5 Paths extraction algorithm

```
1:  C₃ ← result of index creation algorithm: final transitive closure
2:  M₁ ← the set of adjacency matrices of the input RSM
3:  M₂ ← the set of adjacency matrices of the final graph
4:  function GETPATHS(vₛ, v_f, N)
5:      q_N⁰ ← Start state of automata for N
6:      F_N ← Final states of automata for N
7:      res ← ⋃_{f∈F_N} GETPATHSINNER((q_N, vₛ), (f, v_f))
8:      return res
9:  function GETSUBPATHS((sᵢ, vᵢ), (s_j, v_j), (s_k, v_k))
10:     l ← {(vᵢ, t, v_k) | M₂ᵗ[sᵢ, s_k] ∧ M₁[vᵢ, v_k]}
              ∪ ⋃_{N|M₂^N[sᵢ,s_k]} GETPATHS(vᵢ, v_k, N)
              ∪ GETPATHSINNER((sᵢ, vᵢ), (s_k, v_k))
11:     r ← {(v_k, t, v_j) | M₂ᵗ[s_k, s_j] ∧ M₁[v_k, v_j]}
              ∪ ⋃_{N|M₂^N[s_k,s_j]} GETPATHS(v_k, v_j, N)
              ∪ GETPATHSINNER((s_k, v_k), (s_j, v_j))
12:     return l · r
13: function GETPATHSINNER((sᵢ, vᵢ), (s_j, v_j))
14:     parts ← {(s_k, v_k) | C₃[(sᵢ, vᵢ), (s_k, v_k)] = 1 ∧
        C₃[(s_k, v_k), (s_j, v_j)] = 1}
15:     return ⋃_{(s_k,v_k)∈parts} GETSUBPATHS((sᵢ, vᵢ), (s_j, v_j), (s_k, v_k))
```

Paths extraction is implemented as three mutually recursive functions. The entry point is GETPATHS(v_s, v_f, N). This function returns a set of paths between v_s and v_f such that the word formed by the path is derivable from nonterminal N.

To compute such paths it is necessary to compute paths from vertices of the form (q_N^s, v_s) to vertices of the form (q_N^f, v_f) in the result of transitive closure, where q_N^s is an initial state of RSM for N and q_N^f is a final state. To do it

GETPATHSINNER$((s_i, v_i), (s_j, v_j))$ is used. This function finds all possible vertices (s_k, v_k) which split path from (s_i, v_i) to (s_j, v_j) into two subpaths. After that, function GETSUB-PATHS$((s_i, v_i), (s_j, v_j), (s_k, v_k))$ is used to compute corresponding subpaths. Each part of the path may be a single edge, or path with length more than one. In the second case GET-PATHSINNER is used to restore corresponding paths. In the first case, the edge can be labeled by terminal or nonterminal. In the first case corresponding edge should be added to the result. In the second case, GETPATHS should be used to restore paths.

Note, that, first of all, we assume that sets are computed lazily. It is necessary to work correctly in the case of an infinite number of paths. Second, we use a set of path as a result, so we did not check duplicated paths manually.

3.3 An example

In this section we introduce detailed example to demonstrate steps of the proposed algorithms. Our example is based on the classical worst case scenario introduced by Jelle Hellings in [21]. Namely, let we have a graph $\mathcal{G}$ presented in Figure 1 and the RSM R presented in Figure 5.

First step we represent graph as a set of Boolean matrices as presented in Figure 2, and RSM as a set of Boolean matrices as presented in Figure 4. Note, that we should formally add new empty matrix M_2^S to $\mathcal{M}_2$, where edges labeled by S will be added in time of the computation.

After the initialization, the algorithm handles ε-case. The input RSM does not have ε-transitions and does not have states that are both start and final, therefore, no edges added at this stage. After that we should iteratively compute $\mathcal{M}_2$ and C_3. The loop iteration number of matrices evaluation is provided as the subscript in parentheses.

First iteration. Firstly, we compute Kronecker product of the $\mathcal{M}_1$ and $\mathcal{M}_{2,(0)}$ matrices and store result in the $\mathcal{M}_{3,(1)}$, and collapse this matrix to the single Boolean matrix $M'_{3,(1)}$. For the sake of simplicity, we provide only $M'_{3,(1)}$, which is evaluated as follows in the equivalent way.

$$M'_{3,(1)} = M_1^a \otimes M_{2,(0)}^a + M_1^b \otimes M_{2,(0)}^b + M_1^S \otimes M_{2,(0)}^S =$$

	(0,0)(0,1)(0,2)(0,3)	(1,0)(1,1)(1,2)(1,3)	(2,0)(2,1)(2,2)(2,3)	(3,0)(3,1)(3,2)(3,3)
(0,0)	. . . .	1 . . .	. . . .	. . . .
(0,1)	. . . .	. . 1 .	. . . .	. . . .
(0,2)	. . . 1	. . . .	. . . .	. . . .
(0,3)	. . . .	. . . .	. . . .	. . . .
(1,0)	. . . .	. . . .	. . . .	. . . .
(1,1)	. . . .	. . . .	. . . .	. . . .
(1,2)	. . . .	. . . .	. . . .	. . . 1
(1,3)	. . . .	. . . .	. . . .	. . 1 .
(2,0)	. . . .	. . . .	. . . .	. . . .
(2,1)	. . . .	. . . .	. . . .	. . . .
(2,2)	. . . .	. . . .	. . . .	. . . 1
(2,3)	. . . .	. . . .	. . . .	. . 1 .
(3,0)	. . . .	. . . .	. . . .	. . . .
(3,1)	. . . .	. . . .	. . . .	. . . .
(3,2)	. . . .	. . . .	. . . .	. . . .
(3,3)	. . . .	. . . .	. . . .	. . . .

As far as the input graph has no edges with label S, therefore, the correspondent block of the Kronecker product will be empty. Then, the transitive closure evaluation result, stored in the matrix $C_{3,(1)}$, introduces one new path of length 2 (respective cell is filled with a grey colour).

$$C_{3,(1)} = transitiveClosure(M'_{3,(1)}) =$$

	(0,0)(0,1)(0,2)(0,3)	(1,0)(1,1)(1,2)(1,3)	(2,0)(2,1)(2,2)(2,3)	(3,0)(3,1)(3,2)(3,3)
(0,0)	. . . .	1 . . .	. . . .	. . . .
(0,1)	. . . .	. . 1 .	. . . .	. . . 1
(0,2)	. . . 1	. . . .	. . . .	. . . .
(0,3)	. . . .	. . . .	. . . .	. . . .
(1,0)	. . . .	. . . .	. . . .	. . . .
(1,1)	. . . .	. . . .	. . . .	. . . .
(1,2)	. . . .	. . . .	. . . .	. . . 1
(1,3)	. . . .	. . . .	. . . .	. . 1 .
(2,0)	. . . .	. . . .	. . . .	. . . .
(2,1)	. . . .	. . . .	. . . .	. . . .
(2,2)	. . . .	. . . .	. . . .	. . . 1
(2,3)	. . . .	. . . .	. . . .	. . 1 .
(3,0)	. . . .	. . . .	. . . .	. . . .
(3,1)	. . . .	. . . .	. . . .	. . . .
(3,2)	. . . .	. . . .	. . . .	. . . .
(3,3)	. . . .	. . . .	. . . .	. . . .

This path starts in the vertex $(0, 1)$ and finishes in the vertex $(3, 3)$. We can see, that 0 and 3 are a start and a final states of the some component state machine for label S in R respectively. Thus we can conclude that there exists a path between vertices 1 and 3 in the graph, such that respective word is derivable from S in the R execution flow.

As a result, we can add the edge $(1, S, 3)$ to the result graph, what is formally done by the update of the matrix M_2^S.

Second iteration. Modified graph Boolean adjacency matrices contain now edge with label S. Therefore, this label contributes to the non-empty corresponding matrix block in the evaluated matrix $M'_{3,2}$. The transitive closure evaluation introduces three new paths. Since only path between vertices $(0, 0)$ and $(3, 2)$ connects start and final states if the automata, the edge $(0, S, 2)$ is added to the result graph.

$$M'_{3,(2)} = M_1^a \otimes M_{2,(2)}^a + M_1^b \otimes M_{2,(2)}^b + M_1^S \otimes M_{2,(2)}^S =$$

	(0,0)(0,1)(0,2)(0,3)	(1,0)(1,1)(1,2)(1,3)	(2,0)(2,1)(2,2)(2,3)	(3,0)(3,1)(3,2)(3,3)
(0,0)	. . . .	1 . . .	. . . .	. . . .
(0,1)	. . . .	. . 1 .	. . . .	. . . .
(0,2)	. . . 1	. . . .	. . . .	. . . .
(0,3)	. . . .	. . . .	. . . .	. . . .
(1,0)	. . . .	. . . .	. . . .	. . . .
(1,1)	. . . .	. . . .	. 1 . .	. . . .
(1,2)	. . . .	. . . .	. . . .	. . . 1
(1,3)	. . . .	. . . .	. . . .	. . 1 .
(2,0)	. . . .	. . . .	. . . .	. . . .
(2,1)	. . . .	. . . .	. . . .	. . . .
(2,2)	. . . .	. . . .	. . . .	. . . 1
(2,3)	. . . .	. . . .	. . . .	. . 1 .
(3,0)	. . . .	. . . .	. . . .	. . . .
(3,1)	. . . .	. . . .	. . . .	. . . .
(3,2)	. . . .	. . . .	. . . .	. . . .
(3,3)	. . . .	. . . .	. . . .	. . . .

$$C_{3,(2)} = transitiveClosure(M'_{3,(2)}) =$$

	(0,0)(0,1)(0,2)(0,3)	(1,0)(1,1)(1,2)(1,3)	(2,0)(2,1)(2,2)(2,3)	(3,0)(3,1)(3,2)(3,3)
(0,0)	. . . .	1 . . .	. 1 . .	1 . . 1
(0,1)	. . . .	. . 1 .	. . . .	. . . .
(0,2)	. . . 1	. . . .	. . . .	. . . .
(0,3)	. . . .	. . . .	. . . .	. . . .
(1,0)	. . . .	. . . .	. . . .	. . . .
(1,1)	. . . .	. . . .	. 1 . .	1 . . 1
(1,2)	. . . .	. . . .	. . . .	. . . 1
(1,3)	. . . .	. . . .	. . . .	. . 1 .
(2,0)	. . . .	. . . .	. . . .	. . . .
(2,1)	. . . .	. . . .	. . . .	. . . .
(2,2)	. . . .	. . . .	. . . .	. . . 1
(2,3)	. . . .	. . . .	. . . .	. . 1 .
(3,0)	. . . .	. . . .	. . . .	. . . .
(3,1)	. . . .	. . . .	. . . .	. . . .
(3,2)	. . . .	. . . .	. . . .	. . . .
(3,3)	. . . .	. . . .	. . . .	. . . .

The result transitive closure matrix $C_{3,(6)}$ of the remaining iterations evaluated as follows. The result graph is presented in figure 7.

$C_{3,(6)} =$

	(0,0)	(0,1)	(0,2)	(0,3)	(1,0)	(1,1)	(1,2)	(1,3)	(2,0)	(2,1)	(2,2)	(2,3)	(3,0)	(3,1)	(3,2)	(3,3)
(0,0)	.	.	.	.	1	.	.	.	.	.	.	.	.	.	1	1
(0,1)	.	.	.	.	.	1	.	.	.	.	.	.	1	1	1	1
(0,2)	.	.	.	.	1	.	.	.	.	.	.	.	1	1	1	1
(0,3)	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
(1,0)	.	.	.	.	.	.	.	.	.	.	1	1	.	.	1	1
(1,1)	.	.	.	.	.	.	.	.	.	1	1	.	.	.	1	1
(1,2)	.	.	.	.	.	.	.	.	.	.	1	.	.	.	1	1
(1,3)	.	.	.	.	.	.	.	.	.	.	.	.	.	.	1	.
(2,0)	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
(2,1)	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
(2,2)	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	1
(2,3)	.	.	.	.	.	.	.	.	.	.	.	.	.	.	1	.
(3,0)	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
(3,1)	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
(3,2)	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
(3,3)	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.

Figure 7: The result graph $\mathcal{G}$

```
getPaths(2, 2, S)
  getPathsInner((0, 2), (3, 2))
    parts= {(1, 0), (2, 3)}
    getSubpaths((0, 2), (3, 2), (1, 0))
      l={2 --a--> 0}
        ...
        getPathsInner((0, 0), (3, 2))
          parts = {(1, 1), (2, 3)}
          getSubpaths((0, 0), (3, 2), (1, 1))
            ...
            getPaths(1, 3, S)
              ...
              getSubpaths((0, 1), (3, 3), (1, 2))
                l = {1 --a--> 2}
                r = {2 --b--> 3}
                return {1 --a--> 2 --b--> 3}
    getSubpaths((0, 0), (3, 2), (2, 3))
      ...
      getPaths(1, 3, S) // An alternative way to get paths
                        //      from 1 to 3 which leads to
                        //      infinite set of paths
      return r_∞^{1~>3} // An infinite set of path from 1 to 3
      ...
    return {0 --a--> 1 --a--> 2 --b--> 3 --b--> 2} ∪ ({0 --a--> 1} · r_∞^{1~>3} · {3 --b--> 2})
  return {2 --a--> 0 --a--> 1 --a--> 2 --a--> 0 --a--> 1 --a--> 2 --b--> 3 --b--> 2 --b--> 3 --b--> 2} ∪ ({2 --a--> 0 --a--> 1 --a--> 2 --a--> 0 --a--> 1} · r_∞^{1~>3} · {3 --b--> 2 --b--> 3 --b--> 2 --b--> 3 --b--> 2})
```

Figure 8: Example of call stack trace

At this point the index creation is finished. One can use it to answer reachability queries, but for some problems it can be used to restore paths for some reachable vertices. The result transitive closure matrix C_3 or so called *index* could be used for that. For example, let we try to restore paths from 2 to 2 derived from S in the result graph.

To get these paths we should call getPaths(2, 2, s) function. Partial trace of this call is presented below in figure 8. First, we must query paths for all possible start and final states of the machine for the provided graph vertices. Since in the example RSM the component state machine with label S has single final state, the function getPathsInner is called with arguments $(0, 2)$ and $(3, 2)$. Note, that in the path extraction algorithm passed values to the functions is pairs of the machine state and graph vertex, which uniquely identify cell of the index matrix C_3. Possible paths concatenation vertices are stored as parts={(1,0),(2,3)}. Then we try to get parts of paths going throw index vertex $(1, 0)$. All possible concatenations variants of the paths are queried in the corresponding getSubpaths function call. As the result, we get the set of possible paths in the graph from 2 to 2.

Lazy evaluation is required here, since the result graph may possibly have an infinite number of path between some vertices pair. Another approach here is to try to query some fixed number of paths, or just one path. Eventually, the paths enumeration problem is actual here: how can we enumerate paths with small delay.

4 IMPLEMENTATION DETAILS

In order to evaluate the proposed algorithm, we implement its naïve version: transitive closure computes on each iteration from scratch, without incremental techniques utilization. For implementation we use PyGraphBLAS[2] — a Python wrapper for SuiteSparse library [15][3]. SuiteSparse is a C implementation of GraphBLAS [25] standard which introduces linear algebra building blocks for graph analysis algorithms implementation. Thus we provide a highly-optimized parallel CPU implementation of the naïve version of the proposed algorithm[4].

In the current version we do not provide integration with graph database and graph query language, because our goal is the algorithm applicability evaluation. So, we suppose that graph is stored in file, and query is expressed in terms of context-free grammar and stored in file too. As it was shown in [40] it is possible to integrate SuiteSparse based implementation in the RedisGraph database. To provide integration with query language, it is necessary to extend the language

---

first. It is possible, for example one can use existing proposal[5] to extend Cypher language, but it requires a lot of technical effort, so it is an interesting challenge for future research to provide full-stack support for CFPQ.

Paths extraction also is implemented in Python by using PyGraphBLAS. For evaluation we implement a version which has an additional parameter: a maximal number of paths to extract. This modification allows as to avoid lazy evaluation which is not natural for Python. Note that one can provide other modifications of paths extraction algorithm based on the proposed idea.

5 EVALUATION

We evaluate the implemented algorithm on doth regular and context-free path queryes in order to demonstrate applicability of the proposed solution. Namely, goals of the evaluation are following.

(1) Investigate practical applicability of RPQ evaluation by the proposed algorithm.
(2) Compare Azimov's algorithm for reachability CFPQ and the proposed algorithm.
(3) Investigate practical applicability of paths extraction algorithm for both regular and context-free queries.

For evaluation, we use a PC with Ubuntu 18.04 installed. It has Intel core i7-6700 CPU, 3.4GHz, and DDR4 64Gb RAM. As far as we evaluate only algorithm execution time, we store each graph fully in RAM as its adjacency matrix in sparse format. Note, that graph loading time is not included in the result time of evaluation.

5.1 RPQ Evaluation

In oder to investigate applicability of the proposed algorithm for RPQ over real-world graphs we collect a set of real-world and synthetic graphs and evaluate queries generated by using the most popular templates for RPQs.

5.1.1 Dataset. Brief description of collected graphs are presented in table 1. Namely, the dataset consists of several parts. The first one is a set of LUBM graphs with different number of vertices, generated by [?]. Second one is a graphs from Uniprot database[6] [?]: *proteomes* and *!!!*. RDFs and from DBPedia.

Queries for evaluation was generated by using templates of the most popular RPQs which are collected from [?] and [?], and are presented in table ??. We generate 10 queries for each template and each graph using the most frequent relations from the given graph randomly.

---

[5]Cypher language extension proposal which introduces a syntax to express context-free queries: https://github.com/thobe/openCypher/blob/rpq/cip/1. accepted/CIP2017-02-06-Path-Patterns.adoc. Access date: 07.07.2020.
[6]

Graph	#V	#E
LUBM100	300000	4000000
LUBM300	3	4
LUBM500	3	4
LUBM1M	3	4
LUBM1.5M	3	4
LUBM1.9M	3	4
Uniprotkb	3	4
Proteomes	3	4
Geospecies	3	4
Taxonomy	3	4
DBPedia	3	4

Table 1: Graphs for RPQ evaluation

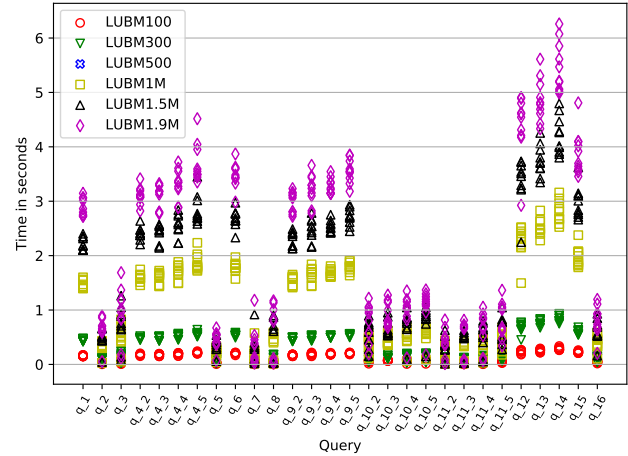**Figure 9: Reachability index creation time for LUBM series**

5.1.2 Results. Results of evaluation

Reachability index creation time for each query for LUBM graphs set is presented in figure 9. We can observe linear dependency of evaluation time on graph size.
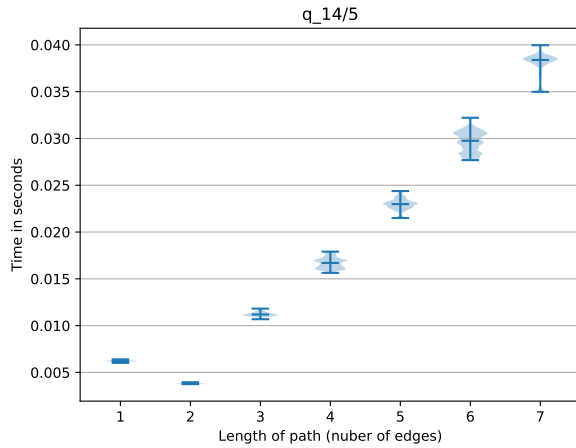
Paths extraction

5.1.3 Conclusion.

5.2 CFPQ Evaluation

Comparison with matrix-based algorithm.

5.2.1 Dataset. Dataset for evaluation. It should be CFPQ_Data[7] Same-generation queries, memory aliases.

---

[7]CFPQ_Data is a dataset for CFPQ evaluation which contains both synthetic and real-world data and queries https://github.com/JetBrains-Research/CFPQ_Data. Access date: 07.07.2020.

Figure 10: Single path extraction

5.2.2 Results. Results of evaluation.
Index creation.
Paths extraction.

5.2.3 Conclusion.

6 RELATED WORK

Language constrained path querying widely used in graph databases, static code analysis and other areas. Both, RPQ and CFPQ (known as CFL reachability problem in static code analysis) actively studied last years.

There is a huge number of theoretical research on RPQ and it's specific cases. RPQ with single-path semantics was investigated from theoretical point of view by Barrett et al. in [5]. In order to research practical limits and restrictions of RPQ, a number of high-performance RPQ algorithms were provided. For example, derivative-based solution provided by Maurizio Nolé and Carlo Sartiani which is implemented on the top of Pregel-based system [34], or solution of André Koschmieder et al. [26]. But only limited number of practical solutions provide ability to restore paths of interest. One of the resent work is a research of Xin Wang et al. [44] in which Pregel-based provenance-aware RPQ algorithm, which utilizes a Glushkov's construction [17], is provided. Applicability of linear algebra based RPQ algorithms with paths-providing semantics is not investigated.

On the other hand, a bunch of CFPQ algorithms based on different ideas and with different properties was proposed in recent years. All of them have not better than cubic time complexity in terms of input graph size, and exploit ideas of different parsing algorithms, such as CYK in works of Jelle Hellings [20] and Phillip Bradford [7], (G)LR and (G)LL in works of Ekaterina Verbitskaia et al. [43], Semyon Grigorev

et al. [18], Fred Santos et al. [36], Ciro Medeiros et al. [31]. Worth mentioning separately Azimov's algorithm [3], which is first, in our knowledge, linear-algebra based algorithm for CFPQ. It was shown by Arseniy Terekhov et al. [40] that this algorithm can be applied for real-world graph analysis problems, while Jochem Kuijpers et al. shows in [28] that other state-of-the-art CFPQ algorithms are not performant enough to handle real-world graphs.

One of the important properties of both RPQ and CFPQ algorithms is an ability to restore paths of interest. Some of mentioned algorithms can solve only reachability problem, while in some cases it is important to provide at least one path satisfies query. While Arseniy Terekhov et al. [40] provide first linear algebra based CFPQ algorithm with single path semantics, Jelle Hellings in [?] provides first theoretical investigation of this problem. Also he provide overview of related researches and shows that the problem is related to strings generation problem and respective results from formal language theory. Also he conclude that both theoretical and empirical investigation of CFPQ with single-path and all-path semantics are in early stage, and we agree with this point of view, because we only demonstrates applicability of our solution on paths extraction, without detailed investigation of its properties.

Subcubic CFPQ is a long-standing problem which is actively studied in both graph database and static code analysis communities. The question on existence of subcubic CFPQ algorithm was asked by Mihalis Yannakakis in 1990 in [45]. He notes that Valiant's algorithm [41], the first known truly subcubic algorithm for context-free parsing, can be generalized to direct acyclic graph querying, but it unlikely can be applied for general CFPQ. A bit later Thomas Reps propose a CFL reachability as a framework for interprocedural static code analysis formulate a problem of subcubic bottleneck of context-free language reachability [?]. Since these problems were formulated,!!!! The most general result is a slightly subcubic algorithm inspired by recursive state machine reachability, which was provided by Swarat Chaudhuri in [13]. This algorithm achieve logarithmic speedup and thus $O(n^3/\log n)$ time complexity. The first truly subcubic algorithm with $\widetilde{O}(n^\omega)$ time complexity (ω is the best exponent for matrix multiplication, $\widetilde{O}$ is the asymptotic upper-bound mod polylog factors) for general graph and 1-Dyck language was provided by Phillip Bradford in [8]. Unfortunately, this result cannot be generalized to general context-free queries. The similar result was provided by Andreas Pavlogiannis and Anders Alnor Mathiasen in [35]. Another partial case was investigated by Krishnendu Chatterjee et al. in [11]. The $O(m + n \cdot \alpha(n))$ algorithm for an arbitrary Dyck querying of bidirected graph was described. Here m is a number of edges, n is a number of vertices in the input graph, and $\alpha(x)$

is an inverse Ackermann function. Specific types of static code analysis related to CFL-r, especially Andersen's Pointer Analysis also actively studying. For example, recently BMM-hardness of 1-Dyck reachability was proven by Qirun Zhang in [46]. Other partial cases such as tree querying also were studied.

Utilization of linear algebra is a promising way to high-performance graph analysis. There is a big number of works on specific graph algorithm formulation in terms of linear algebra, for example classical algorithms for transitive closure and all pairs shortest paths. Recently this direction was summarized in GrpahBLAS API [25] which provide building blocks to develop graph analysis algorithm in terms of linear algebra. There is a number of implementation of this API, such as SuiteSparse:GraphBLAS [15] or CombBLAS [9]. Also, linear algebra based approaches to evaluate different classes of queries in different systems actively studying. This approach demonstrates significant performance improvement when applied for SPARQL queries evaluation [24, 32] and for Datalog queries evaluation [37]. Finally, RedisGraph [10], a linear-algebra powered graph database, was created, and it was shown that it outperforms many other graph databases in some scenarios.

7 CONCLUSION AND FUTURE WORK

In this work we present an improved version of the tensor-based algorithm for CFPQ: we reduce the algorithm to operations over Boolean matrices, and we provide ability to extract all paths which satisfie the query. Moreover, the provided algorithm can handle grammars in EBNF, thus it does not requires grammar to be in CNF transformation and avoids grammar explosion. As a result, the algorithm demonstrates practical performance not only on CFPQ queries, but also on RPQ ones, which shown by our evaluation. Thus, we provide universal linear algebra based algorithm for RPQ and CFPQ evaluation with all-paths semantics.

The first important task for the future research is an detailed investigation of paths extraction algorithm. Jelle Hellings in [?] provides theoretical investigation of single path extraction, and shows that the problem is related to formal language theory. All paths extraction is more complicated and should be investigated carefully in order to provide optimal algorithm.

Also the algorithm open a way to attack long-standing problem on subcubic CFPQ by reducing it to incremental transitive closure: *incremental transitive closure with $O(n^{3-\varepsilon})$ total update time for n^2 updates, such that each update returns all new reachable pairs, implies $O(n^{3-\varepsilon})$ CFPQ algorithm.* In this work we prove $O(n^3/\log n)$ time complexity by providing $O(n/\log n)$ transitive closure algorithm.

Thus, the first task for the future is to find truly sublinear algorithm for incremental transitive closure or, as a first step, to improve logarithmic factor. Also, it is interesting to get improved bounds in partial cases. For example, fully dynamic transitive closure for planar graphs can be supported in $O(n^{2/3} \log n)$ time per update [39], and for undirected graph one can use *disjoint sets* which provide operations with time complexity bounded by inverse Ackermann function. Can we use these facts to provide better CFPQ algorithm for respective partial cases? In the case of planarity it is interesting to investigate properties of the input graph and grammar which allow to preserve planarity during query evaluation.

On the other hand, provided reduction open a way to investigate streaming graph querying. This way we can formulate the following questions.

(1) Can we provide more detailed analysis of dynamic CFPQ queries, than provided in [6]?
(2) Can we provide practical solution for CFPQ querying of streaming graphs?
(3) Can we improve existing solutions for RPQ of streaming graphs?

From a practical perspective, it is necessary to analyze the usability of advanced algorithms for dynamic transitive closure. In the current work we evaluate naïve implementation in which transitive closure recalculated on each iteration from scratch. In [19] it is shown that some of advanced algorithms for dynamic transitive closure can be efficiently implemented. Can one of these algorithms be efficiently parallelized and utilized in the proposed algorithm?

Also, it is necessary to evaluate GPGPU-based implementation. Experience in Azimov's algorithm shows that the utilization of GPGPUs allows one to improve performance because operations of linear algebra can be efficiently implemented on GPGPU [33, 40]. Moreover, for practical reason, it is interesting to provide a multi-GPU version of the algorithm and to utilize unified memory, which is suitable for linear algebra based processing of out-of-GPGPU-memory data and traversing on large graphs [14, 16].

In order to simplify the distributed processing of huge graphs, it may be necessary to investigate different formats for sparse matrices, such as HiCOO format [29]. Another interesting question in this direction is about utilization of virtualization techniques: should we implement distributed version of algorithm manually or it can be better to use CPU and RAM virtualization to get a virtual machine with huge amount of RAM and big number of computational cores. The experience of the Trinity project team shows that it can make sense [38].

Finally, it is necessary to provide a multiple-source version of the algorithm and integrate it with a graph database. Redis-Graph[8] [10] is a suitable candidate for this purpose. This database uses SuiteSparse—an implementation of GraphBLAS standard—as a base for graph processing. This fact allowed to Arseny Terkhov et.al. to integrate Azimov's algorithm to RedisGraph with minimal effort [40].

REFERENCES

[1] Rajeev Alur, Kousha Etessami, and Mihalis Yannakakis. 2001. Analysis of Recursive State Machines. In *Computer Aided Verification*, Gérard Berry, Hubert Comon, and Alain Finkel (Eds.). Springer Berlin Heidelberg, Berlin, Heidelberg, 207–220.

[2] Renzo Angles, Marcelo Arenas, Pablo Barceló, Aidan Hogan, Juan Reutter, and Domagoj Vrgoč. 2017. Foundations of Modern Query Languages for Graph Databases. *ACM Comput. Surv.* 50, 5, Article 68 (Sept. 2017), 40 pages. https://doi.org/10.1145/3104031

[3] Rustam Azimov and Semyon Grigorev. 2018. Context-free Path Querying by Matrix Multiplication. In *Proceedings of the 1st ACM SIGMOD Joint International Workshop on Graph Data Management Experiences & Systems (GRADES) and Network Data Analytics (NDA) (GRADES-NDA '18)*. ACM, New York, NY, USA, Article 5, 10 pages. https://doi.org/10.1145/3210259.3210264

[4] Pablo Barceló Baeza. 2013. Querying Graph Databases. In *Proceedings of the 32nd ACM SIGMOD-SIGACT-SIGAI Symposium on Principles of Database Systems (PODS '13)*. Association for Computing Machinery, New York, NY, USA, 175–188. https://doi.org/10.1145/2463664.2465216

[5] Chris Barrett, Riko Jacob, and Madhav Marathe. 2000. Formal-Language-Constrained Path Problems. *SIAM J. Comput.* 30, 3 (May 2000), 809–837. https://doi.org/10.1137/S0097539798337716

[6] Patricia Bouyer and Vincent Jugé. 2017. Dynamic Complexity of the Dyck Reachability. In *Proceedings of the 20th International Conference on Foundations of Software Science and Computation Structures - Volume 10203*. Springer-Verlag, Berlin, Heidelberg, 265–280. https://doi.org/10.1007/978-3-662-54458-7_16

[7] P. G. Bradford. 2017. Efficient exact paths for dyck and semi-dyck labeled path reachability (extended abstract). In *2017 IEEE 8th Annual Ubiquitous Computing, Electronics and Mobile Communication Conference (UEMCON)*. IEEE, 247–253. https://doi.org/10.1109/UEMCON.2017.8249039

[8] Phillip G. Bradford. 2017. Efficient exact paths for dyck and semi-dyck labeled path reachability (extended abstract). *2017 IEEE 8th Annual Ubiquitous Computing, Electronics and Mobile Communication Conference (UEMCON)* (2017), 247–253.

[9] Aydın Buluç and John R Gilbert. 2011. The Combinatorial BLAS: Design, Implementation, and Applications. *Int. J. High Perform. Comput. Appl.* 25, 4 (Nov. 2011), 496–509. https://doi.org/10.1177/1094342011403516

[10] P. Cailliau, T. Davis, V. Gadepally, J. Kepner, R. Lipman, J. Lovitz, and K. Ouaknine. 2019. RedisGraph GraphBLAS Enabled Graph Database. In *2019 IEEE International Parallel and Distributed Processing Symposium Workshops (IPDPSW)*. IEEE, 285–286. https://doi.org/10.1109/IPDPSW.2019.00054

[11] Krishnendu Chatterjee, Bhavya Choudhary, and Andreas Pavlogiannis. 2017. Optimal Dyck Reachability for Data-Dependence and Alias Analysis. *Proc. ACM Program. Lang.* 2, POPL, Article 30 (Dec. 2017), 30 pages. https://doi.org/10.1145/3158118

[12] Swarat Chaudhuri. 2008. Subcubic Algorithms for Recursive State Machines. In *Proceedings of the 35th Annual ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL '08)*. Association for Computing Machinery, New York, NY, USA, 159–169. https://doi.org/10.1145/1328438.1328460

[13] Swarat Chaudhuri. 2008. Subcubic Algorithms for Recursive State Machines. *SIGPLAN Not.* 43, 1 (Jan. 2008), 159âĂŞ169. https://doi.org/10.1145/1328897.1328460

[14] Steven Wei Der Chien, Ivy Bo Peng, and Stefano Markidis. 2019. Performance Evaluation of Advanced Features in CUDA Unified Memory. In *2019 IEEE/ACM Workshop on Memory Centric High Performance Computing, MCHPC@SC 2019, Denver, CO, USA, November 18, 2019*. IEEE, 50–57. https://doi.org/10.1109/MCHPC49590.2019.00014

[15] Timothy A. Davis. 2019. Algorithm 1000: SuiteSparse:GraphBLAS: Graph Algorithms in the Language of Sparse Linear Algebra. *ACM Trans. Math. Softw.* 45, 4, Article 44 (Dec. 2019), 25 pages. https://doi.org/10.1145/3322125

[16] Prasun Gera, Hyojong Kim, Piyush Sao, Hyesoon Kim, and David Bader. 2020. Traversing Large Graphs on GPUs with Unified Memory. *Proc. VLDB Endow.* 13, 7 (March 2020), 1119–1133. https://doi.org/10.14778/3384345.3384358

[17] V M Glushkov. 1961. THE ABSTRACT THEORY OF AUTOMATA. *Russian Mathematical Surveys* 16, 5 (Oct. 1961), 1–53. https://doi.org/10.1070/rm1961v016n05abeh004112

[18] Semyon Grigorev and Anastasiya Ragozina. 2017. Context-free Path Querying with Structural Representation of Result. In *Proceedings of the 13th Central & Eastern European Software Engineering Conference in Russia (CEE-SECR '17)*. ACM, New York, NY, USA, Article 10, 7 pages. https://doi.org/10.1145/3166094.3166104

[19] Kathrin Hanauer, Monika Henzinger, and Christian Schulz. 2020. Faster Fully Dynamic Transitive Closure in Practice. In *18th International Symposium on Experimental Algorithms, SEA 2020, June 16-18, 2020, Catania, Italy (LIPIcs)*, Simone Faro and Domenico Cantone (Eds.), Vol. 160. Schloss Dagstuhl - Leibniz-Zentrum für Informatik, 14:1–14:14. https://doi.org/10.4230/LIPIcs.SEA.2020.14

[20] Jelle Hellings. 2014. Conjunctive context-free path queries. In *Proceedings of ICDT'14*. 119–130.

[21] Jelle Hellings. 2015. Querying for Paths in Graphs using Context-Free Path Queries. *arXiv preprint arXiv:1502.02242* (2015).

[22] John E. Hopcroft, Rajeev Motwani, and Jeffrey D. Ullman. 2006. *Introduction to Automata Theory, Languages, and Computation (3rd Edition)*. Addison-Wesley Longman Publishing Co., Inc., USA.

[23] T. Ibaraki and N. Katoh. 1983. On-line computation of transitive closures of graphs. *Inform. Process. Lett.* 16, 2 (1983), 95 – 97. https://doi.org/10.1016/0020-0190(83)90033-9

[24] Fuad Jamour, Ibrahim Abdelaziz, Yuanzhao Chen, and Panos Kalnis. 2019. Matrix Algebra Framework for Portable, Scalable and Efficient Query Engines for RDF Graphs. In *Proceedings of the Fourteenth EuroSys Conference 2019 (EuroSys '19)*. Association for Computing Machinery, New York, NY, USA, Article 27, 15 pages. https://doi.org/10.1145/3302424.3303962

[25] J. Kepner, P. Aaltonen, D. Bader, A. Buluc, F. Franchetti, J. Gilbert, D. Hutchison, M. Kumar, A. Lumsdaine, H. Meyerhenke, S. McMillan, C. Yang, J. D. Owens, M. Zalewski, T. Mattson, and J. Moreira. 2016. Mathematical foundations of the GraphBLAS. In *2016 IEEE High Performance Extreme Computing Conference (HPEC)*. 1–9. https://doi.org/10.1109/HPEC.2016.7761646

[26] André Koschmieder and Ulf Leser. 2012. Regular Path Queries on Large Graphs. In *Scientific and Statistical Database Management*, Anastasia Ailamaki and Shawn Bowers (Eds.). Springer Berlin Heidelberg, Berlin,

---

[8] RedisGraph is a graph database that is based on the Property Graph Model. Project web page: https://oss.redislabs.com/redisgraph/. Access date: 07.07.2020.

Heidelberg, 177–194.

[27] Egor V. Kostylev, Juan L. Reutter, Miguel Romero, and Domagoj Vrgoč. 2015. SPARQL with Property Paths. In *The Semantic Web - ISWC 2015*, Marcelo Arenas, Oscar Corcho, Elena Simperl, Markus Strohmaier, Mathieu d'Aquin, Kavitha Srinivas, Paul Groth, Michel Dumontier, Jeff Heflin, Krishnaprasad Thirunarayan, Krishnaprasad Thirunarayan, and Steffen Staab (Eds.). Springer International Publishing, Cham, 3–18.

[28] Jochem Kuijpers, George Fletcher, Nikolay Yakovets, and Tobias Lindaaker. 2019. An Experimental Study of Context-Free Path Query Evaluation Methods. In *Proceedings of the 31st International Conference on Scientific and Statistical Database Management (SSDBM '19)*. ACM, New York, NY, USA, 121–132. https://doi.org/10.1145/3335783.3335791

[29] Jiajia Li, Jimeng Sun, and Richard Vuduc. 2018. HiCOO: Hierarchical Storage of Sparse Tensors. In *Proceedings of the International Conference for High Performance Computing, Networking, Storage, and Analysis (SC '18)*. IEEE Press, Article 19, 15 pages.

[30] Leonid Libkin, Wim Martens, and Domagoj Vrgoč. 2016. Querying Graphs with Data. *J. ACM* 63, 2, Article 14 (March 2016), 53 pages. https://doi.org/10.1145/2850413

[31] Ciro M. Medeiros, Martin A. Musicante, and Umberto S. Costa. 2018. Efficient Evaluation of Context-free Path Queries for Graph Databases. In *Proceedings of the 33rd Annual ACM Symposium on Applied Computing (SAC '18)*. ACM, New York, NY, USA, 1230–1237. https://doi.org/10.1145/3167132.3167265

[32] Saskia Metzler and Pauli Miettinen. 2015. On Defining SPARQL with Boolean Tensor Algebra. *CoRR* abs/1503.00301 (2015). arXiv:1503.00301 http://arxiv.org/abs/1503.00301

[33] Nikita Mishin, Iaroslav Sokolov, Egor Spirin, Vladimir Kutuev, Egor Nemchinov, Sergey Gorbatyuk, and Semyon Grigorev. 2019. Evaluation of the Context-Free Path Querying Algorithm Based on Matrix Multiplication. In *Proceedings of the 2Nd Joint International Workshop on Graph Data Management Experiences & Systems (GRADES) and Network Data Analytics (NDA) (GRADES-NDA'19)*. ACM, New York, NY, USA, Article 12, 5 pages. https://doi.org/10.1145/3327964.3328503

[34] Maurizio Nolé and Carlo Sartiani. 2016. Regular Path Queries on Massive Graphs. In *Proceedings of the 28th International Conference on Scientific and Statistical Database Management (SSDBM '16)*. Association for Computing Machinery, New York, NY, USA, Article 13, 12 pages. https://doi.org/10.1145/2949689.2949711

[35] Andreas Pavlogiannis and Anders Alnor Mathiasen. 2020. The Fine-Grained and Parallel Complexity of Andersen's Pointer Analysis. arXiv:cs.PL/2006.01491

[36] Fred C. Santos, Umberto S. Costa, and Martin A. Musicante. 2018. A Bottom-Up Algorithm for Answering Context-Free Path Queries in Graph Databases. In *Web Engineering*, Tommi Mikkonen, Ralf Klamma, and Juan Hernández (Eds.). Springer International Publishing, Cham, 225–233.

[37] TAISUKE SATO. 2017. A linear algebraic approach to datalog evaluation. *Theory and Practice of Logic Programming* 17, 3 (2017), 244âĂŞ265. https://doi.org/10.1017/S1471068417000023

[38] Bin Shao, Haixun Wang, and Yatao Li. 2013. Trinity: A Distributed Graph Engine on a Memory Cloud. In *Proceedings of the 2013 ACM SIGMOD International Conference on Management of Data (SIGMOD '13)*. Association for Computing Machinery, New York, NY, USA, 505–516. https://doi.org/10.1145/2463676.2467799

[39] Sairam Subramanian. 1993. A fully dynamic data structure for reachability in planar digraphs. In *Algorithms—ESA '93*, Thomas Lengauer (Ed.). Springer Berlin Heidelberg, Berlin, Heidelberg, 372–383.

[40] Arseniy Terekhov, Artyom Khoroshev, Rustam Azimov, and Semyon Grigorev. 2020. Context-Free Path Querying with Single-Path Semantics by Matrix Multiplication. In *Proceedings of the 3rd Joint International Workshop on Graph Data Management Experiences & Systems (GRADES) and Network Data Analytics (NDA) (GRADES-NDA'20)*. Association for Computing Machinery, New York, NY, USA, Article 5, 12 pages. https://doi.org/10.1145/3398682.3399163

[41] Leslie G. Valiant. 1975. General Context-Free Recognition in Less than Cubic Time. *J. Comput. Syst. Sci.* 10, 2 (April 1975), 308–315. https://doi.org/10.1016/S0022-0000(75)80046-8

[42] Oskar van Rest, Sungpack Hong, Jinha Kim, Xuming Meng, and Hassan Chafi. 2016. PGQL: A Property Graph Query Language. In *Proceedings of the Fourth International Workshop on Graph Data Management Experiences and Systems (GRADES '16)*. Association for Computing Machinery, New York, NY, USA, Article 7, 6 pages. https://doi.org/10.1145/2960414.2960421

[43] Ekaterina Verbitskaia, Semyon Grigorev, and Dmitry Avdyukhin. 2016. Relaxed Parsing of Regular Approximations of String-Embedded Languages. In *Perspectives of System Informatics*, Manuel Mazzara and Andrei Voronkov (Eds.). Springer International Publishing, Cham, 291–302.

[44] Xin Wang, Simiao Wang, Yueqi Xin, Yajun Yang, Jianxin Li, and Xiaofei Wang. 2019. Distributed Pregel-based provenance-aware regular path query processing on RDF knowledge graphs. *World Wide Web* 23, 3 (Nov. 2019), 1465–1496. https://doi.org/10.1007/s11280-019-00739-0

[45] Mihalis Yannakakis. 1990. Graph-theoretic Methods in Database Theory. In *Proceedings of the Ninth ACM SIGACT-SIGMOD-SIGART Symposium on Principles of Database Systems (PODS '90)*. ACM, New York, NY, USA, 230–242. https://doi.org/10.1145/298514.298576

[46] Qirun Zhang. 2020. Conditional Lower Bound for Inclusion-Based Points-to Analysis. arXiv:cs.PL/2007.05569