

PPoPP 2020

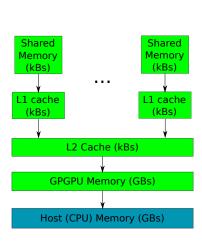


POSTER: Optimizing GPU Programs By Partial Evaluation

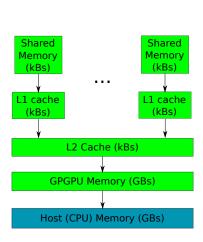
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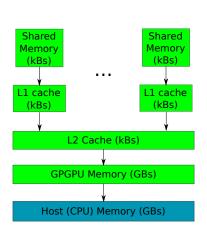
February 24, 2020



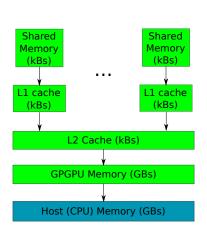
- Global memory
 - © Big



- Global memory
 - [©] Big
 - Slow
- Shared memory
 - Fast



- Global memory
 - © Big
 - Slow
- Shared memory
 - © Fast
 - Relatively small
 - Manual allocation mamagement
- Constant memory
 - © Fast



- Global memory
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 - Slow
- Shared memory
 - © Fast
 - Relatively small
 - Manual allocation mamagement
- Constant memory
 - © Fast
 - Only for appropriate access pattern
 - Small
 - Static allocation
- Memory traffic is a bottleneck

- Substring matching
- Filtering by using Hidden Markov Models (HMM)

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- Substring matching ⇒ Data curving (cyber forensics)
- Filtering by using Hidden Markov Models (HMM) ⇒ Homology search (bioinformatics)

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```
● Filtering by using Hidden Markov Models (HMN (bioinformatics)

One filter for many data chunks

→ many runs of procedure

-_global__ void estimateSimilarity

(int* filterParams, int* data, ...)

{
...
```

filterParams is a static during one data porcessing session.

Substring matching ⇒ Data curving (cyber forensics)

```
• Filtering by using Hidden Markov Models (HMN (bioinformatics)

One filter for many runs of procedure

__global__ void estimateSimilarity

(int* filterParams, int* data, ...)

{
...
}
```

filterParams is a static during one data porcessing session. How can we use this fact to optimize our procedure?

Partial Evaluation



[Scipy] Sparse matrices multiplication by using Scipy in Python

!!! Framework

[Scipy] Sparse matrices multiplication by using Scipy in Python[M4RI] Dense matrices multiplication by using m4ri library which implements the Method of Four Russians in C

Evaluation: Data Curving

[GPU4R] Our own implementation of the Method of Four Russians in CUDA C

Evaluation: Data Curving

- [GPU4R] Our own implementation of the Method of Four Russians in CUDA C
- [GPU_N] Our own implementation of the naïve boolean matrix multiplication in CUDA C

Evaluation: Data Curving

- [GPU4R] Our own implementation of the Method of Four Russians in CUDA C
- [GPU_N] Our own implementation of the naïve boolean matrix multiplication in CUDA C
- [GPU_Py] Our own implementation of naïve boolean matrix multiplication in Python by using numba compiler

Evaluation: !!!

[CuSprs]

- Rustam Azimov, 2018, "Context-free Path Querying by Matrix Multiplication"
- Implementation is based on NVIDIA cuSPARSE library (CUDA C, GPGPU)

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[CuSprs]

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[CYK]

- X. Zhang et al, 2016, "Context-free path queries on RDF graphs"
- CYK-based algorithm implemented in Java (CPU)

Limitations

[RDF]

- ► The set of the real-world RDF files (ontologies)
- Queries:

 $G_4: s \rightarrow SCOR \ s \ SCO \ | \ TR \ s \ T \ | \ SCOR \ SCO \ | \ TR \ T$ $G_5: s \rightarrow SCOR \ s \ SCO \ | \ SCO$

Limitations

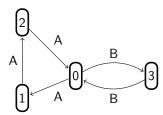
[RDF]

- ► The set of the real-world RDF files (ontologies)
- Queries:

 $G_4: s \rightarrow SCOR \ s \ SCO \ | \ TR \ s \ T \ | \ SCOR \ SCO \ | \ TR \ T \ G_5: s \rightarrow SCOR \ s \ SCO \ | \ SCO$

[Worst]

 The input graph is two cycles of coprime lengths with one shared vertex



• Query: $G_1: s \rightarrow A \ s \ B \mid A \ B$

Dataset

[Full]

- ▶ The input graph is sparse, but the result is a full graph
- Queries:

 $G_2: s \rightarrow s \ s \mid A$

 $G_3: s \rightarrow s \ s \ s \mid A$

Dataset

[Full]

- ▶ The input graph is sparse, but the result is a full graph
- Queries:

 $G_2: s \rightarrow s \ s \ | \ A$ $G_3: s \rightarrow s \ s \ | \ A$

[Sparse]

- Sparse graphs are generated by GTgraph
- ▶ Query: $G_1: s \rightarrow A \ s \ B \mid A \ B$

Conclusion

OS: Ubuntu 18.04

CPU: Intel core i7 8700k 3,7GHz

RAM: DDR4 32 Gb

• GPGPU: NVIDIA GeForce 1080Ti (11Gb RAM)

Future Research

- Swithc to CUDA C partial evaluator
- Reduce specialization overhead
- Integrete with shared memory register spilling

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- Daniil Berezun: daniil.berezun@jetbrains.com
- Dataset and algorithm implementations: https://github.com/SokolovYaroslav/CFPQ-on-GPGPU

Thanks!