

E2

0.05

0.10 C A B

C NOTE 3

48X **b** 

**BOTTOM VIEW** 

e

e/2

**DATE 27 APR 2007** 

- NOTES:
  1. DIMENSIONS AND TOLERANCING PER ASME
  - Y14.5M, 1994.
    CONTROLLING DIMENSION: MILLIMETERS.
- DIMENSION & APPLIES TO THE PLATED TERMINAL AND IS MEASURED ABETWEEN
- 0.15 AND 0.30 MM FROM TERMINAL TIP.
   COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

|     | MILLIMETERS |      |  |
|-----|-------------|------|--|
| DIM | MIN         | MAX  |  |
| Α   | 0.80        | 1.00 |  |
| A1  | 0.00        | 0.05 |  |
| A3  | 0.20 REF    |      |  |
| b   | 0.20        | 0.30 |  |
| D   | 7.00 BSC    |      |  |
| D2  | 5.00        | 5.20 |  |
| E   | 7.00 BSC    |      |  |
| E2  | 5.00        | 5.20 |  |
| е   | 0.50 BSC    |      |  |
| K   | 0.20        |      |  |
| L   | 0.30        | 0.50 |  |

## **GENERIC MARKING DIAGRAM\***



= Assembly Location

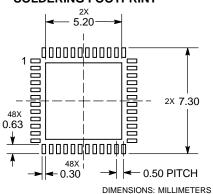
WL = Wafer Lot

YY = Year

WW = Work Week

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

## SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

| DOCUMENT NUMBER: | 98AON24490D               | Electronic versions are uncontrolled except when  |   |
|------------------|---------------------------|---|---|
| STATUS:          | ON SEMICONDUCTOR STANDARD | accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |   |
| NEW STANDARD:    |                           |   |   |
| DESCRIPTION:     | QFN48 7X7, 0.50P          | PAGE 1 OF 2   | 2 |



| <b>DOCUMENT</b> | NUMBER: |
|-----------------|---------|
| 98AON24490      | D       |

PAGE 2 OF 2

| ISSUE | REVISION                                   | DATE        |
|-------|--|-------------|
| 0     | RELEASED FOR PRODUCTION. REQ. S. MOHAMMED. | 27 APR 2007 |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |
|       |  |             |

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, its patent rights nor the rights of others. SCILLC products are not designed, intended, or aumorized for use as components in systems intended to support or sustain life, or for any other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

© Semiconductor Components Industries, LLC, 2007 Case Outline Number: April, 2007 - Rev. 010 485AJ